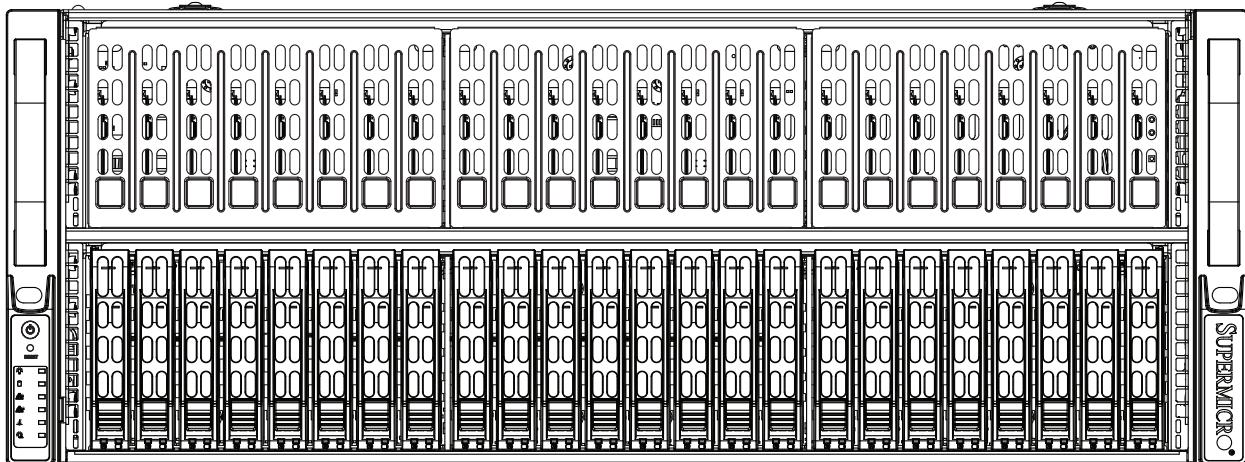




SuperServer®
4029GP-TRT
4029GP-TRT2
4029GP-TRT3



USER'S MANUAL

Revision 1.0b

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Manual Revision 1.0b

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Preface

About this Manual

This manual is written for professional system integrators and PC technicians. It provides information for the installation and use of the SuperServer 4029GP-TRT/TRT2/TRT3. Installation and maintenance should be performed by experienced technicians only.

Please refer to the 4029GP-TRT/TRT2/TRT3 server specifications page on our website for updates on supported memory, processors and operating systems (<http://www.supermicro.com>).

Notes

For your system to work properly, please follow the links below to download all necessary drivers/utilities and the user's manual for your server.

- Supermicro product manuals: <http://www.supermicro.com/support/manuals/>
- Product drivers and utilities: <https://www.supermicro.com/wftp/driver>
- Product safety info: http://www.supermicro.com/about/policies/safety_information.cfm

If you have any questions, please contact our support team at:
support@supermicro.com

This manual may be periodically updated without notice. Please check the Supermicro website for possible updates to the manual revision level.

Warnings

Special attention should be given to the following symbols used in this manual.



Warning! Indicates important information given to prevent equipment/property damage or personal injury.



Warning! Indicates high voltage may be encountered when performing a procedure.

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Chapter 1

Introduction

1.1 Overview

This chapter provides a brief outline of the functions and features of the 4029GP-TRT/TRT2/TRT3. The 4029GP-TRT/TRT2/TRT3 is based on the X11DPG-O motherboard and the SC418GTS-R4000BP2 chassis. The only difference between the models is what daughterboard is used for each system.

In addition to the motherboard and chassis, several important parts that are included with the system are listed below.

Main Parts List		
Description	Part Number	Quantity
2U Active CPU Heat Sink with Narrow Bolster Plate Mounting Mechanism	SNK-P0068APS4	2
Black 3rd generation hot-swap 2.5" HDD trays	MCP-220-00047-0B	24
2U hybrid backplane for 20xSAS3/SATA3 and 4xSAS3/SATA3/NVMe	BPN-SAS3-216A-N4	1
PCI-E Daughterboard for 4029GP-TRT2 system only	MBD-X10DRG-O-PCIE-P	1
PCI-E Daughterboard for 4029GP-TRT system only	MBD-X9DRG-O-PCIE-P	1
PCI-E Daughterboard for 4029GP-TRT3 system only	MBD-X11DPG-O-PCIE-P	1
Riser cards	RSC-X9DRG-O	4
1U 2000W Redundant Titanium Power Supplies	PWS-2K05A-1R	4
92 x 92 x 38-mm, 11.5K RPM, middle fans with housing	FAN-0151L	8
Rail set for 4U 17.2" Width	MCP-290-00057-0N	1

1.2 Unpacking the System

Inspect the box the SuperServer 4029GP-TRT/TRT2/TRT3 was shipped in and note if it was damaged in any way. If any equipment appears damaged, please file a damage claim with the carrier who delivered it.

Decide on a suitable location for the rack unit that will hold the server. It should be situated in a clean, dust-free area that is well ventilated. Avoid areas where heat, electrical noise and electromagnetic fields are generated. It will also require a grounded AC power outlet nearby. Be sure to read the precautions and considerations noted in Appendix B.

1.3 System Features

The following table provides you with an overview of the main features of the 4029GP-TRT/TRT2/TRT3. Please refer to Appendix C for additional specifications.

System Features	
Motherboard	
X11DPG-O	
Chassis	
SC418GTS-R4000BP2	
CPU	
Dual Intel® 81xx/61xx/51xx/41xx/31xx or 82xx/62xx/52xx/42xx/32xx series processors	
Socket Type	
Socket P0-LGA3647	
Memory	
Up to 6 TB of 3DS Load Reduced DIMM (3DS LRDIMM)/Load Reduced DIMM (LRDIMM)/3DS Registered DIMM (3DS RDIMM)/Registered DIMM (RDIMM) DDR4 (288-pin) ECC 2933/2666/2400/2133 MHz speed and up to 128 GB size at 1.2V in 24 slots.	
Chipset	
Intel PCH C622 chipset	
Expansion Slots	
Up to 12 PCI-E 3.0 slots	
Hard Drives	
Up to 24 2.5" drives (8 are connected by default)	
Power	
Four 2000W power supplies	
Other	
ASpeed AST 2500 BMC	
Form Factor	
4U Rack mount server	
Dimensions	
(WxHxD) 17.2 x 1.7 x 23.5 in. (437 x 43 x 597 mm)	

1.4 Server Chassis Features

Control Panel

There are two buttons located on the front of the chassis: a power on/off button and a reset button. In addition there are six LEDs. The locations of these buttons and LEDs on the control panel are described below. See Chapter 4 for details on the control panel connections.

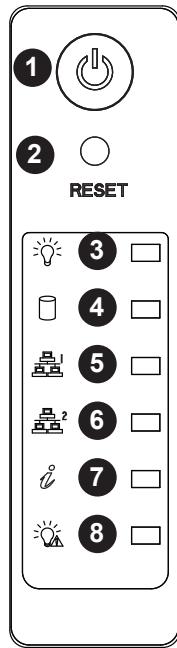


Figure 1-1. Control Panel View

Control Panel Features		
Item	Feature	Description
1	Power Button	The main power switch applies or removes primary power from the power supply to the server but maintains standby power. To perform most maintenance tasks, unplug the system to remove all power.
2	Reset Button	The reset button is used to reboot the system.
3	Power LED	Indicates power is being supplied to the system power supply units. This LED is illuminated when the system is operating normally.
4	HDD LED	Indicates IDE channel activity. SAS2/SATA drive and/or DVD-ROM drive activity when flashing.
5	NIC1 LED	Indicates network activity on GLAN2 when flashing.
6	NIC2 LED	Indicates network activity on GLAN1 when flashing.
7	Universal Information LED	See the following table for the status shown by this LED.
8	Power Fail LED	Indicates a power supply module has failed.

Universal Information LED	
Status	Description
Continuously on and red	An overheat condition has occurred (this may be caused by cable congestion).
Blinking red (1 Hz)	Fan failure: check for an inoperative fan.
Blinking red (0.25 Hz)	Power failure: check for an inoperative power supply.
Solid blue	Local UID has been activated. Use this function to locate the server in a rack environment.
Blinking blue (300 msec)	Remote UID has been activated. Use this function to activate the server from a remote location.

Front Features

The SC418GTS-R4000BP2 is a 4U chassis. See the illustration below for the features included on the front of the chassis.

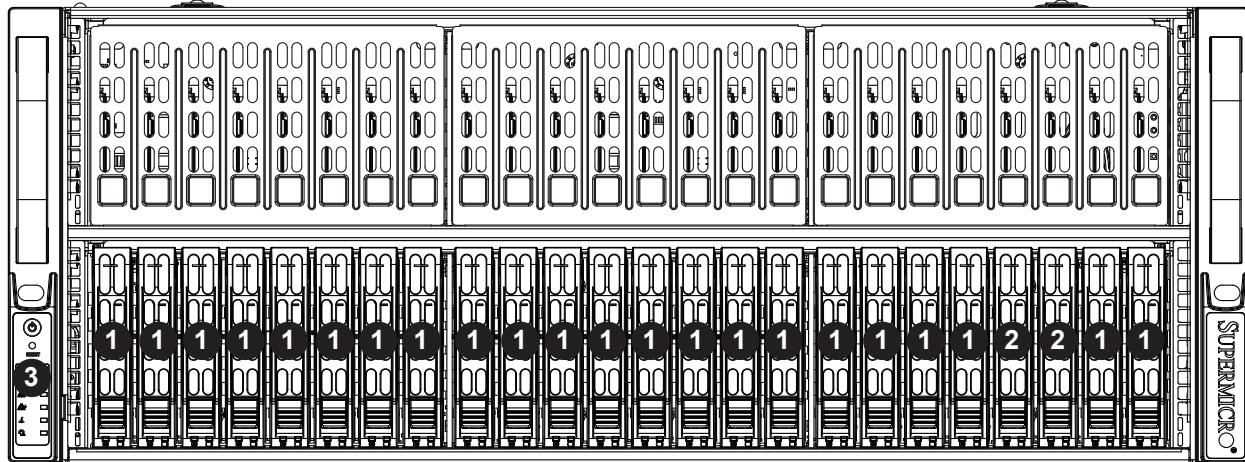


Figure 1-2. Chassis Front View

Front Chassis Features		
Item	Feature	Description
1	Hot-swap drive bays	Drive bays for hot-swap 2.5" drive carriers for SATA/SAS drives
2	Hybrid hot-swap drive bays	These two HDD slots are hybrid and can support SATA/SAS/NVMe drives
3	Control Panel	Control panel for the server. See the Control Panel section above for details.

Rear Features

The illustration below shows the features included on the rear of the chassis.

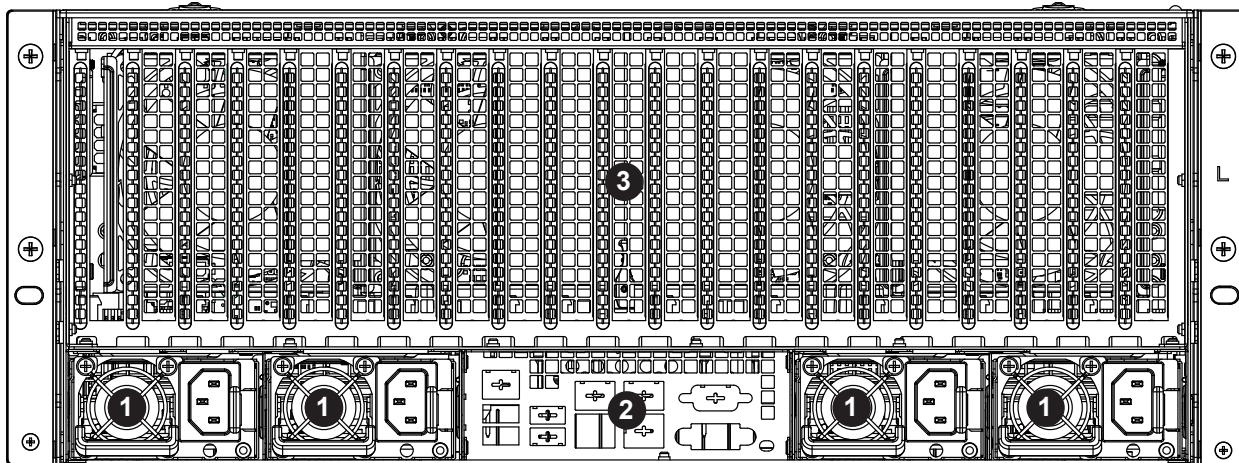


Figure 1-3. Chassis Rear View

Rear Chassis Features		
Item	Feature	Description
1	Power Supplies	Two hot-swappable redundant power supplies are available for use.
2	Rear I/O ports	See Motherboard Layout below for details on the rear I/O ports.
3	Rear PCI-E Expansion Slots	These slots are provided in the chassis rear for accessing PCI-E Expansion cards using four riser cards. The 4029GP-TRT system has up to eleven (11) slots, while the 4029GP-TRT2 system has up to twelve (12) slots.

1.5 Motherboard Layout

Below is a layout of the X11DPG-O with jumper, connector and LED locations shown. See the table on the following page for descriptions. For detailed descriptions, pinout information and jumper settings, refer to Chapter 4.

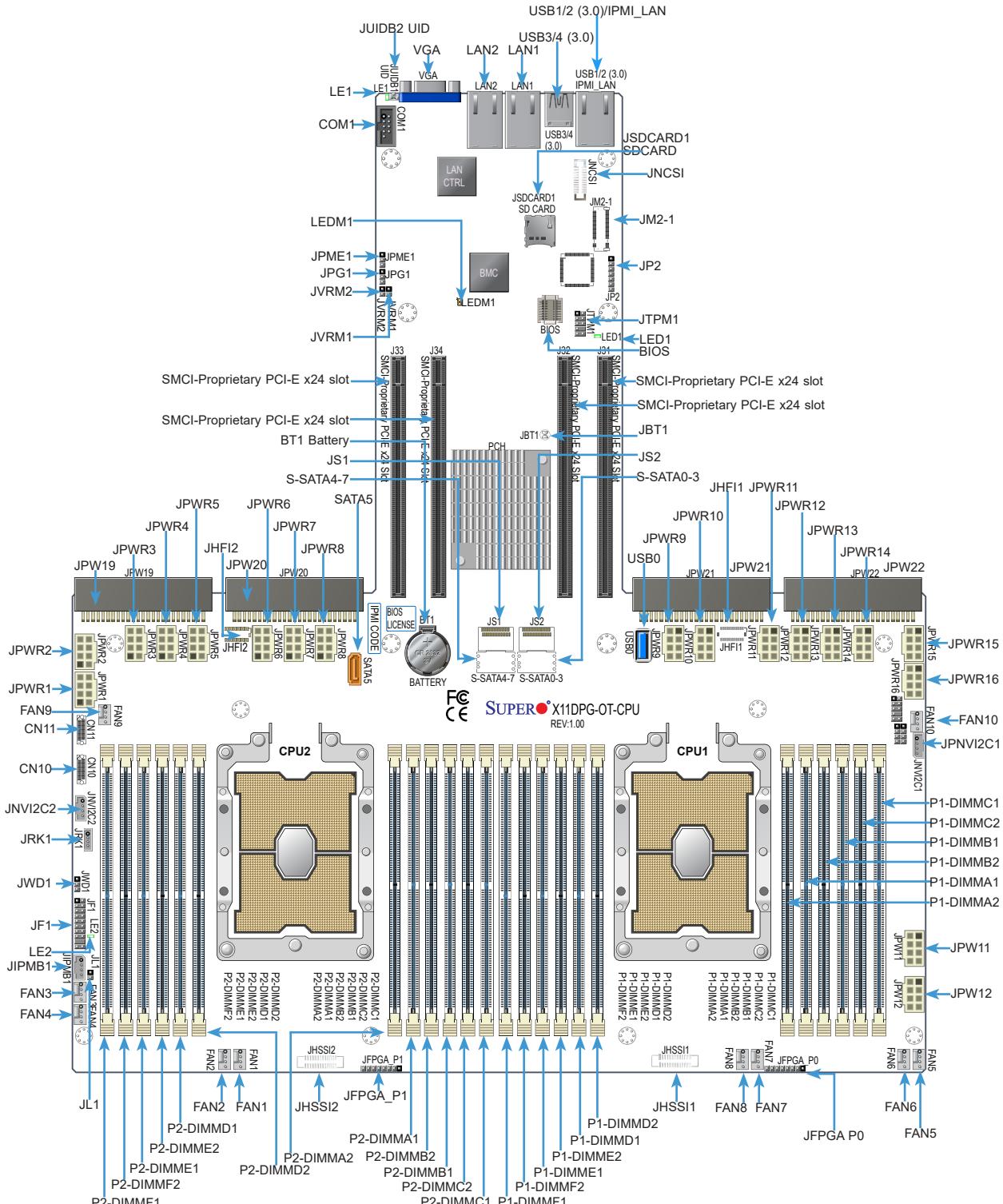


Figure 1-4. Motherboard Layout

Quick Reference Table

Jumper	Description	Default Setting
GBT1	CMOS Clear	Open (Normal) (See Chpt. 4)
JPG1	VGA Enable	Pins 1-2 (Enable)
JPME1	Manufacturing Mode Select	Pins 1-2 (Normal)
JVRM1	VRM SMBus Clock (to BMC or PCH)	Pins 1-2 (BMC, Normal)
JVRM2	VRM SMBus Data (to BMC or PCH)	Pins 1-2 (BMC, Normal)
JWD1	Watch Dog Timer Enable	Pins 1-2 (Reset to System)

LED	Description	Status
LE1	UID (Unit Identifier) LED	Solid Blue: Unit identified
LE2	Power LED	On: Onboard power on
LED 1	M.2 Power LED	On: M.2 power on
LEDM1	BMC Heartbeat LED	Blinking Green: BMC normal

Connector	Description
BT1	Onboard CMOS battery
CN10/CN11	x4 Oculink Connectors connected to CPU2 for NVMe drive for HSSI add-on card use
COM1	COM port (COM1) for front access
FAN1-10	System cooling fan headers (Fan1-Fan 8) & active CPU heatsink fan headers (Fan9/Fan10)
IPMI_LAN	Dedicated IPMI LAN port
LAN1/LAN2	10GbE LAN Ethernet ports 1/2 on the I/O back panel
JF1	Front panel control header
JHFI1/JHFI2	Host Fabric Interface (HFI) sideband headers used for the HFI carrier cards (JHFI1 is used when an F model CPU is installed in CPU Socket 1; JHFI2 is used when an F-model CPU is installed in CPU Socket 2.) (See Notes below.)
JHSSI1/2	Intel 81xx/61xx/51xx/41xx/31xx CPU-FPGA sideband headers (1/2) for CPU-FPGA add-on cards
JIPMB1	4-pin System Management Bus (SMBus) I ² C header (for an IPMI-supported card)
JL1	Chassis Intrusion header
JM2-1	PCI-E M.2 slot
JNCS1	Add-on card header used for the AOC that supports NCSI (Network Communication Services Interface)
JNVI ² C1/ JNVI ² C2	NVMe SMBus (I ² C) headers used for PCI-E hot-plug SMBus clock & data connections (an SMCI-proprietary NVMe add-on card and cable are required for each NVMe SMBus header. This feature is available for a Supermicro complete system only.)
JPW19/20, JPW 21/22	SMCI-proprietary power supply units 19/20, 21/22 for system use
JPWR1-8, JPWR9-16	8-pin GPU power supply connectors 1-8, 9-16
JRK1	Intel RAID Key header for NVMe SDD
JSDCARD1	BMC_SD card header
JTPM1	Trusted Platform Module/Port 80 connector
JUIDB1	UID (Unit Identifier) switch

Note: Table is continued on the next page.

Connector	Description
SATA5	SATA3.0 port with power-pin built-in w/support of SuperDOM (Device-On Module)
S-SATA 0-3, 4-7	SATA3.0 connections supported by Intel SCU
USB0	Type A internal USB 2.0 header (USB Port 0)
USB1/2, 3/4	Backplane Universal Serial Bus (USB) 3.0 ports 1/2, 3/4
VGA	VGA Port

Notes: For the HFI sideband carrier card to function properly, please install the HFI card to an appropriate PCI-E slot of your choice, and install an F model processor in the corresponding CPU socket (JHFI1: for CPU1, JHFI2: for CPU2). 2. Connect an HFI cable from the HFI card to JHFI (HFI header) and connect an IFP cable from HFI card to the processor. (See Chapter 3 for more info.)

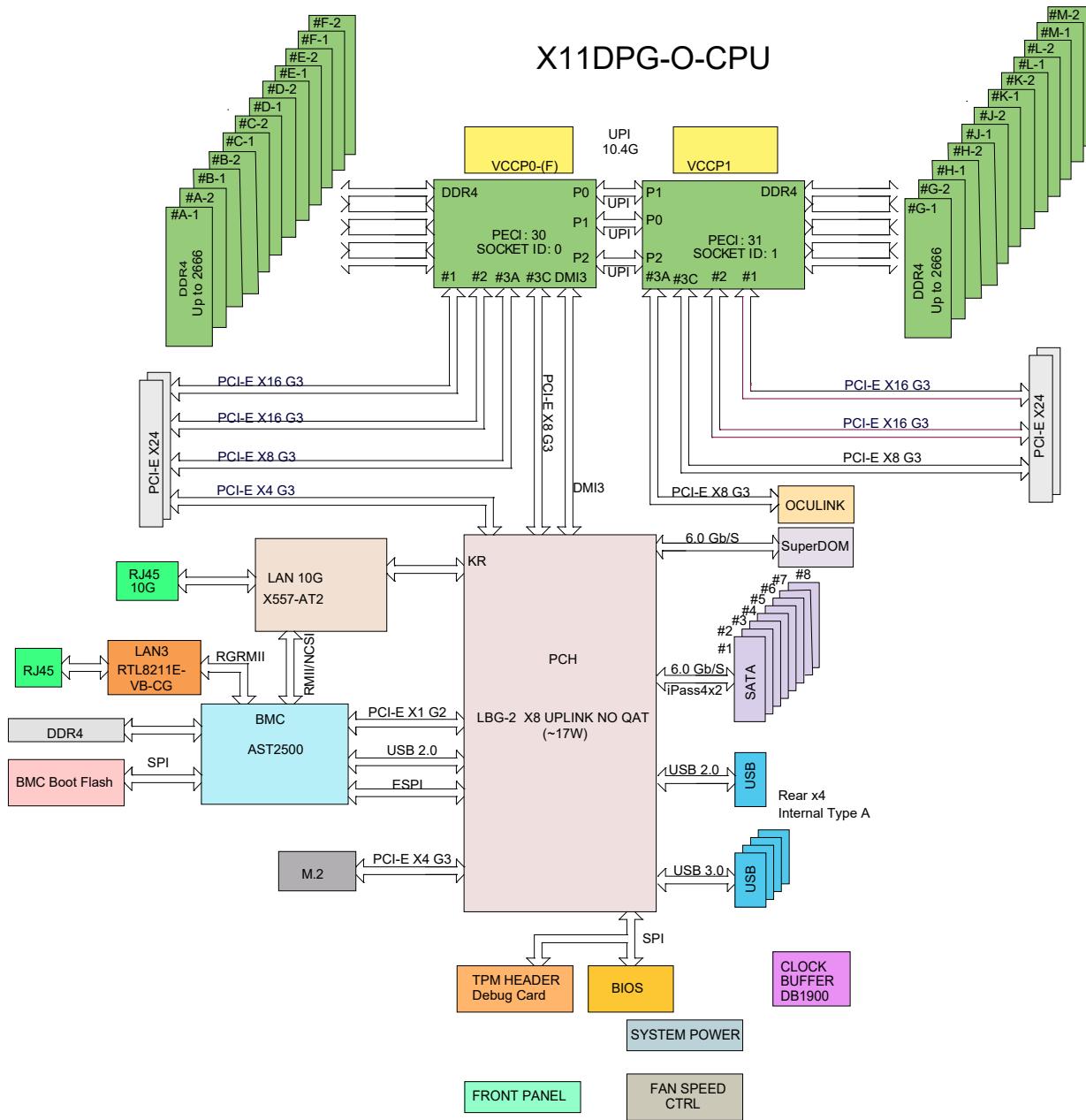


Figure 1-5. System Block Diagram for X11DPG-O-CPU

Note: This is a general block diagram and may not exactly represent the features on your motherboard. See the System Specifications appendix for the actual specifications of your motherboard.

Chapter 2

Server Installation

2.1 Overview

This chapter provides advice and instructions for mounting your system in a server rack. If your system is not already fully integrated with processors, system memory etc., refer to Chapter 4 for details on installing those specific components.

Caution: Electrostatic Discharge (ESD) can damage electronic components. To prevent such damage to PCBs (printed circuit boards), it is important to use a grounded wrist strap, handle all PCBs by their edges and keep them in anti-static bags when not in use.

2.2 Preparing for Setup

The box in which the system was shipped should include the rackmount hardware needed to install it into the rack. Please read this section in its entirety before you begin the installation.

Choosing a Setup Location

- The system should be situated in a clean, dust-free area that is well ventilated. Avoid areas where heat, electrical noise and electromagnetic fields are generated.
- Leave enough clearance in front of the rack so that you can open the front door completely (~25 inches) and approximately 30 inches of clearance in the back of the rack to allow sufficient space for airflow and access when servicing.
- This product should be installed only in a Restricted Access Location (dedicated equipment rooms, service closets, etc.).
- This product is not suitable for use with visual display workplace devices according to §2 of the German Ordinance for Work with Visual Display Units.

Rack Precautions

- Ensure that the leveling jacks on the bottom of the rack are extended to the floor so that the full weight of the rack rests on them.

- In single rack installations, stabilizers should be attached to the rack. In multiple rack installations, the racks should be coupled together.
- Always make sure the rack is stable before extending a server or other component from the rack.
- You should extend only one server or component at a time - extending two or more simultaneously may cause the rack to become unstable.

Server Precautions

- Review the electrical and general safety precautions in Appendix B.
- Determine the placement of each component in the rack *before* you install the rails.
- Install the heaviest server components at the bottom of the rack first and then work your way up.
- Use a regulating uninterruptible power supply (UPS) to protect the server from power surges and voltage spikes and to keep your system operating in case of a power failure.
- Allow any drives and power supply modules to cool before touching them.
- When not servicing, always keep the front door of the rack and all covers/panels on the servers closed to maintain proper cooling.

Rack Mounting Considerations

Ambient Operating Temperature

If installed in a closed or multi-unit rack assembly, the ambient operating temperature of the rack environment may be greater than the room's ambient temperature. Therefore, consideration should be given to installing the equipment in an environment compatible with the manufacturer's maximum rated ambient temperature (TMRA).

Airflow

Equipment should be mounted into a rack so that the amount of airflow required for safe operation is not compromised.

Mechanical Loading

Equipment should be mounted into a rack so that a hazardous condition does not arise due to uneven mechanical loading.

Circuit Overloading

Consideration should be given to the connection of the equipment to the power supply circuitry and the effect that any possible overloading of circuits might have on overcurrent protection and power supply wiring. Appropriate consideration of equipment nameplate ratings should be used when addressing this concern.

Reliable Ground

A reliable ground must be maintained at all times. To ensure this, the rack itself should be grounded. Particular attention should be given to power supply connections other than the direct connections to the branch circuit (i.e. the use of power strips, etc.).



To prevent bodily injury when mounting or servicing this unit in a rack, you must take special precautions to ensure that the system remains stable. The following guidelines are provided to ensure your safety:

- This unit should be mounted at the bottom of the rack if it is the only unit in the rack.
- When mounting this unit in a partially filled rack, load the rack from the bottom to the top with the heaviest component at the bottom of the rack.
- If the rack is provided with stabilizing devices, install the stabilizers before mounting or servicing the unit in the rack.

2.3 Procedure for Rack Mounting

This section provides information on installing a 4U chassis into a rack unit with the rails provided. There are a variety of rack units on the market, so the assembly procedure may differ slightly. Also refer to the installation instructions for your rack unit.

Note: This rail will fit a rack between 26.5" and 36.4" deep.

Identifying the Inner Rack Rails

The chassis package includes one pair of rack rail assemblies in the rack mounting kit. Each assembly consists of an inner rail that secures to the chassis and an outer rail that is attached directly to the rack. The inner rails are etched with "L" (Left side) and "R" (Right side).

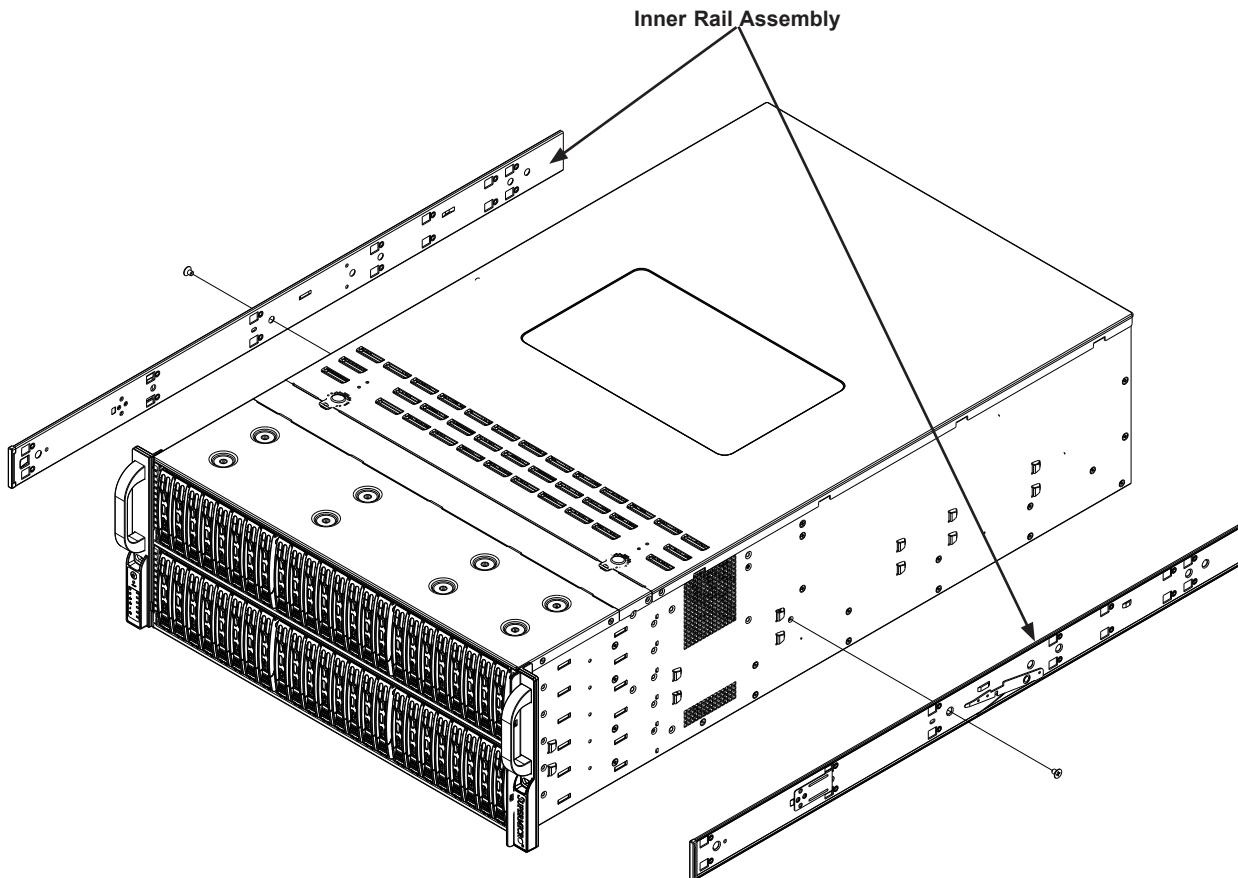


Figure 2-1. Identifying the Rack Rails

Note: The figure above is for illustrative purposes only. Always install servers at the bottom of the rack first.



Warning: do not pick up the server with the front handles. They are designed to pull the system from a rack only.

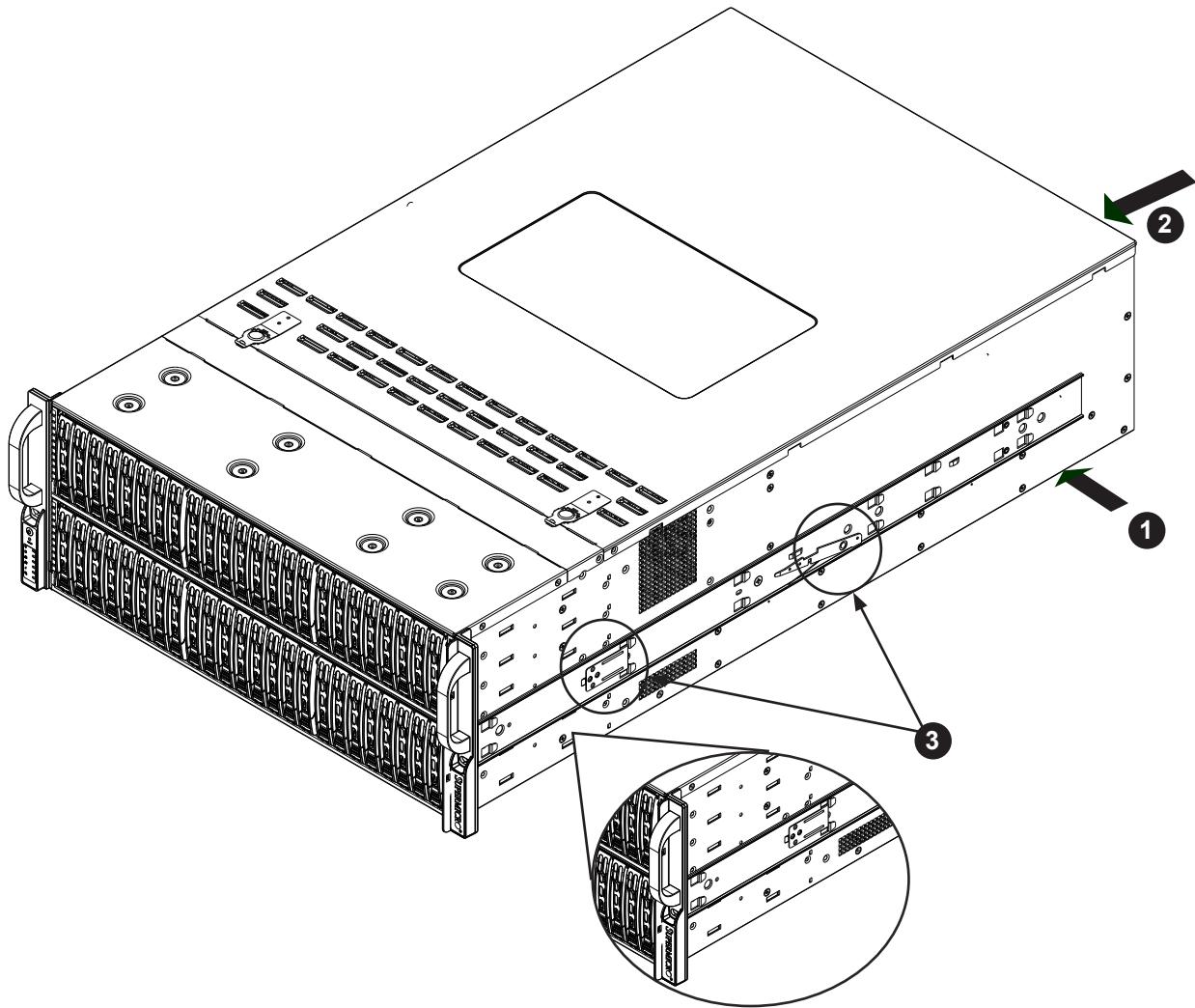


Figure 2-2. Installing the Inner Rails

Note: The figure above is for illustrative purposes only. Always install servers at the bottom of the rack first.

Installing the Inner Rails on the Chassis

Installing the Inner Rails

1. Identify the left and right side inner rails. Place the correct inner rail on the side of the chassis, aligning the hooks of the chassis with the inner rail holes. Make sure the rail faces "outward" so that it will fit with the rack's mounting bracket.
2. Slide the rail toward the front of the chassis to hook the inner rail onto the side of the chassis.
3. If desired, secure the rail with two flat head M4 x 4mm screws as illustrated.
4. Repeat for the other inner rail.



Warning: Stability hazard. The rack stabilizing mechanism must be in place, or the rack must be bolted to the floor before you slide the unit out for servicing. Failure to stabilize the rack can cause the rack to tip over.

Installing the Outer Rails onto the Rack

Installing the Outer Rails

1. Press upward on the locking tab at the rear end of the middle rail.
2. Push the middle rail back into the outer rail.
3. Hang the hooks on the front of the outer rail onto the square holes on the front of the rack. If desired, use screws to secure the outer rails to the rack.
4. Pull out the rear of the outer rail, adjusting the length until it just fits within the posts of the rack.
5. Hang the hooks of the rear section of the outer rail onto the square holes on the rear of the rack. Take care that the proper holes are used so the rails are level. If desired, use screws to secure the rear of the outer rail to the rear of the rack.
6. Repeat for the other outer rail.

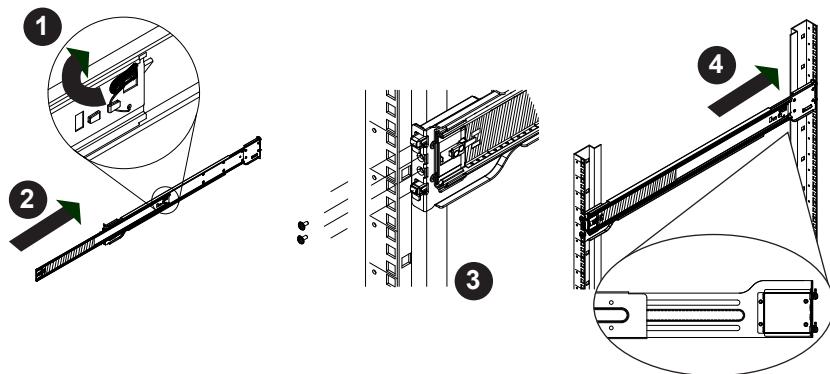


Figure 2-3. Extending and Mounting the Outer Rails

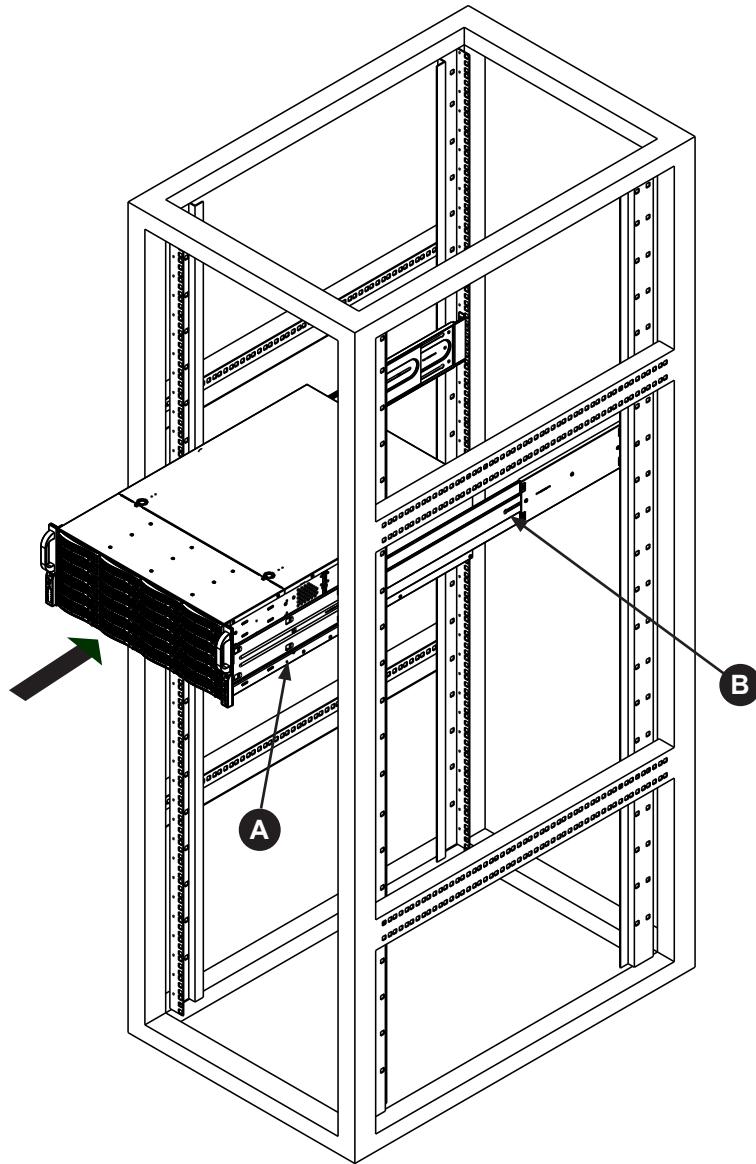


Figure 2-4. Installing the Chassis into a Rack

Note: Figures are for illustrative purposes only. Always install servers into racks in the lower positions first.

Installing the Chassis into a Rack

Installing the Chassis into a Rack:

1. Align the chassis rails (A) with the front of the rack rails (B).
2. Slide the chassis rails into the rack rails, keeping the pressure even on both sides. You may have to depress the locking tabs while inserting. When the server has been pushed completely into the rack, the locking tabs should "click" into the locked position.
3. If screws are used, tighten the screws on the front and rear of the outer rails.
4. (Optional) Insert and tightening the thumbscrews that hold the front of the server to the rack.

Removing the Chassis from the Rack

Caution! It is dangerous for a single person to off-load the heavy chassis from the rack without assistance. Be sure to have sufficient assistance supporting the chassis when removing it from the rack. Use a lift.

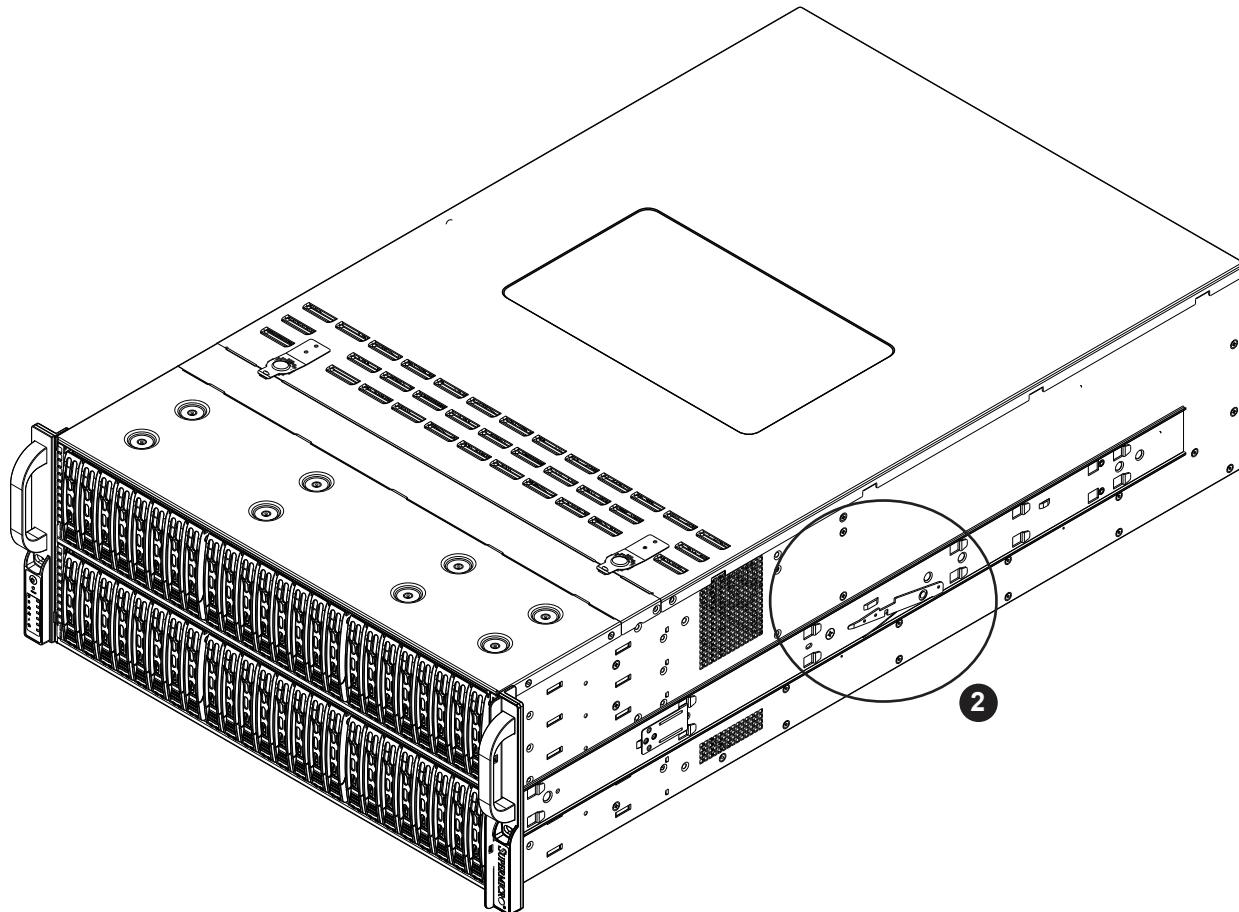


Figure 2-5. Removing the Chassis From the Rack

Removing the Chassis from the Rack

1. Pull the chassis forward out the front of the rack until it stops.
2. Press the release latches on each of the inner rails downward simultaneously and move the chassis forward in the rack.

Note: The figure above is for illustrative purposes only. Always install servers at the bottom of the rack first.

Chapter 3

Maintenance and Component Installation

This chapter provides instructions on installing and replacing main system components. To prevent compatibility issues, only use components that match the specifications and/or part numbers given.

Installation or replacement of most components require that power first be removed from the system. Please follow the procedures given in each section.

3.1 Removing Power

Use the following procedure to ensure that power has been removed from the system. This step is necessary when removing or installing non hot-swap components or when replacing a non-redundant power supply.

1. Use the operating system to power down the system.
2. After the system has completely shut-down, disconnect the AC power cord(s) from the power strip or outlet. (If your system has more than one power supply, remove the AC power cords from all power supply modules.)
3. Disconnect the power cord(s) from the power supply module(s).

3.2 Accessing the System

The SC418GTS-R4000BP2 features a removable top cover, which allows easy access to the inside of the chassis.

Removing the Top Cover

1. Begin by removing power from the system as described in Section 3.1.
2. Remove the screws securing the cover to the chassis.
3. Slide the cover toward the rear of the chassis. See Figure 3-1.
4. Lift the cover from the chassis.

Warning: Except for short periods of time, do not operate the server without the cover in place. The chassis cover must be in place to allow for proper airflow and to prevent overheating.

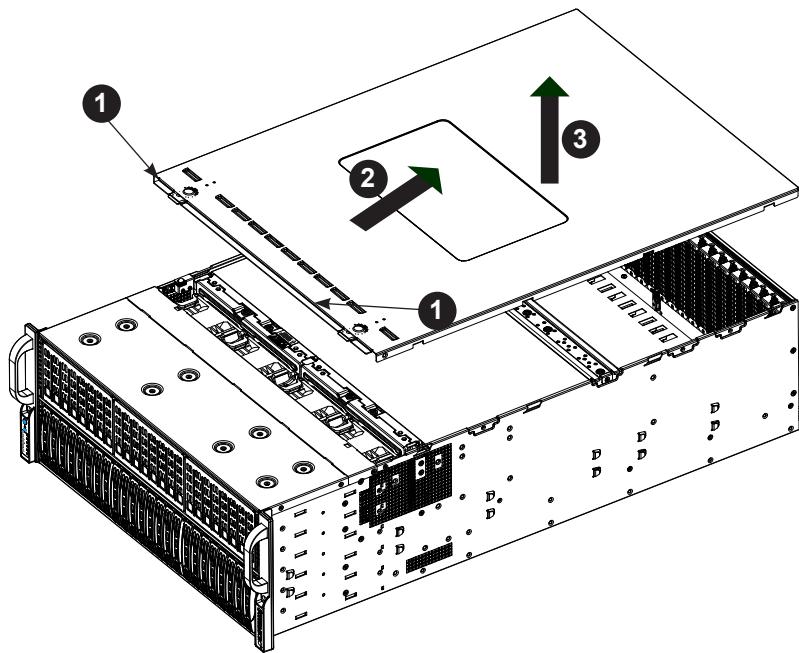


Figure 3-1. Accessing the Inside of the System

Caution: Except for short periods of time, do not operate the server without the cover in place. The chassis cover must be in place to allow proper airflow and prevent overheating.

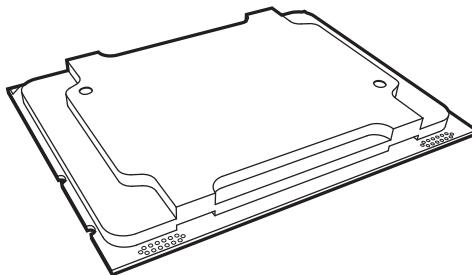
3.3 Processor and Heatsink Installation

The processor (CPU) and processor carrier should be assembled together first to form the processor carrier assembly. This will be attached to the heatsink to form the processor heatsink module (PHM) before being installed onto the CPU socket.

Notes:

- Use ESD protection.
- Unplug the AC power cord from all power supplies after shutting down the system.
- Check that the plastic protective cover is on the CPU socket and none of the socket pins are bent. If they are, contact your retailer.
- When handling the processor, avoid touching or placing direct pressure on the LGA lands (gold contacts). Improper installation or socket misalignment can cause serious damage to the processor or CPU socket, which may require manufacturer repairs.
- Thermal grease is pre-applied on a new heatsink. No additional thermal grease is needed.
- Refer to the Supermicro website for updates on processor support.
- All graphics in this manual are for illustrations only. Your components may look different.

The Intel® Xeon® Scalable Series Processor

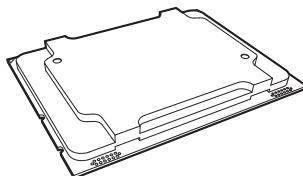


Non-Fabric Model

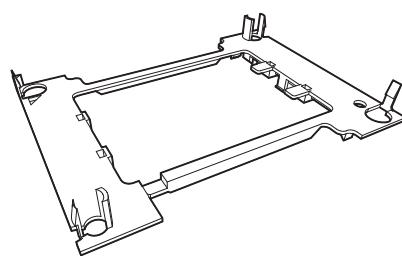
Overview of the Processor Carrier Assembly

The processor carrier assembly contains the Intel Xeon Non-Fabric (Non-F) processor and a processor carrier.

1. Non-F Processor



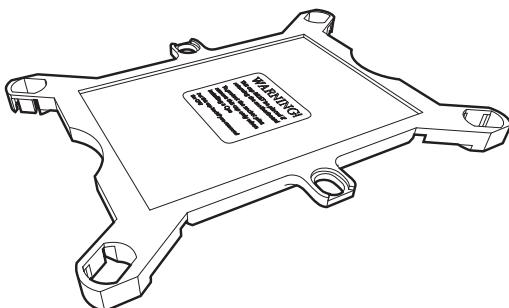
2. Processor Carrier



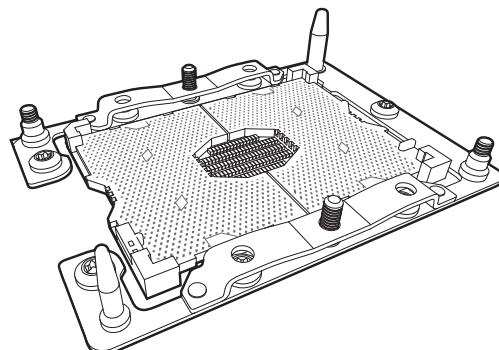
Overview of the CPU Socket

The CPU socket is protected by a plastic protective cover.

1. Plastic Protective Cover



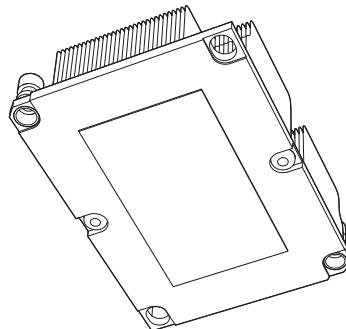
2. CPU Socket



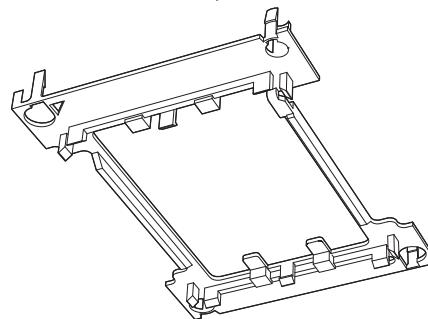
Overview of the Processor Heatsink Module

The Processor Heatsink Module (PHM) contains a heatsink, a processor carrier, and the Intel Xeon Non-Fabric (Non-F) processor.

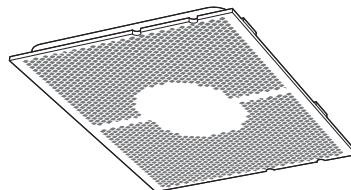
1. Heatsink with Thermal Grease



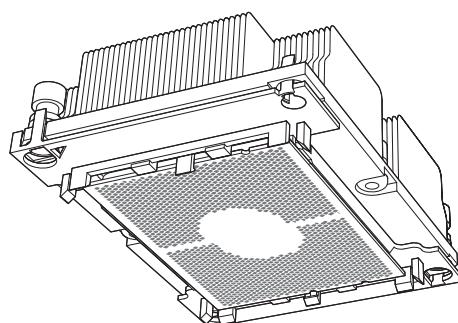
2. Processor Carrier



3. Non-F Processor



Processor Heatsink Module

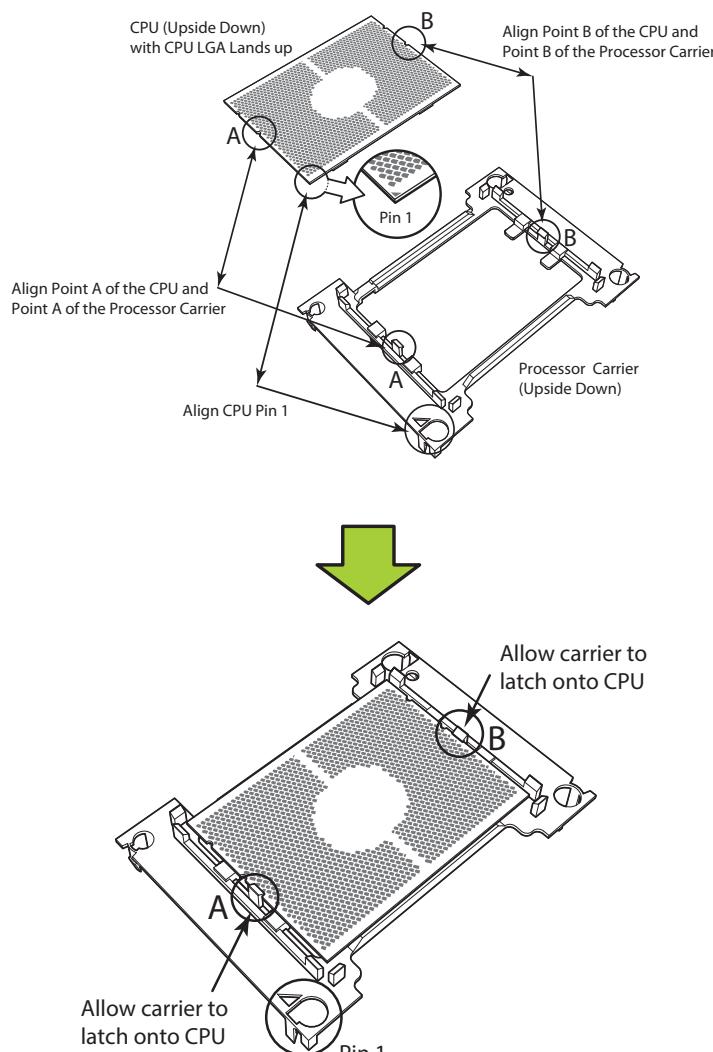


Bottom View

Creating the Non-F Model Processor Carrier Assembly

To install a Non-F model processor into the processor carrier, follow the steps below:

1. Hold the processor with the LGA lands (gold contacts) facing up. Locate the small, gold triangle in the corner of the processor and the corresponding hollowed triangle on the processor carrier. These triangles indicate pin 1. See the images below.
2. Using the triangles as a guide, carefully align and place Point A of the processor into Point A of the carrier. Then gently flex the other side of the carrier for the processor to fit into Point B.
3. Examine all corners to ensure that the processor is firmly attached to the carrier.

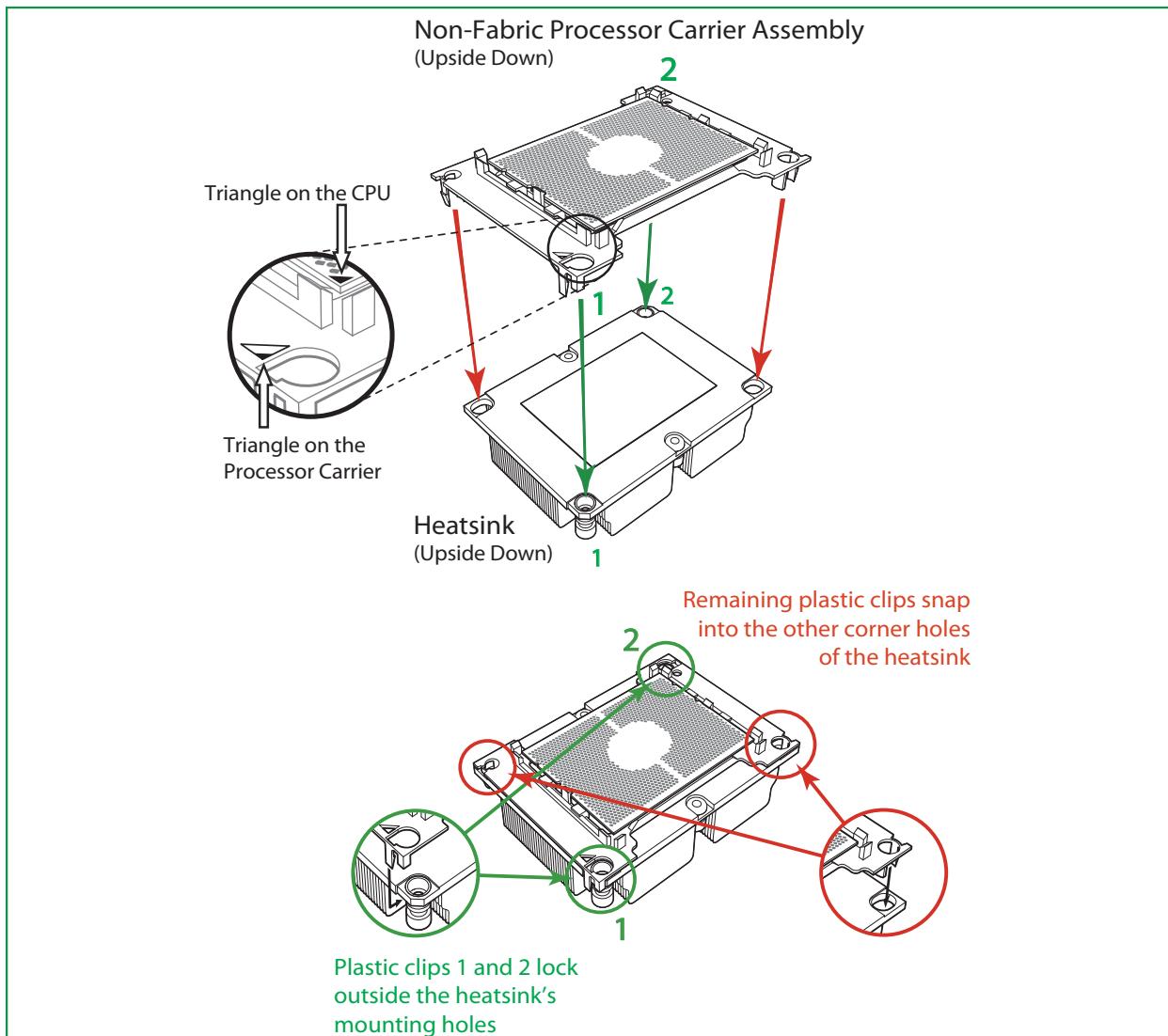


Processor Carrier Assembly (Non-F Model)

Assembling the Processor Heatsink Module

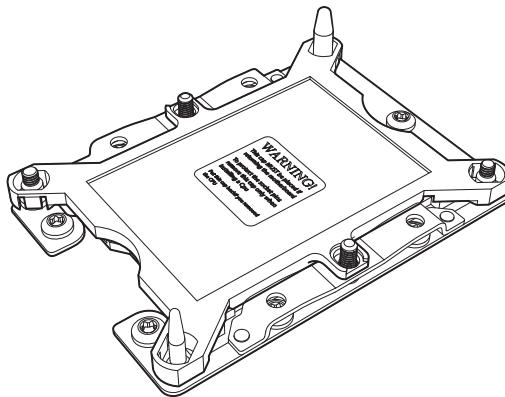
After creating the processor carrier assembly for the Non-F model processor, mount it onto the heatsink to create the processor heatsink module (PHM):

1. Note the label on top of the heatsink, which marks the heatsink mounting holes as 1, 2, 3, and 4. If this is a new heatsink, the thermal grease has been pre-applied on the underside. Otherwise, apply the proper amount of thermal grease.
2. Turn the heatsink over with the thermal grease facing up. Hold the processor carrier assembly so the processor's gold contacts are facing up, then align the triangle on the assembly with hole 1 of the heatsink. Press the processor carrier assembly down. The plastic clips of the assembly will lock outside of holes 1 and 2, while the remaining clips will snap into their corresponding holes.
3. Examine all corners to ensure that the plastic clips on the processor carrier assembly are firmly attached to the heatsink.

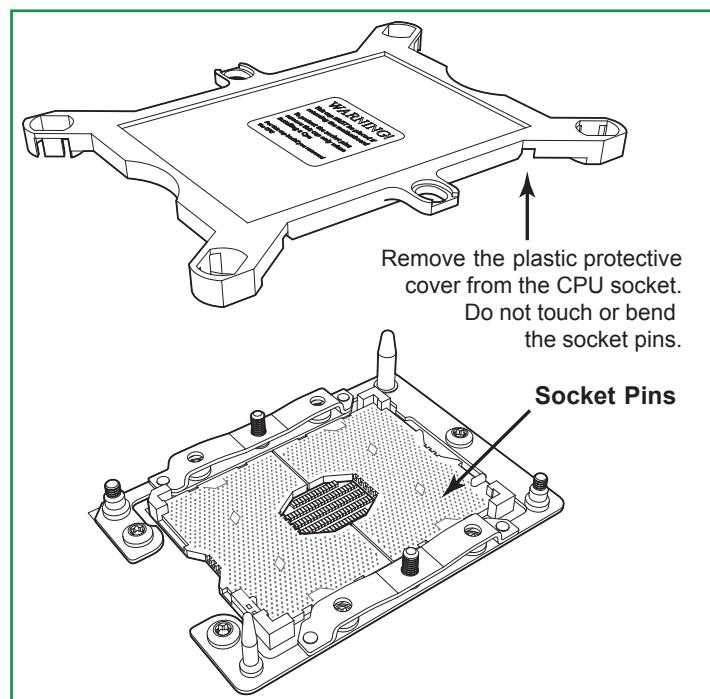


Preparing the CPU Socket for Installation

This motherboard comes with a plastic protective cover installed on the CPU socket. Remove it from the socket to install the Processor Heatsink Module (PHM). Gently pull up one corner of the plastic protective cover to remove it.



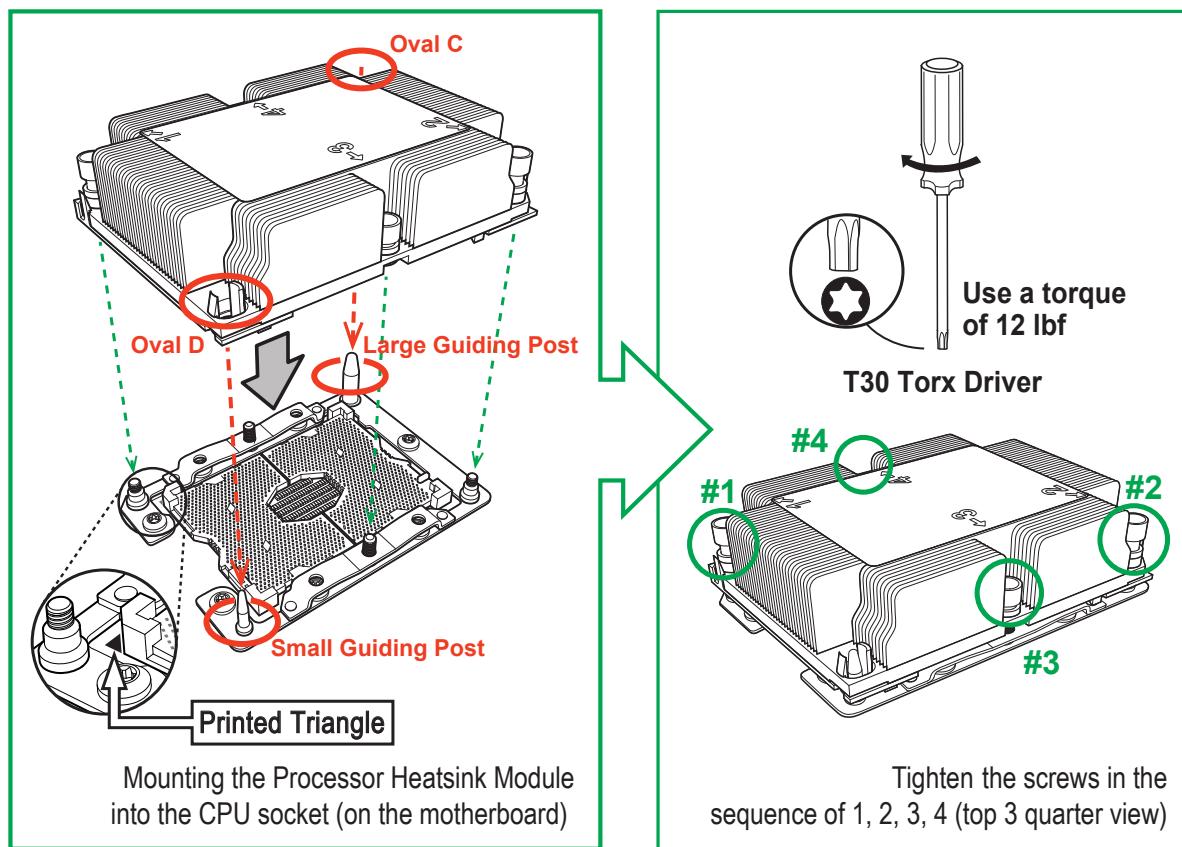
CPU Socket with Plastic Protective Cover



Installing the Processor Heatsink Module (PHM)

- Once you have assembled the processor heatsink module (PHM) by following the instructions, you are ready to install the processor heatsink module (PHM) into the CPU socket on the motherboard. To install the PHM into the CPU socket, follow the instructions below.
- Locate the triangle (pin 1) on the CPU socket, and locate the triangle (pin 1) at the corner of the PHM that is closest to "1." (If you have difficulty locating pin 1 of the PHM, turn the PHM upside down. With the LGA-lands side facing up, you will note the hollow triangle located next to a screw at the corner. Turn the PHM right side up, and you will see a triangle marked on the processor clip at the same corner of hollow triangle.)
- Carefully align pin 1 (the triangle) on the the PHM against pin 1 (the triangle) on the CPU socket.
- Once they are properly aligned, insert the two diagonal oval holes on the heatsink into the guiding posts.
- Using a T30 Torx-bit screwdriver, install four screws into the mounting holes on the socket to securely attach the PHM onto the motherboard starting with the screw marked "1" (in the sequence of 1, 2, 3, and 4).

Note: Do not use excessive force when tightening the screws to avoid damaging the LGA-lands and the processor.

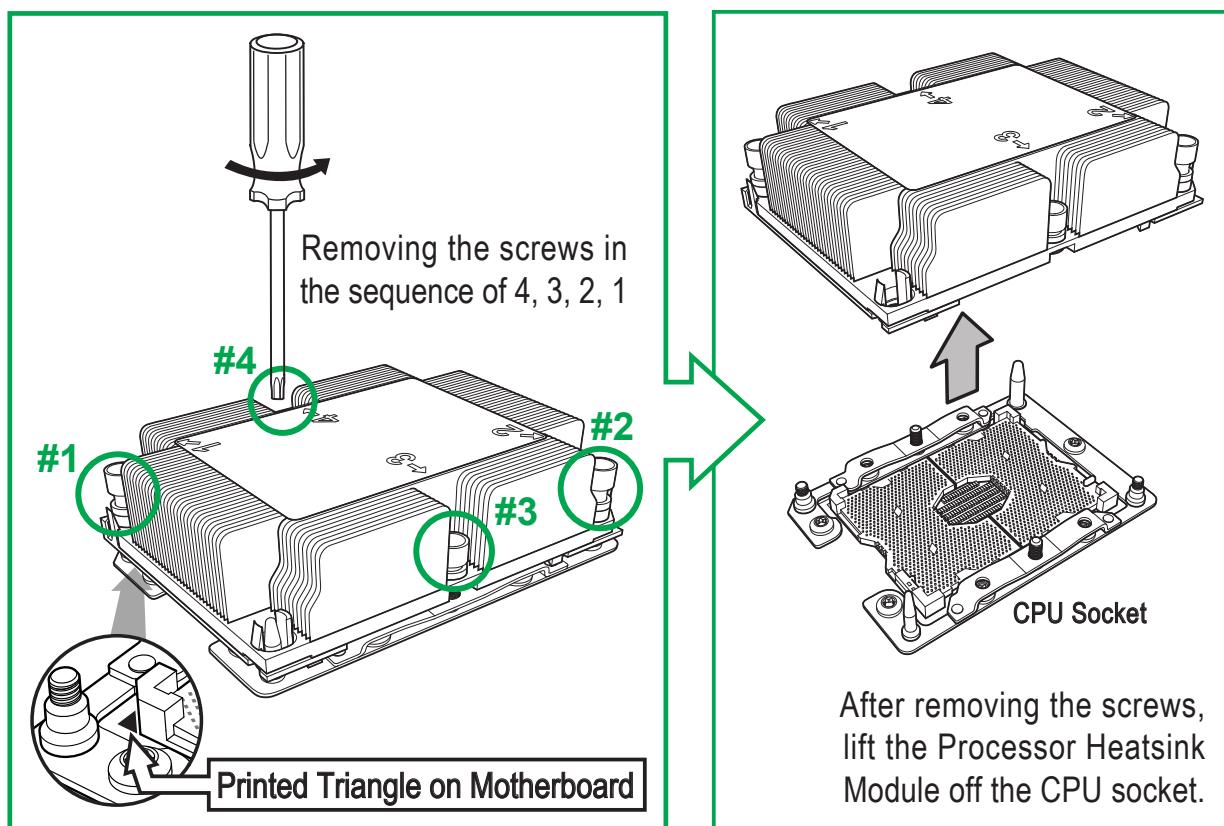


Removing the Processor Heatsink Module (PHM) from the Motherboard

Before removing the processor heatsink module (PHM), unplug power cord from the power outlet.

1. Using a T30 Torx-bit screwdriver, turn the screws on the PHM counterclockwise to loosen them from the socket, starting with screw marked #4 (in the sequence of 4, 3, 2, 1).
2. After all four screws are removed, wiggle the PHM gently and pull it up to remove it from the socket.

Note: To properly remove the processor heatsink module, be sure to loosen and remove the screws on the PHM in the sequence of 4, 3, 2, 1 as shown below.



Memory Installation

ESD Precautions

Electrostatic Discharge (ESD) can damage electronic components including memory modules. To avoid damaging your DIMM modules, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Handle the memory module by its edges only.
- Put the memory modules into the antistatic bags when not in use.
- Check the Supermicro website for recommended memory modules

Introduction to Intel® Optane DC Persistent Memory

Intel® 82xx/62xx/52xx/42xx supports new DCPMM (Optane™ DC Persistent Memory Modules) technology. DCPMM offers data persistence with higher capacity at lower latencies than the existing memory modules and provides hyper-speed storage capability for high performance computing platforms with flexible configuration options.

Memory Support

The X11DPG-OT-CPU supports up to 6 TB of 3Ds Load Reduced DIMM (3Ds LRDIMM) Load Reduced DIMM (LRDIMM), 3Ds Registered DIMM (3Ds RDIMM) Registered DIMM (RDIMM), DDR4 (288-pin) ECC 2933/2666/2400/2133 MHz memory modules in 24 slots. Populating these DIMM modules with a pair of memory modules of the same type and size will result in interleaved memory, which will improve memory performance.

Note: 2933 MHz memory is supported by the 82xx/62xx series processors only.

DDR4 Memory Support for 81xx/61xx/51xx/41xx/31xx Processors						
Type	Ranks Per DIMM and Data Width	DIMM Capacity (GB)		Speed (MT/s)		
		DRAM Density		One Slot per Channel	Two Slots per Channel	
		4 Gb	8 Gb	One DIMM per Channel	One DIMM per Channel	Two DIMMs per Channel
		1.2 Volts	1.2 Volts	1.2 Volts	1.2 Volts	1.2 Volts
RDIMM	SRx4	4 GB	8 GB	2666	2666	2666
	SRx8	8 GB	16 GB			
	DRx8	8 GB	16 GB			
	DRx4	16 GB	32 GB			
RDIMM 3Ds	QRX4	N/A	2H-64GB			
	8RX4	N/A	4H-128GB			
LRDIMM	QRx4	32 GB	64 GB			
LRDIMM 3Ds	QRx4	N/A	2H-64GB			
	8Rx4	N/A	4H-128 GB			

DDR4 Memory Support for 82xx/62xx/52xx/42xx/32xx Processors							
Type	Ranks Per DIMM and Data Width	DIMM Capacity (GB)			Speed (MT/s)		
		DRAM Density			One Slot per Channel	Two Slots per Channel	
		4 Gb	8 Gb	16 Gb	One DIMM per Channel	One DIMM per Channel	
		1.2 Volts	1.2 Volts	1.2 Volts	1.2 Volts	1.2 Volts	
RDIMM	SRx4	4 GB	8 GB	16 GB	2933	2933	2933
	SRx8	8 GB	16 GB	32 GB			
	DRx8	8 GB	16 GB	32 GB			
	DRx4	16 GB	32 GB	64 GB			
RDIMM 3Ds	QRX4	N/A	2H-64GB	2H-128GB			
	8RX4	N/A	4H-128GB	4H-256GB			
LRDIMM	QRx4	32 GB	64 GB	128 GB			
LRDIMM 3Ds	QRx4	N/A	2H-64GB	2H-64GB			
	8Rx4	N/A	4H-128 GB	4H-256 GB			

Memory Installation Sequence

Memory modules for the X11 UP/DP/MP motherboards are populated using the "Fill First" method. The blue memory slot of each channel is considered the "first DIMM module" of the channel, and the black slot, the second module of the channel. When installing memory modules, be sure to populate the blue memory slots first and then populate the black slots. To maximize memory capacity and performance, please populate all DIMM slots on the motherboard, including all blue slots and black slots.

General Memory Population Requirements

1. Be sure to use the memory modules of the same type and speed on the motherboard. Mixing of memory modules of different types and speeds is not allowed.
2. Using unbalanced memory topology such as populating two DIMMs in one channel while populating one DIMM in another channel on the same motherboard will result in reduced memory performance.

Populating memory slots with a pair of DIMM modules of the same type and size will result in interleaved memory, which will improve memory performance. Check the Supermicro website for possible updates to memory support.

DIMM Population Guidelines for Optimal Performance

For optimal memory performance, follow the instructions listed in the tables below when populating memory modules.

Key Parameters for DIMM Configuration

Key Parameters for DIMM Configurations	
Parameters	Possible Values
Number of Channels	1, 2, 3, 4, 5, or 6
Number of DIMMs per Channel	1DPC (1 DIMM Per Channel) or 2DPC (2 DIMMs Per Channel)
DIMM Type	RDIMM (w/ECC), 3DS RDIMM, LRDIMM, 3DS LRDIMM
DIMM Construction	non-3DS RDIMM Raw Cards: A/B (2Rx4), C (1Rx4), D (1Rx8), E (2Rx8) 3DS RDIMM Raw Cards: A/B (4Rx4) non-3DS LRDIMM Raw Cards: D/E (4Rx4) 3DS LRDIMM Raw Cards: A/B (8Rx4)

DIMM Mixing Guidelines

General DIMM Mixing Guidelines
<ul style="list-style-type: none"> • All DIMMs must be all DDR4 DIMMs. • x4 and x8 DIMMs can be mixed in the same channel. • Mixing of LRDIMMs and RDIMMs is not allowed in the same channel, across different channels, and across different sockets. • Mixing of non-3DS and 3DS LRDIMM is not allowed in the same channel, across different channels, and across different sockets.

Mixing of DIMM Types within a Channel			
DIMM Types	RDIMM	LRDIMM	3DS LRDIMM
RDIMM	Allowed	Not Allowed	Not Allowed
LRDIMM	Not Allowed	Allowed	Not Allowed
3DS LRDIMM	Not Allowed	Not Allowed	Allowed

Memory Population Table for the X11DP Motherboard w/24 DIMM Slots Onboard	
CPUs/DIMMs	Memory Population Sequence
1 CPU & 1 DIMM	CPU1: P1-DIMMA1
1 CPU & 2 DIMMs	CPU1: P1-DIMMA1/P1-DIMMD1
1 CPU & 3 DIMMs	CPU1: P1-DIMMC1/P1-DIMMB1/P1-DIMMA1
1 CPU & 4 DIMMs	CPU1: P1-DIMMB1/P1-DIMMA1/P1-DIMMD1/P1-DIMME1
1 CPU & 5 DIMMs*	CPU1: P1-DIMMC1/P1-DIMMB1/P1-DIMMA1/P1-DIMMD1/P1-DIMME1
1 CPU & 6 DIMM	CPU1: P1-DIMMC1/P1-DIMMB1/P1-DIMMA1/P1-DIMMD1/P1-DIMME1/P1-DIMMF1
1 CPU & 7 DIMMs*	CPU1: P1-DIMMB1/P1-DIMMB2/P1-DIMMA1/P1-DIMMA2/P1-DIMMD1/P1-DIMME1/P1-DIMMF1
1 CPU & 8 DIMMs	CPU1: P1-DIMMB1/P1-DIMMB2/P1-DIMMA1/P1-DIMMA2/P1-DIMMD2/P1-DIMMD1/P1-DIMME2/P1-DIMME1
1 CPU & 9 DIMMs*	CPU1: P1-DIMMC1/P1-DIMMC2/P1-DIMMB1/P1-DIMMB2/P1-DIMMA1/P1-DIMMA2/P1-DIMMD1/P1-DIMME1/P1-DIMMF1
1 CPU & 10 DIMMs*	CPU1: P1-DIMMC1/P1-DIMMB1/P1-DIMMB2/P1-DIMMA1/P1-DIMMA2/P1-DIMMD2/P1-DIMMD1/P1-DIMME2/P1-DIMME1/P1-DIMMF1
1 CPU & 11 DIMMs*	CPU1: P1-DIMMC1/P1-DIMMC2/P1-DIMMB1/P1-DIMMB2/P1-DIMMA1/P1-DIMMA2/P1-DIMMD2/P1-DIMMD1/P1-DIMME2/P1-DIMME1/P1-DIMMF1
1 CPU & 12 DIMMs	CPU1: P1-DIMMC1/P1-DIMMC2/P1-DIMMB1/P1-DIMMB2/P1-DIMMA1/P1-DIMMA2/P1-DIMMD2/P1-DIMMD1/P1-DIMME2/P1-DIMME1/P1-DIMMF2/P1-DIMMF1
2 CPUs & 2 DIMMs	CPU1: P1-DIMMA1 CPU2: P2-DIMMA1
2 CPUs & 4 DIMMs	CPU1: P1-DIMMA1/P1-DIMMD1 CPU2: P2-DIMMA1/P2-DIMMD1
2 CPUs & 6 DIMMs	CPU1: P1-DIMMC1/P1-DIMMB1/P1-DIMMA1 CPU2: P2-DIMMC1/P2-DIMMB1/P2-DIMMA1
2 CPUs & 8 DIMMs	CPU1: P1-DIMMB1/P1-DIMMA1/P1-DIMMD1/P1-DIMME1 CPU2: P2-DIMMB1/P2-DIMMA1/P2-DIMMD1/P2-DIMME1
2 CPUs & 10 DIMMs	CPU1: P1-DIMMC1/P1-DIMMB1/P1-DIMMA1/P1-DIMMD1/P1-DIMME1/P1-DIMMF1 CPU2: P2-DIMMB1/P2-DIMMA1/P2-DIMMD1/P2-DIMME1
2 CPUs & 12 DIMMs	CPU1: P1-DIMMC1/P1-DIMMB1/P1-DIMMA1/P1-DIMMD1/P1-DIMME1/P1-DIMMF1 CPU2: P2-DIMMC1/P2-DIMMB1/P2-DIMMA1/P2-DIMMD1/P2-DIMME1/P2-DIMMF1
2 CPUs & 14 DIMMs	CPU1: P1-DIMMB1/P1-DIMMB2/P1-DIMMA1/P1-DIMMA2/P1-DIMMD2/P1-DIMMD1/P1-DIMME2/P1-DIMME1 CPU2: P2-DIMMC1/P2-DIMMB1/P2-DIMMA1/P2-DIMMD1/P2-DIMME1/P2-DIMMF1
2 CPUs & 16 DIMMs	CPU1: P1-DIMMB1/P1-DIMMB2/P1-DIMMA1/P1-DIMMA2/P1-DIMMD2/P1-DIMMD1/P1-DIMME2/P1-DIMME1 CPU2: P2-DIMMB1/P2-DIMMB2/P2-DIMMA1/P2-DIMMA2/P2-DIMMD2/P2-DIMMD1/P2-DIMME2/P2-DIMME1
2 CPUs & 18 DIMMs	CPU1: P1-DIMMC1/P1-DIMMC2/P1-DIMMB1/P1-DIMMB2/P1-DIMMA1/P1-DIMMA2/P1-DIMMD2/P1-DIMMD1/P1-DIMME2/P1-DIMME1 CPU2: P2-DIMMC1/P2-DIMMB1/P2-DIMMA1/P2-DIMMD1/P2-DIMME1/P2-DIMMF1
2 CPUs & 20 DIMMs	CPU1: P1-DIMMC1/P1-DIMMC2/P1-DIMMB1/P1-DIMMB2/P1-DIMMA1/P1-DIMMA2/P1-DIMMD2/P1-DIMMD1/P1-DIMME2/P1-DIMME1 CPU2: P2-DIMMB1/P2-DIMMB2/P2-DIMMA1/P2-DIMMA2/P2-DIMMD2/P2-DIMMD1/P2-DIMME2/P2-DIMME1
2 CPUs & 22 DIMMs*	CPU1: P1-DIMMC1/P1-DIMMC2/P1-DIMMB1/P1-DIMMB2/P1-DIMMA1/P1-DIMMA2/P1-DIMMD2/P1-DIMMD1/P1-DIMME2/P1-DIMME1/P1-DIMMF1 CPU2: P2-DIMMC1/P2-DIMMC2/P2-DIMMB1/P2-DIMMB2/P2-DIMMA1/P2-DIMMA2/P2-DIMMD2/P2-DIMMD1/P2-DIMME2/P2-DIMME1
2 CPUs & 24 DIMMs	CPU1: P1-DIMMC1/P1-DIMMC2/P1-DIMMB1/P1-DIMMB2/P1-DIMMA1/P1-DIMMA2/P1-DIMMD2/P1-DIMMD1/P1-DIMME2/P1-DIMME1/P1-DIMMF2/P1-DIMMF1 CPU2: P2-DIMMC1/P2-DIMMC2/P2-DIMMB1/P2-DIMMB2/P2-DIMMA1/P2-DIMMA2/P2-DIMMD2/P2-DIMMD1/P2-DIMME2/P2-DIMME1/P2-DIMMF2/P2-DIMMF1

*Unbalanced, not recommended.

Symmetric Population within 1 CPU Socket														
Modes	P1-DIMMF1	P1-DIMMF2	P1-DIMME1	P1-DIMME2	P1-DIMMD1	P1-DIMMD2	P1-DIMMA2	P1-DIMMA1	P1-DIMMB2	P1-DIMMB1	P1-DIMMC2	P1-DIMMC1	Channel Config.	
AD	DRAM1	DCPMM	DRAM1	DCPMM	DRAM1	DCPMM	DCPMM	DRAM1	DCPMM	DRAM1	DCPMM	DRAM1	2-2-2	
MM	DRAM1	DCPMM	DRAM1	DCPMM	DRAM1	DCPMM	DCPMM	DRAM1	DCPMM	DRAM1	DCPMM	DRAM1	2-2-2	
AD + MM	DRAM3	DCPMM	DRAM3	DCPMM	DRAM3	DCPMM	DCPMM	DRAM3	DCPMM	DRAM3	DCPMM	DRAM3	2-2-2	
AD	DRAM1	-	DRAM1	-	DRAM1	DCPMM	DCPMM	DRAM1	-	DRAM1	-	DRAM1	2-1-1	
MM	DRAM2	-	DRAM2	-	DRAM2	DCPMM	DCPMM	DRAM2	-	DRAM2	-	DRAM2	2-1-1	
AD + MM	DRAM3	-	DRAM3	-	DRAM3	DCPMM	DCPMM	DRAM3	-	DRAM3	-	DRAM3	2-1-1	
AD	DRAM1	-	DRAM1	DCPMM	DRAM1	DCPMM	DCPMM	DRAM1	DCPMM	DRAM1	-	DRAM1	2-2-1	
MM	DRAM1	-	DRAM1	DCPMM	DRAM1	DCPMM	DCPMM	DRAM1	DCPMM	DRAM1	-	DRAM1	2-2-1	
AD + MM	DRAM3	-	DRAM3	DCPMM	DRAM3	DCPMM	DCPMM	DRAM3	DCPMM	DRAM3	-	DRAM3	2-2-1	
AD	DCPMM	-	DRAM1	-	DRAM1	-	-	DRAM1	-	DRAM1	-	DCPMM	1-1-1	
MM	DCPMM	-	DRAM1	-	DRAM1	-	-	DRAM1	-	DRAM1	-	DCPMM	1-1-1	
AD + MM	DCPMM	-	DRAM3	-	DRAM3	-	-	DRAM3	-	DRAM3	-	DCPMM	1-1-1	
AD	DCPMM	-	DRAM1	-	DCPMM	2-2-1								

Asymmetric Population within 1 CPU Socket													
Modes	P1-DIMMF1	P1-DIMMF2	P1-DIMME1	P1-DIMME2	P1-DIMMD1	P1-DIMMD2	P1-DIMMA2	P1-DIMMA1	P1-DIMMB2	P1-DIMMB1	P1-DIMMC2	P1-DIMMC1	Channel Config.
AD	DRAM1	-	DRAM1	-	DRAM1	-	DCPMM	DRAM1	-	DRAM1	-	DRAM1	2/1-1-1
AD*	DRAM1	-	DRAM1	-	DRAM1	-	DCPMM	DRAM1	-	DRAM1	-	DRAM1	2/1-1-1

Legend (for the two tables above)													
DDR4 Type							Capacity						
DRAM1							Any Capacity						
DRAM2							Refer to Validation Matrix (DDR4 DIMMs validated with DCPMM) below.						
DRAM3													

Note: DDR4 single rank x8 is not available for DCPMM Memory Mode or App-Direct Mode.

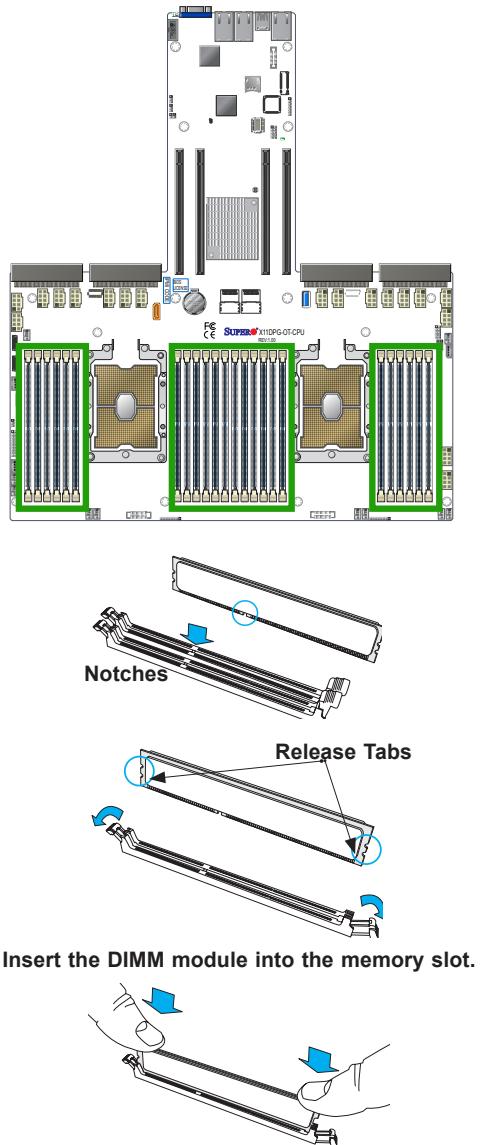
Legend (-for the two tables above)													
Capacity													
DCPMM		Any Capacity (Uniformly for all channels for a given configuration)											

- * 2nd socket has no DCPMM DIMM
- For MM, general NM/FM ratio is between 1:4 and 1:16. Excessive capacity for FM can be used for AD. (NM = Near Memory; FM = Far Memory)
- For each individual population, rearrangements between channels are allowed as long as the resulting population is compliant with the PDG rules for the 82xx/62xx/52xx/42xx platform.
- For each individual population, please use the same DDR4 DIMM in all slots.
- For each individual population, sockets are normally symmetric with exceptions for 1 DCPMM per socket and 1 DCPMM per node case. Currently, DCPMM modules operate at 2666 MHz.
- No mixing of DCPMM and NVMDIMMs within the same platform is allowed.
- This DCPMM population guide targets a balanced DCPMM-to-DRAM-cache ratio in MM and MM + AD modes.

Validation Matrix (DDR4 DIMMs Validated w/DCPMM)			
DIMM Type	Ranks Per DIMM & Data Width (Stack)	DIMM Capacity (GB)	
		4Gb	8Gb
RDIMM	1Rx4	8GB	16GB
	2Rx8	8GB	16GB
	2Rx4	16GB	32GB
LRDIMM	4Rx4	N/A	64GB
LRDIMM 3DS	8Rx4 (4H)	N/A	128GB

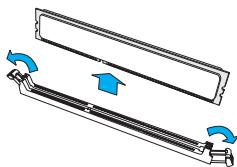
DIMM Installation

1. Follow the instructions given in the memory population guidelines listed in the previous sections to install memory modules on your motherboard. For the system to work properly, please use memory modules of the same type and speed on the motherboard. (See the Note below.)
2. Push the release tabs outwards on both ends of the DIMM slot to unlock it.
3. Align the key of the DIMM module with the receptive point on the memory slot.
4. Align the notches on both ends of the module against the receptive points on the ends of the slot.
5. Use two thumbs together to press the DIMM module straight down into the slot until the module snaps into place.
6. Press the release tabs to the lock positions to secure the DIMM module into the slot.



DIMM Module Removal

Press the release tabs on both ends of the DIMM socket to release the DIMM module from the socket as shown in the drawing on the right.



Warning! **1.** To avoid damage to the DIMM module or the DIMM socket, do not use excessive force when pressing the release tabs on the ends of the DIMM socket. **2.** Handle DIMM modules with care. Carefully follow all the instructions given in Section 1 of this user guide to avoid ESD-related damage to your components or system. **3.** All graphics, including the layout drawing above, are for reference only. Your system components may or may not look the same as shown in this user guide.

PCI Expansion Card Installation

The system includes a daughterboard for GPU/PCIe expansion capabilities. See Figure 3-2 for an example of the daughterboard in the system. See Figures 3-3 and 3-4 for topology for the daughterboards in 4029GP-TRT/TRT2/TRT3 systems.

Installing Expansion Cards

1. Power down the system and open the chassis cover.
2. Remove the screw holding the chassis slot shield in place.
3. Insert the expansion card into the serverboard slot, while aligning the expansion card shield with the slot in the rear of the chassis.
4. Secure expansion card shield to the chassis using the screw previously removed.

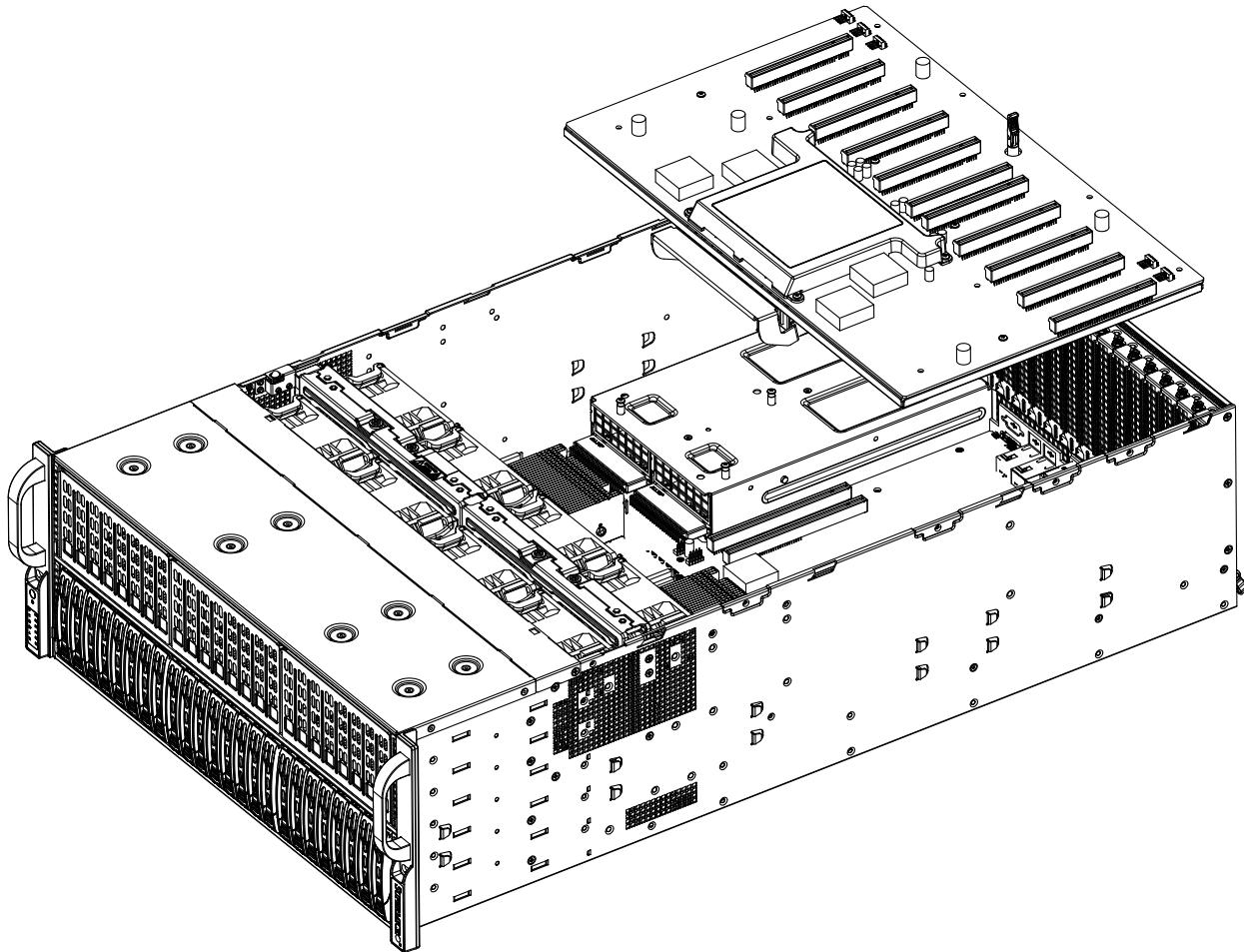


Figure 3-2. Daughter Board in System

Note: The figures above are intended to show the PCI-E expansion card installation locations only. The serverboard may differ from that found in the 4029GP-TRT/TRT2/TRT3.

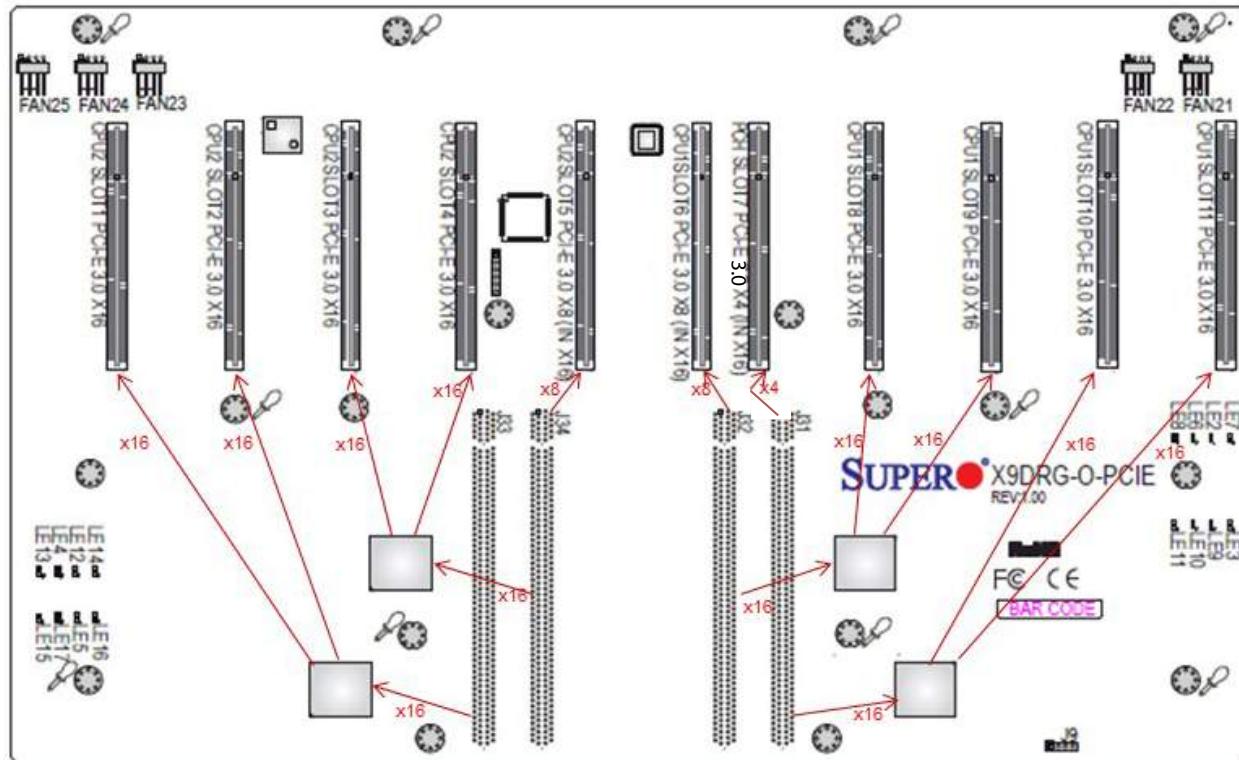


Figure 3-3. X9DRG-O-PCIE-P Daughter Board for 4029GP-TRT System

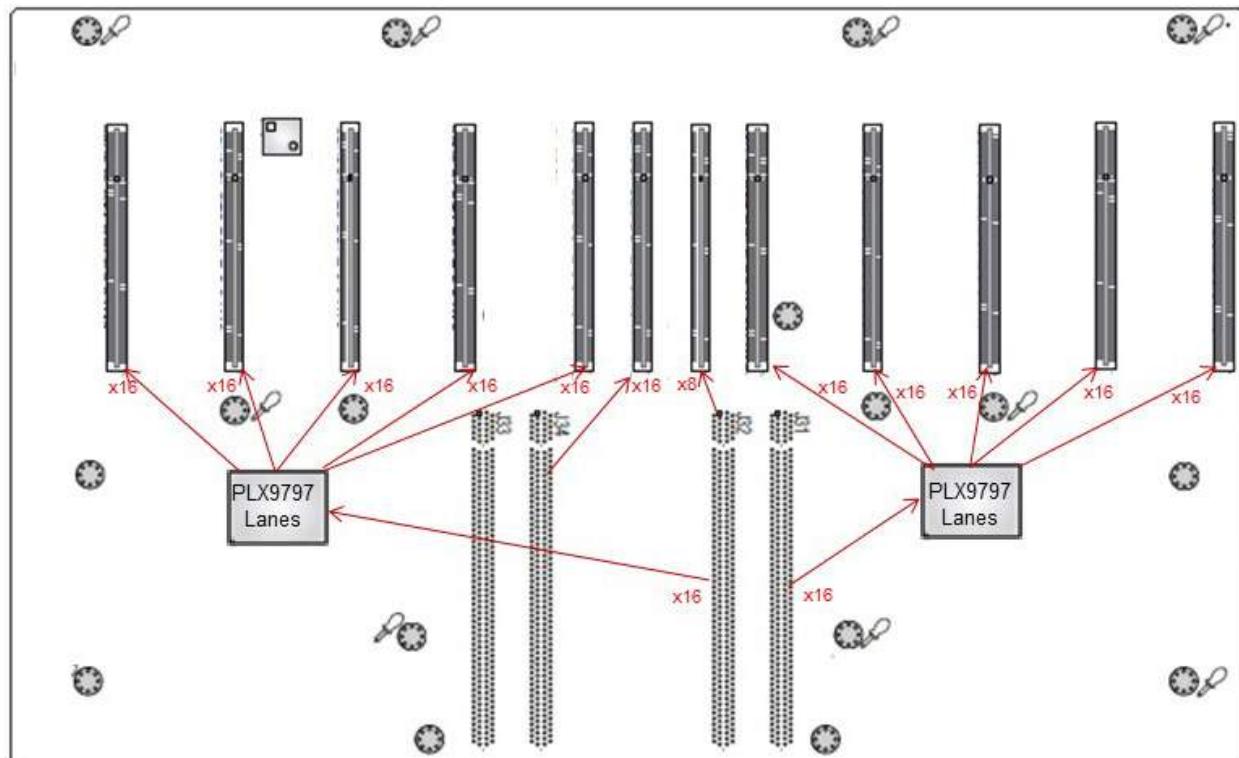


Figure 3-4. X10DRG-O-PCIE-P Daughter Board for 4029GP-TRT2 System

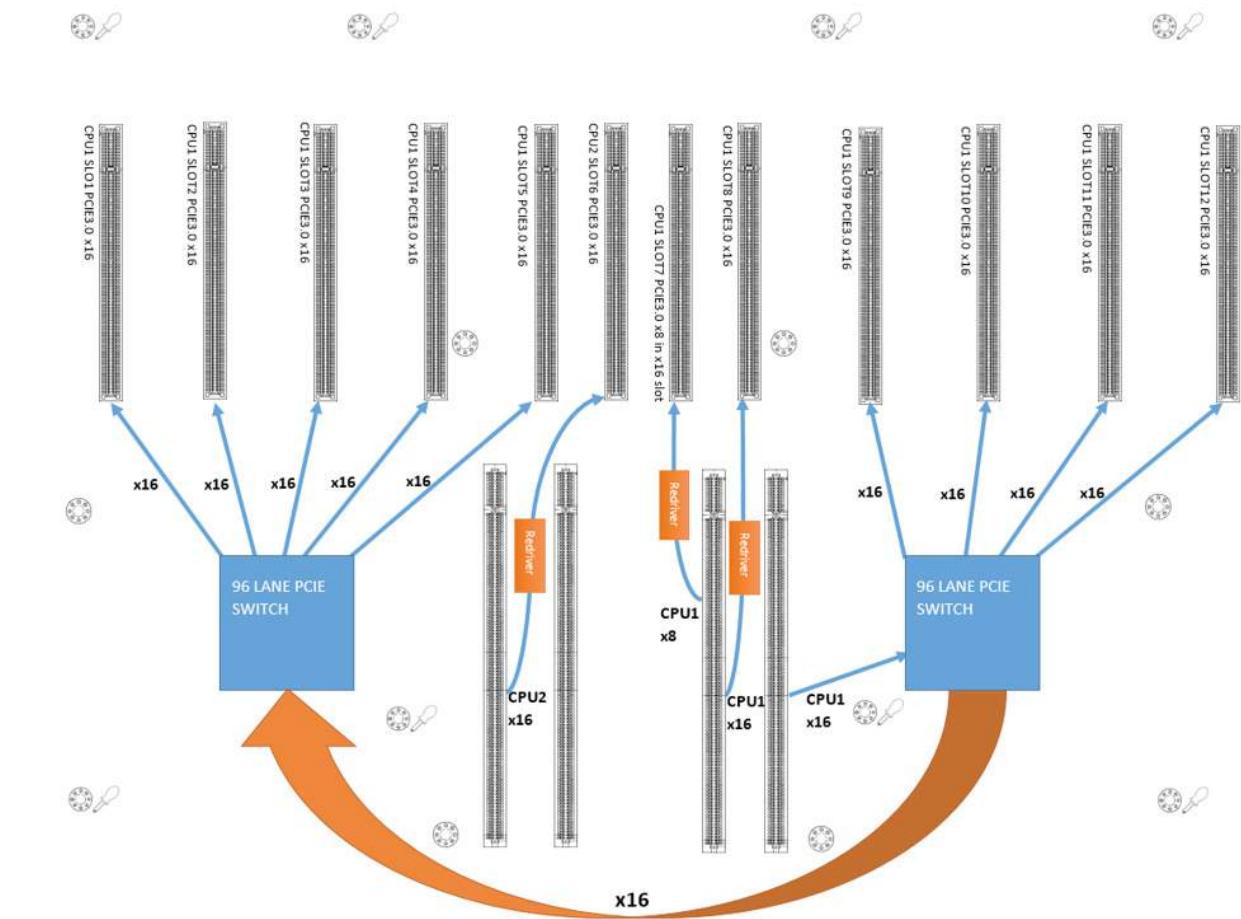


Figure 3-4. X11DPG-O-PCIE-P Daughter Board for 4029GP-TRT3 System

Motherboard Battery

The motherboard uses non-volatile memory to retain system information when system power is removed. This memory is powered by a lithium battery residing on the motherboard.

Replacing the Battery

Begin by removing power from the system as described in section 3.1.

1. Push aside the small clamp that covers the edge of the battery. When the battery is released, lift it out of the holder.
2. To insert a new battery, slide one edge under the lip of the holder with the positive (+) side facing up. Then push the other side down until the clamp snaps over it.

Note: Handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

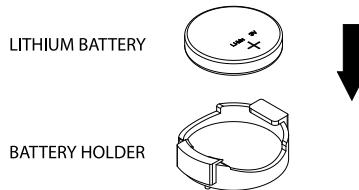


Figure 3-5. Installing the Onboard Battery

Warning: There is a danger of explosion if the onboard battery is installed upside down (which reverses its polarities). This battery must be replaced only with the same or an equivalent type recommended by the manufacturer (CR2032).

3.4 Chassis Components

Hard Drives

The SC418G supports a total of 24 hard disk drives, which are mounted in drive carriers and reside within the hard drive bays. These drives are hot-swappable and can be removed or replaced without powering down the chassis. Enterprise NVMe, SAS or SATA HDDs only are recommended.

Removing Hard Drives and their Carriers from the Hard Drive Bays

1. Press the release button on the drive carrier. This extends the drive carrier handle.
2. Use the handle to pull the drive and its carrier out of the chassis.

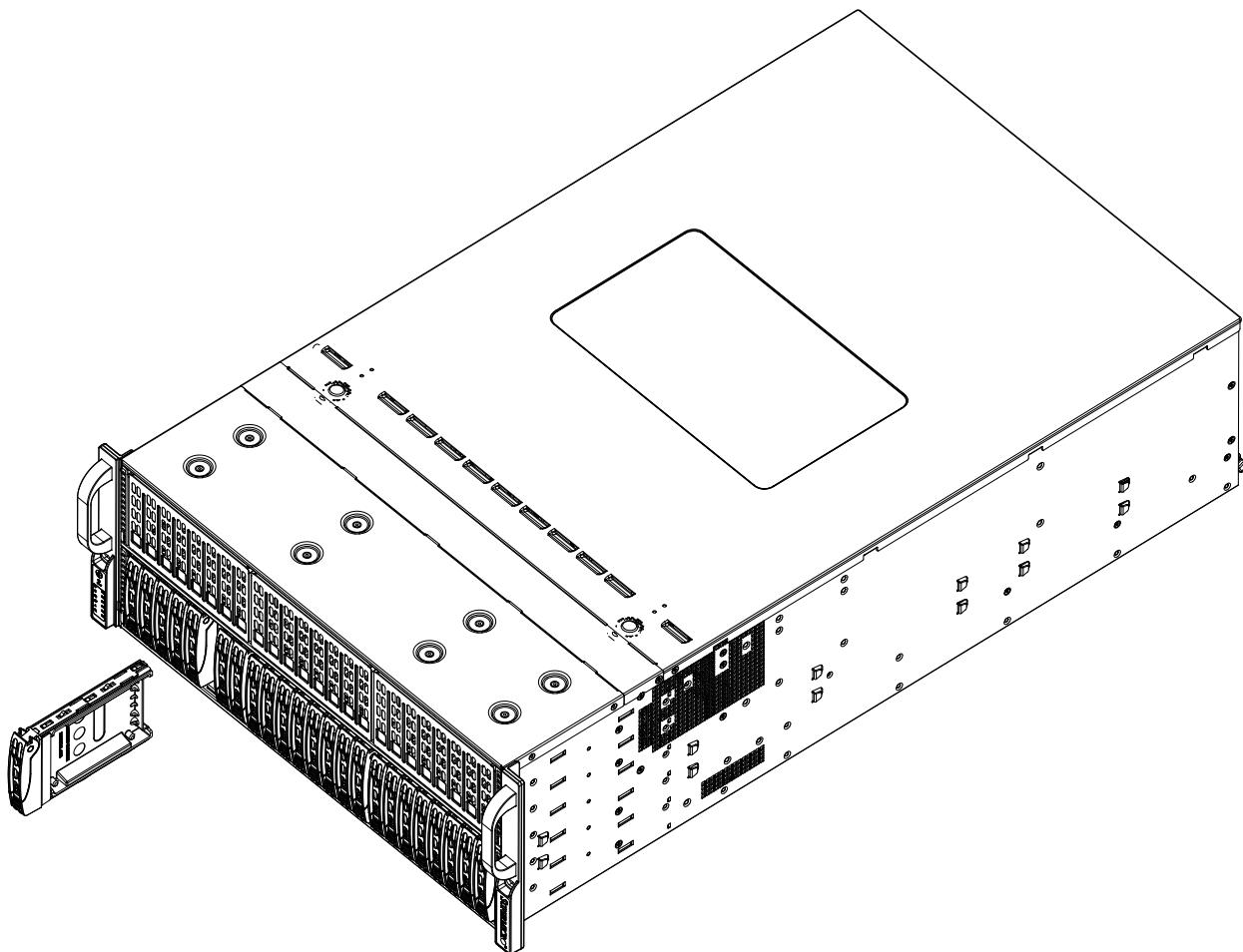


Figure 3-6. Removing a Hard Drive Carrier

Installing a Hard Drive into a Drive Carrier

1. Insert a drive into the carrier with the PCB side facing down and the connector end toward the rear of the carrier.
2. Align the drive in the carrier so that the screw holes of both line up. Note that there are holes in the carrier marked "SATA" to aid in correct installation.
3. Secure the drive to the carrier with four screws as illustrated below.
4. Insert the drive carrier into its bay, keeping the carrier oriented so that the hard drive is on the top of the carrier and the release button is on the right side. When the carrier reaches the rear of the bay, the release handle will retract.
5. Push the handle in until it clicks into its locked position.

Note: Your operating system must have RAID support to enable the hot-plug capability of the hard drives.

Note: Refer to the following FTP site for RAID setup guidelines: <ftp://ftp.supermicro.com/driver/SAS/LSI/LSI_SAS_EmbMRAID_SWUG.pdf> and Supermicro's web site for additional information <<http://www.supermicro.com/support/manuals/>>.

Note: Enterprise level hard disk drives are recommended for use in Supermicro chassis and servers. For information on recommended HDDs, visit the Supermicro website at <http://www.supermicro.com/products/nfo/files/storage/SBB-HDDCompList.pdf>

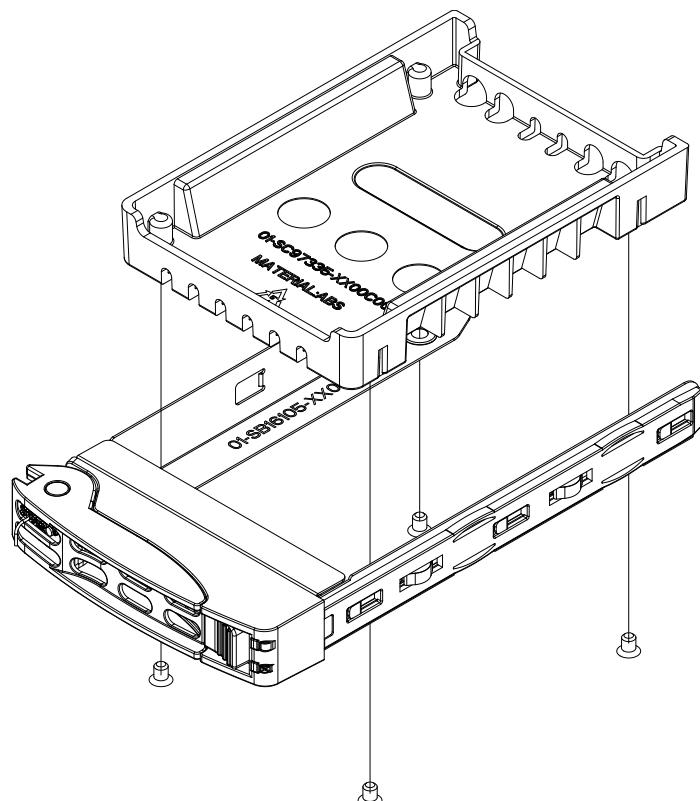


Figure 3-7. Removing a Dummy Drive from a Tray

Caution: Use caution when working around the hard drive backplane. Do not touch the backplane with any metal objects and make sure no ribbon cables touch the backplane or obstruct the holes, which aid in proper airflow.

Caution: Regardless of how many hard drives are installed, all drive carriers must remain in the drive bays to maintain proper airflow.

Hard Drive Carrier Indicators

Each hard drive carrier has two LED indicators: an activity indicator and a status indicator. In RAID configurations, the status indicator lights to indicate the status of the drive. In non-RAID configurations, the status indicator remains off. See the table below for details.

	LED Color	State	Status
Activity LED	Blue	Solid On	SAS/NVMe drive installed
	Blue	Blinking	I/O activity
Status LED	Red	Solid On	Failed drive for SAS/SATA/NVMe with RSTe support
	Red	Blinking at 1 Hz	Rebuild drive for SAS/SATA/NVMe with RSTe support
	Red	Blinking with two blinks and one stop at 1 Hz	Hot spare for SAS/SATA/NVMe with RSTe support
	Red	On for five seconds, then off	Power on for SAS/SATA/NVMe with RSTe support
	Red	Blinking at 4 Hz	Identify drive for SAS/SATA/NVMe with RSTe support
	Green	Solid On	Safe to remove NVMe device
	Amber	Blinking at 1 Hz	Attention state—do not remove NVMe device

Replacing Fans

The chassis contains eight 9-cm system fans that provide cooling for the system. All fans are hot-swappable, so there is no need to power down the system when switching fans.

Changing a System Fan

1. If necessary, open the chassis while the power is running to determine which fan has failed. (Never run the server for an extended period of time with the chassis cover open).
2. Press the fan housing lever to unlock the fan from the bracket.
3. While applying pressure to fan housing lever, gently push the fan upwards from underneath the fan housing to remove it.
4. Place the new fan into the vacant space in the housing. Apply pressure to the top of the new fan to lock it into the fan housing. The new fan should 'click' into place.
5. Confirm that the fan is working properly before replacing the chassis cover.

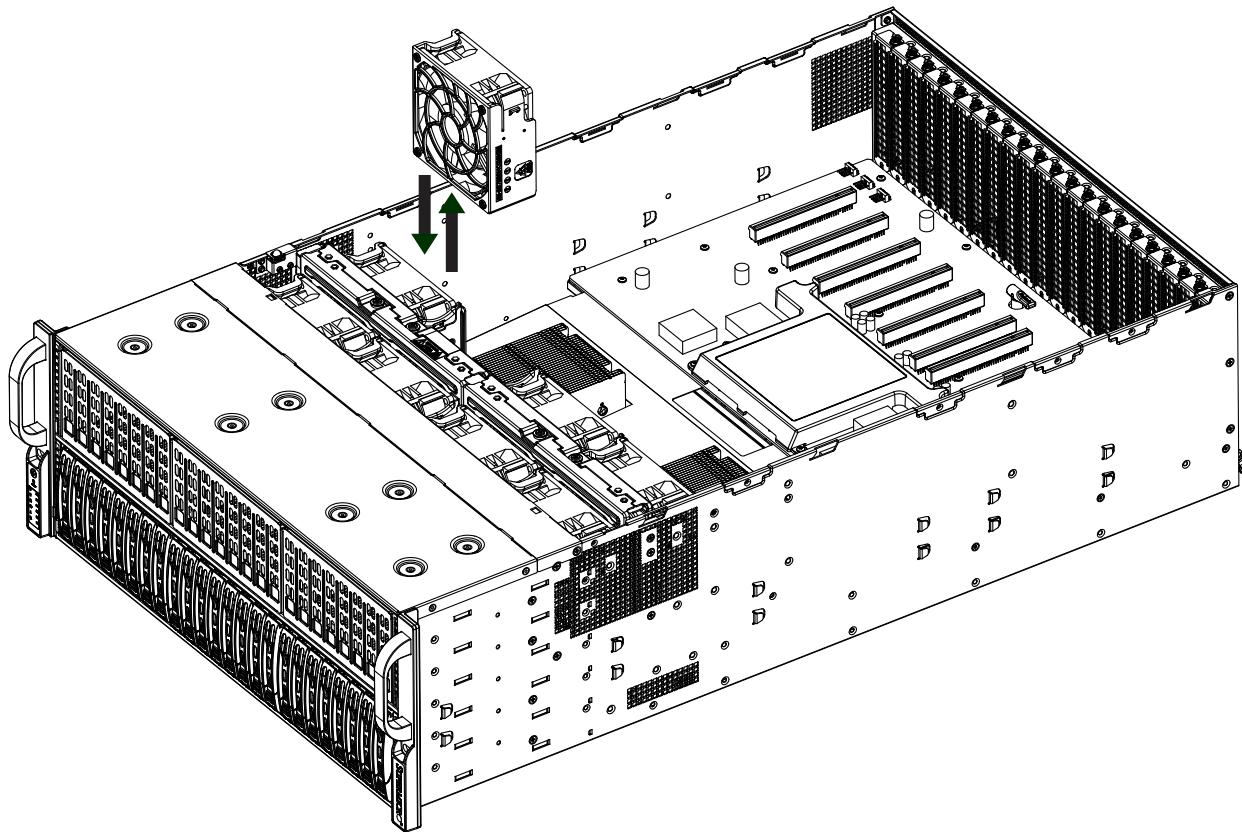


Figure 3-8. Replacing a System Fan

Power Supply

The system includes four hot-plug power modules. They automatically sense the input voltage between 100v to 240v, and operate at that voltage. Note that different input voltages will result in different maximum power output levels.

In the event of a power supply failure, the remaining power supply will automatically take over. The failed power module can be replaced without powering-down the system. Replace with the same model. Replacement modules can be ordered directly from Supermicro.

An amber light on the power supply is illuminated when the power is switched off. A green light indicates that the power supply is operating.

Replacing the Power Supply

1. Unplug the AC power cord from the failed power supply module.
2. Push and hold the release tab on the back of the power supply.
3. Grasp the handle of the power supply and pull it out of its bay.
4. Push the new power supply module into the power bay until it clicks into the locked position.
5. Plug the AC power cord back into the power supply module.

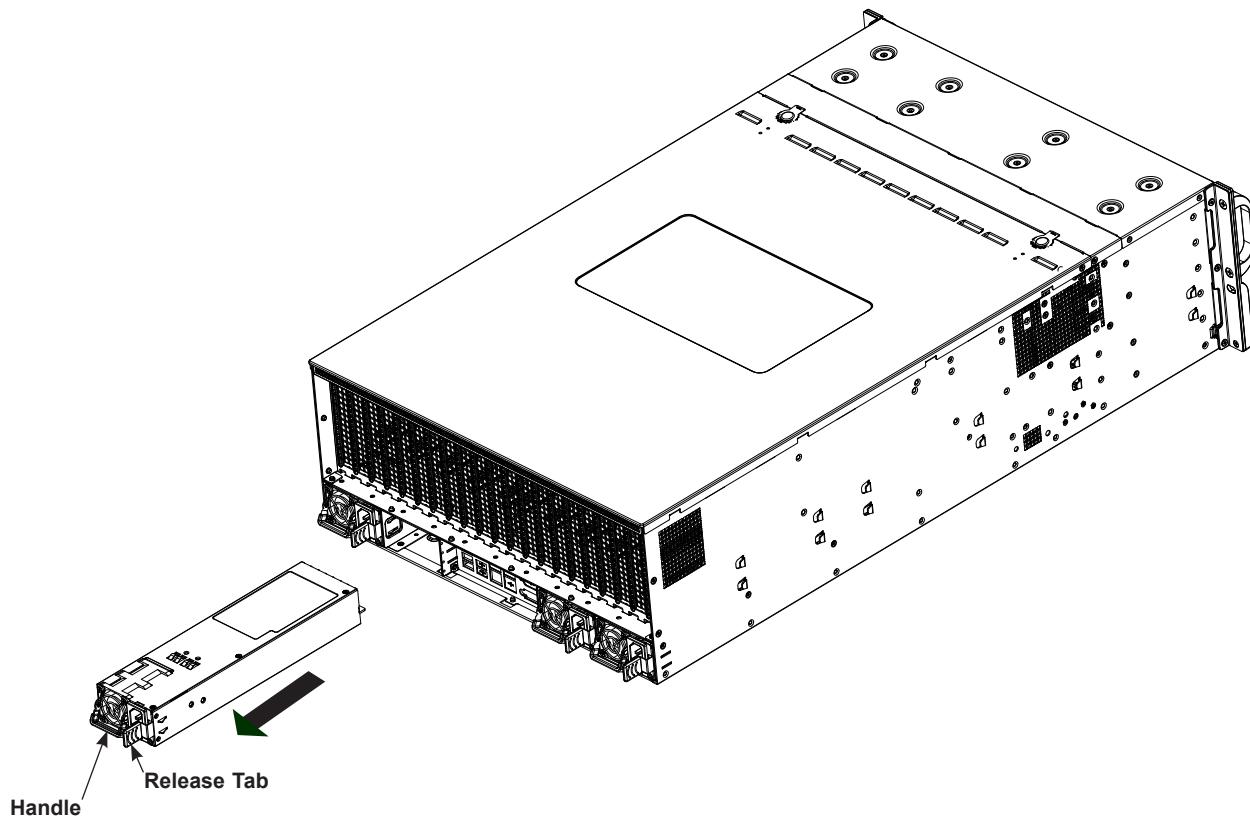


Figure 3-9. Replacing a Power Supply

Chapter 4

Motherboard Connections

This section describes the connections on the motherboard and provides pinout definitions. Note that depending on how the system is configured, not all connections are required. The LEDs on the motherboard are also described here. A serverboard layout indicating component locations may be found in Chapter 1.

Please review the Safety Precautions in Chapter 3 before installing or removing components.

Data Cables

The data cables in the system have been carefully routed to maintain airflow efficiency. If you disconnect any of these cables, take care to re-route them as they were originally when reconnecting them.

Important! Make sure the cables do not come into contact with the fans.

4.1 Power Connections

SMCI-Proprietary Power Connectors

Four SMCI proprietary power supplies are located at JPW19/JPW20 and JPW21/JPW22 on the motherboard. These connectors are reserved for an SMCI proprietary server use only.

Important: To provide adequate power to the motherboard, connect all the 8-pin power connectors to the power supply. Failure to do so may void the manufacturer's warranty on your power supply and motherboard.

12V 8-pin System GPU Power Connectors

Sixteen 8-pin 12V power **GPU** connectors are located at (JPWR1-JPWR8, JPWR9-JPWR16) on the motherboard to provide power to system and GPU components. Refer to the table below for pin definitions.

12V 8-pin GPU Power Pin Definitions	
Pin#	Definition
1 - 4	Ground
5 - 8	+12V

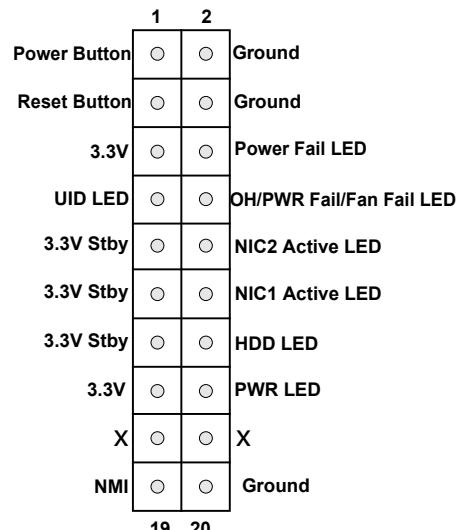
12V 8-pin CPU Power Connectors

Two 8-pin 12V power connectors (JPW11/JPW12) are located on the motherboard to provide power to the processors. Refer to the table below for pin definitions.

12V 8-pin Power Pin Definitions	
Pin#	Definition
1 - 4	Ground
5 - 8	+12V

4.2 Front Control Panel

JF1 contains header pins for various buttons and indicators that are normally located on a control panel at the front of the chassis. These connectors are designed specifically for use with Supermicro chassis. See the figure below for the descriptions of the front control panel buttons and LED indicators.



Power Button

The Power Button connection is located on pins 1 and 2 of JF1. Momentarily contacting both pins will power on/off the system. This button can also be configured to function as a suspend button (with a setting in the BIOS - see Chapter 6). To turn off the power when the system is in suspend mode, press the button for 4 seconds or longer. Refer to the table below for pin definitions.

Power Button Pin Definitions (JF1)	
Pins	Definition
1	Signal
2	Ground

Reset Button

The Reset Button connection is located on pins 3 and 4 of JF1. Attach it to a hardware reset switch on the computer case to reset the system. Refer to the table below for pin definitions.

Reset Button Pin Definitions (JF1)	
Pins	Definition
3	Reset
4	Ground

Power Fail LED

The Power Fail LED connection is located on pins 5 and 6 of JF1. Refer to the table below for pin definitions.

Power Fail LED Pin Definitions (JF1)	
Pin#	Definition
5	3.3V
6	PWR Supply Fail

OH/Fan Fail/PWR Fail/UID LED

Connect an LED cable to pins 7 and 8 of the Front Control Panel (JF1) to use UID/Overheat/Fan Fail/Power Fail LED connections. The LED on pin 8 provides warnings of overheat, power failure or fan failure. Refer to the tables below for details.

Information LED-UID/OH/PWR Fail/Fan Fail LED Pin Definitions (Pin 7 & Pin 8 of JF1)	
Status	Description
Solid red	An overheat condition has occurred. (This may be caused by cable congestion).
Blinking red (1Hz)	Fan failure: check for an inoperative fan.
Blinking red (0.25Hz)	Power failure: check for a non-operational power supply
Solid blue	Local UID is activated. Use this function to locate a unit in a rack mount environment that might be in need of service.
Blinking blue (300 msec)	Remote UID is on. Use this function to identify a unit from a remote location that might be in need of service.

NIC1/NIC2 (LAN1/LAN2)

The NIC (Network Interface Controller) LED connection for LAN port 1 is located on pins 11 and 12 of JF1, and LAN port 2 is on pins 9 and 10. Attach the NIC LED cables here to display network activity. Refer to the table below for pin definitions.

LAN1/LAN2 LED Pin Definitions (JF1)	
Pin#	Definition
9	NIC 2 Activity LED
10	3.3V Stdby
11	NIC 1 Activity LED
12	3.3V Stdby

HDD LED

The HDD LED connection is located on pins 13 and 14 of JF1. Attach a cable to pin 14 to show hard drive activity status. Refer to the table below for pin definitions.

HDD LED Pin Definitions (JF1)	
Pins	Definition
13	3.3V Stdby
14	HDD Active

Power LED

The Power LED connection is located on pins 15 and 16 of JF1. Refer to the table below for pin definitions.

Power LED Pin Definitions (JF1)	
Pins	Definition
15	3.3V
16	PWR LED

NMI Button

The non-maskable interrupt (NMI) button header is located on pins 19 and 20 of JF1. Refer to the table below for pin definitions.

NMI Button Pin Definitions (JF1)	
Pins	Definition
19	Control
20	Ground

4.3 Ports and Headers

Rear I/O Ports

See the figure below for the locations and descriptions of the various I/O ports on the rear of the motherboard.

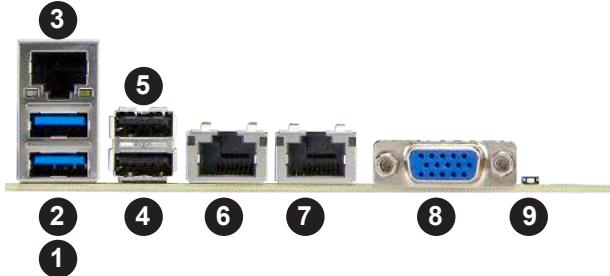


Figure 4-2. Rear I/O Ports

I/O Back Panel Port Descriptions			
Pin#	Definition	Pin#	Definition
1	USB 1 (3.0)	6	LAN Port 1
2	USB 2 (3.0)	7	LAN Port 2
3	IPMI_LAN	8	VGA
4	USB 3 (3.0)	9	UID Switch
5	USB 4 (3.0)		

Ethernet Ports

Two Ethernet ports (LAN1, LAN2) that support 10 GbE LAN connections are located on the I/O backplane. Additionally, a dedicated IPMI LAN, supported by the ACT2500 Baseboard Controller (BMC), is located above USB 1/2 ports on the backplane. The IPMI LAN supports 1 GbE Connection. All these Ethernet LAN ports accept RJ45 type cables. Please refer to the LED Indicator Section for LAN LED information.

VGA Port

The onboard VGA port is located next to LAN Port 2 on the I/O back panel. Use this connection for VGA display.

Universal Serial Bus (USB) Ports

Four USB 3.0 ports (USB1/2, 3/4) are located on the I/O back panel. A Type A internal USB 2.0 header provides front access. Connect appropriate cables here to use USB support. (USB cables are not included).

Back Panel USB (3.0) Pin Definitions			
Pin#	Definition	Pin#	Definition
1	VBUS	10	Power
2	D-	11	USB 2.0 Differential Pair
3	D+	12	
4	Ground	13	Ground of PWR Return
5	StdA_SSRX-	14	SuperSpeed Receiver
6	StdA_SSRX+	15	Differential Pair
7	GND_DRAIN	16	Ground for Signal Return
8	StdA_SSTX-	17	SuperSpeed Transmitter
9	StdA_SSTX+	18	Differential Pair

USB (2.0) Pin Definitions	
Pin#	Definition
1	Vcc
2	Data-
3	Data+
4	Ground

Unit Identifier Switch/UID LED Indicator

A Unit Identifier (UID) switch and a rear UID LED (LE1) are located on the I/O back panel. A front UID switch is located on pins 7 & 8 of the front panel control (JF1). When you press the front or the rear UID switch, both front and rear UID LEDs will be turned on. Press the UID switch again to turn off the LED indicators. The UID indicators provide easy identification of a system that may be in need of service. (Note: UID can also be triggered via IPMI on the motherboard. For more information, please refer to the IPMI User's Guide posted on our website at <http://www.supermicro.com>.)

UID Switch Pin Definitions	
Pin#	Definition
1	Ground
2	Ground
3	Button In
4	Button In

UID LED Pin Definitions	
Color	Status
Blue: On	Unit Identified

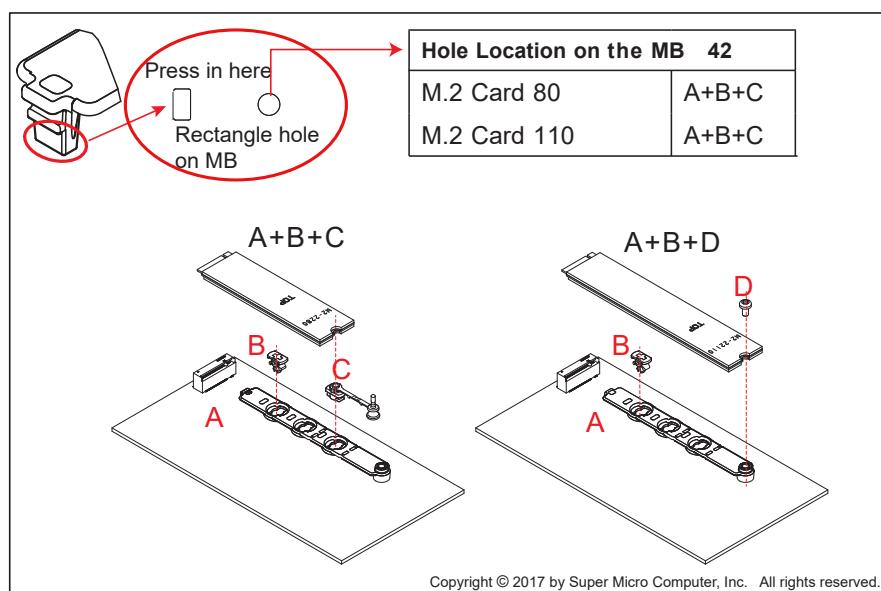
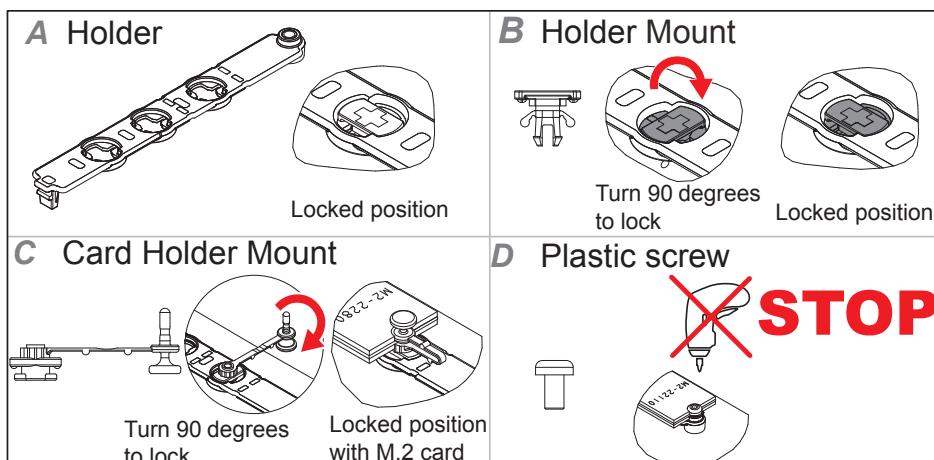
Connectors

(I)-SATA 3.0 and (S)-SATA 3.0 Connections

This motherboard contains one (I)-SATA 3.0 port (SATA5) and two S-SATA 3.0 headers (JS1/JS2). The S-SATA headers, supported by Intel SCU, provide eight S-SATA connections (S-SATA 0-3, 4-7), while SATA5, supported by the Intel PCH, can be used with the Supermicro SuperDOM. SuperDOM, a yellow SATA DOM connector with a power pin built in, does not require a power cable and is backward-compatible with a regular SATA HDD or a third-party SATA DOM that requires external power cables for power supply.

PCI-E M.2 Slot

This motherboard has one PCI-E M.2 slot. M.2 was formerly Next Generation Form Factor (NGFF) and serves to replace mini PCI-E. M.2 allows for a variety of card sizes with increased functionality and spatial efficiency. The M.2 socket on the motherboard supports PCI-E 3.0 x4 (32 Gb/s) SSD cards in the 2280 and 22110 form factors.



Headers

Onboard Fan Headers

There are ten onboard fan headers (FAN1-FAN10) located on the motherboard. FAN1-FAN8 are used for system cooling, whereas FAN9/FAN10 are used as Active CPU heatsink fans. All onboard fans are 4-pin fan headers, which are backward compatible with traditional 3-pin fans. However, only 4-pin fans can support fan speed control (via Hardware Monitoring in the BMC). Please use either all 3-pin fans or all 4-pin fans on the same motherboard.

Fan Header Pin Definitions	
Pin#	Definition
1	Ground (Black)
2	+12V (Red)
3	Tachometer
4	PWM Control

TPM Header

The JTPM1 header is used to connect a Trusted Platform Module (TPM)/Port 80, which is available from a third-party vendor. The TPM/Port 80 connector is a security device that supports encryption and authentication in hard drives. It allows the motherboard to deny access if the TPM associated with the hard drive is not installed in the system. See the table below for pin definitions.

Trusted Platform Module/Port 80 Header Pin Definitions			
Pin#	Definition	Pin#	Definition
1	P3V3	2	SPI TPM CS_N
3	PCI_RESET_N	4	SPI_PCH_MISO
5	SPI_PCH_CLK	6	Ground
7	SPI_PCH_MOSI	8	x
9	P3V3_STBY	10	IRQ TPM SPI_N

RAID Key Header

A RAID Key header is located at JRK1 on the motherboard. RAID key is used to support NVMe SDD. Please refer to the layout below for JRK1 location.

RAID Key Header Pin Definitions	
Pin#	Definition
1	Ground
2	P3V3_STBY
3	Ground
4	RAID_KEY

Chassis Intrusion

A Chassis Intrusion header is located at JL1 on the motherboard. Attach the appropriate cable from the chassis to inform you of a chassis intrusion when the chassis is opened. Refer to the table below for pin definitions.

Chassis Intrusion Pin Definitions	
Pin#	Definition
1	Intrusion Input
2	Ground

Serial Port

A serial port header is located at COM1 on the motherboard. This header provides serial communication support for your system. See the table below for pin definitions.

Serial Port Pin Definitions			
Pin#	Definition	Pin#	Definition
1	DCD	6	DSR
2	RXD	7	RTS
3	TXD	8	CTS
4	DTR	9	RI
5	Ground	10	N/A

System Management Bus (SMBus) Header

A SMBus (I²C) header for IPMI 2.0 is located at JIPMB1. Connect the appropriate cable here to use the IPMB I²C connection on your system. Refer to the table below for pin definitions.

External I ² C Header Pin Definitions	
Pin#	Definition
1	Data
2	Ground
3	Clock
4	No Connection

NVMe SMBus Headers

NVMe SMBus (I²C) headers (JNVI²C1/JNVI²C2), used for PCI-E SMBus clock and data connections, provide hot-plug support via dedicated SMBus interface. This feature is only available for a Supermicro complete system with an SMCI-proprietary NVMe add-on card and cable installed for each NVMe SMBus header supported. See the table below for pin definitions.

NVMe SMBus Header Pin Definitions	
Pin#	Definition
1	Data
2	Ground
3	Clock
4	VCCIO

Host Fabric Interface (HFI) Carrier Card Sideband Headers (Available when F model CPUs are used)

Two Host Fabric Interface (HFI) carrier card headers are located at JHFI1/JHFI2 on the motherboard. These headers will only work properly when an F model processor is installed on the the corresponding CPU socket (JHFI1: CPU1, JHFI2: CPU2). After ensuring that an F model CPU is properly seated in the corresponding CPU socket, please use an HFI sideband cable to connect the carrier card to the JHFI header, and then use an optional IFP (Internal-Faceplate-to-Processor) cable to connect the carrier card to the F model CPU that is seated in the Socket. (See Note below). See page 34 for the installation instructions.

Notes:

1. For an HFI carrier card to function properly, please purchase the appropriate IFP cable from Supermicro. Please refer to Supermicro's website at www.supermicro.com for the part number of the IFP cable specified for your system.
2. JHFI1 (when an F model CPU is installed in CPU Socket1)
3. JHFI2 (when an F model CPU is installed in CPU Socket2)

4.4 Jumpers

Explanation of Jumpers

To modify the operation of the motherboard, jumpers are used to choose between optional settings. Jumpers create shorts between two pins to change the function associated with it. Pin 1 is identified with a square solder pad on the printed circuit board. See the motherboard layout page for jumper locations.

Note: On a two-pin jumper, "Closed" means the jumper is on both pins and "Open" indicates the jumper is either on only one pin or has been completely removed.

CMOS Clear

JBT1 is used to clear CMOS, which will also clear any passwords. Instead of pins, this jumper consists of contact pads to prevent accidentally clearing the contents of CMOS.

To Clear CMOS

1. First power down the system and unplug the power cord(s).
2. Remove the cover of the chassis to access the motherboard.
3. Remove the onboard battery from the motherboard.
4. Short the CMOS pads with a metal object such as a small screwdriver for at least four seconds.
5. Remove the screwdriver (or shorting device).
6. Replace the cover, reconnect the power cord(s) and power on the system.

Notes: Clearing CMOS will also clear all passwords.

Do not use the PW_ON connector to clear CMOS.



VGA Port Enable/Disable

Jumper JPG1 is used to enable or disable the VGA port on the I/O back panel. Close pin 1 and pin 2 for VGA support. The default setting is Enabled.

VGA Port Enable/Disable Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

Manufacturing Mode Select

Close JPME1 to bypass SPI flash security and force the system to use the Manufacturing Mode, which will allow you to flash the system firmware from a host server to modify system settings. See the table below for jumper settings.

Manufacturing Mode Select Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Normal (Default)
Pins 2-3	Manufacturing Mode

Watch Dog

JWD1 controls the Watch Dog function. Watch Dog is a monitor that can reboot the system when a software application hangs. Jumping pins 1-2 will cause Watch Dog to reset the system if an application hangs. Jumping pins 2-3 will generate a non-maskable interrupt signal for the application that hangs. Watch Dog must also be enabled in BIOS. The default setting is Reset.

Note: When Watch Dog is enabled, the user needs to write their own application software to disable it.

Watch Dog Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Reset
Pins 2-3	NMI
Open	Disabled

I²C Bus for VRM

Jumpers JVRM1 and JVRM2 allow the VRM SMB Clock and Data to access the Baseboard Management Controller (BMC). See the tables below for jumper settings.

JVRM1 (VRM SMB Clock to BMC/PCH) Jumper Settings	
Jumper Setting	Definition
Pins 1-2	SMB Clock to BMC (Normal: Default)
Pins 2-3	SMB Clock to PCH

JVRM2 (VRM SMB Data to BMC/PCH) Jumper Settings	
Jumper Setting	Definition
Pins 1-2	SMB Data to BMC (Normal: Default)
Pins 2-3	SMB Data to PCH

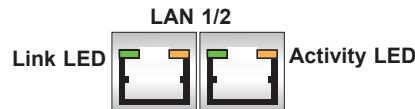
4.5 LED Indicators

LAN LEDs

The LAN ports are located on the IO back panel of the motherboard. Each Ethernet LAN port has two LEDs. The yellow LED indicates activity. The Link LED, located on the left side of the LAN port, may be green, amber or off indicating the speed of the connection. See the tables below for more information.

GLAN Activity Indicator (Right) LED Settings		
Color	State	Definition
Yellow	Flashing	Active

LAN Link Indicator LED Settings	
LED Color	Definition
Off	No Connection, 10 or 100 Mbps
Green	10 Gbps
Amber	1 Gbps



Dedicated IPMI LAN LEDs

In addition to LAN 1/LAN 2, a dedicated IPMI LAN is located on the I/O back panel of the motherboard. The amber LED on the right indicates activity, while the green LED on the left indicates the speed of the connection. See the table below for more information.

IPMI LAN Link LED (Left) & Activity LED (Right)		
Color	State	Definition
Link (Left)	Green: Solid Amber: Solid	100 Mbps 1 Gbps
Activity (Right)	Yellow: Blinking	Active



BMC Heartbeat LED

The BMC heartbeat LED is located at LED1. When this LED is blinking green, BMC is functioning normally. See the table below for the LED status.

BMC Heartbeat LED Indicator	
LED Color	Definition
Green: Blinking	BMC Normal

Onboard Power LED

The Onboard Power LED is located at LE2 on the motherboard. When this LED is on, the system is on. Be sure to turn off the system and unplug the power cord before removing or installing components. Refer to the table below for more information.

Onboard Power LED Indicator	
LED Color	Definition
Off	System Off (power cable not connected)
Green	System On

M.2 Power LED

The Power LED indicator for the M.2 device is located at LED1 on the motherboard. When this LED is on, the M.2 power is on. Refer to the table below for more information.

M.2 Power LED Indicator	
LED	Definition
Off	M.2 Power Off
On	M.2 Power On

Chapter 5

Software

After the hardware has been installed, you can install the Operating System (OS), configure RAID settings and install the drivers.

5.1 Microsoft Windows OS Installation

If you will be using RAID, you must configure RAID settings before installing the Windows OS and the RAID driver. Refer to the RAID Configuration User Guides posted on our website at www.supermicro.com/support/manuals.

Installing the OS

1. Create a method to access the MS Windows installation ISO file. That might be a DVD, perhaps using an external USB/SATA DVD drive, or a USB flash drive, or the IPMI KVM console.
2. Retrieve the proper RST/RSTe driver. Go to the Supermicro web page for your motherboard and click on "Download the Latest Drivers and Utilities", select the proper driver, and copy it to a USB flash drive.
3. Boot from a bootable device with Windows OS installation. You can see a bootable device list by clicking **F11** during the system startup.

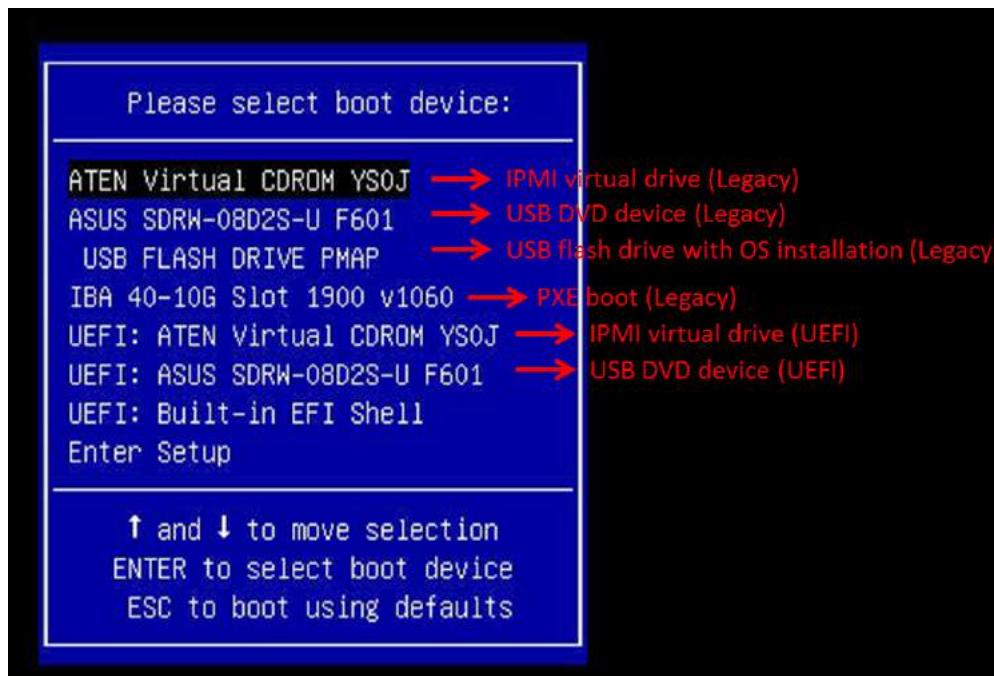


Figure 5-1. Select Boot Device

4. During Windows Setup, continue to the dialog where you select the drives on which to install Windows. If the disk you want to use is not listed, click on “Load driver” link at the bottom left corner.

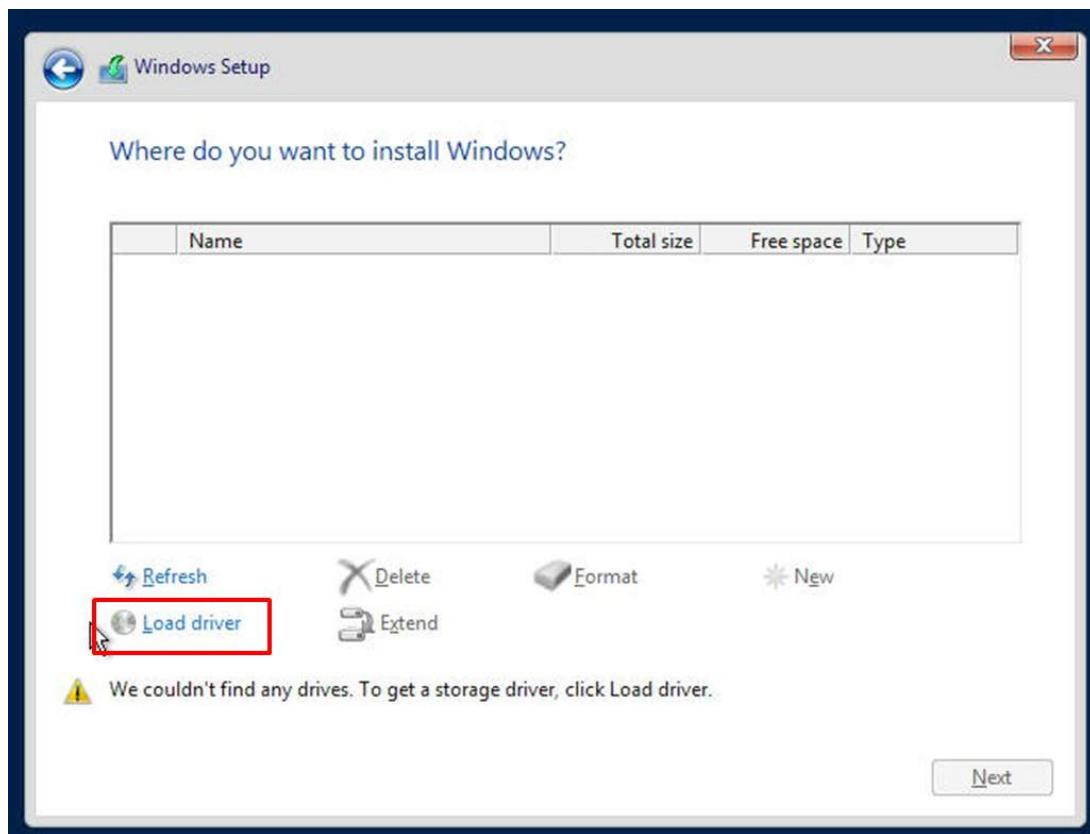


Figure 5-2. Load Driver Link

To load the driver, browse the USB flash drive for the proper driver files.

- For RAID, choose the SATA/sSATA RAID driver indicated then choose the storage drive on which you want to install it.
- For non-RAID, choose the SATA/sSATA AHCI driver indicated then choose the storage drive on which you want to install it.

5. Once all devices are specified, continue with the installation.
6. After the Windows OS installation has completed, the system will automatically reboot multiple times.

5.2 Driver Installation

The Supermicro website contains drivers and utilities for your system at <https://www.supermicro.com/wftp/driver>. Some of these must be installed, such as the chipset driver.

After accessing the website, go into the CDR_Images (in the parent directory of the above link) and locate the ISO file for your motherboard. Download this file to a USB flash drive or a DVD. (You may also use a utility to extract the ISO file if preferred.)

Another option is to go to the Supermicro website at <http://www.supermicro.com/products/>. Find the product page for your motherboard, and "Download the Latest Drivers and Utilities". Insert the flash drive or disk and the screenshot shown below should appear.

Figure 5-1. Driver & Tool Installation Screen

Note: Click the icons showing a hand writing on paper to view the readme files for each item. Click the computer icons to the right of these items to install each item (from top to the bottom) one at a time. **After installing each item, you must re-boot the system before moving on to the next item on the list.** The bottom icon with a CD on it allows you to view the entire contents.

5.3 SuperDoctor® 5

The Supermicro SuperDoctor 5 is a program that functions in a command-line or web-based interface for Windows and Linux operating systems. The program monitors such system health information as CPU temperature, system voltages, system power consumption, fan speed, and provides alerts via email or Simple Network Management Protocol (SNMP).

SuperDoctor 5 comes in local and remote management versions and can be used with Nagios to maximize your system monitoring needs. With SuperDoctor 5 Management Server (SSM Server), you can remotely control power on/off and reset chassis intrusion for multiple systems with SuperDoctor 5 or IPMI. SuperDoctor 5 Management Server monitors HTTP, FTP, and SMTP services to optimize the efficiency of your operation.

Note: The default User Name and Password for SuperDoctor 5 is ADMIN / ADMIN.

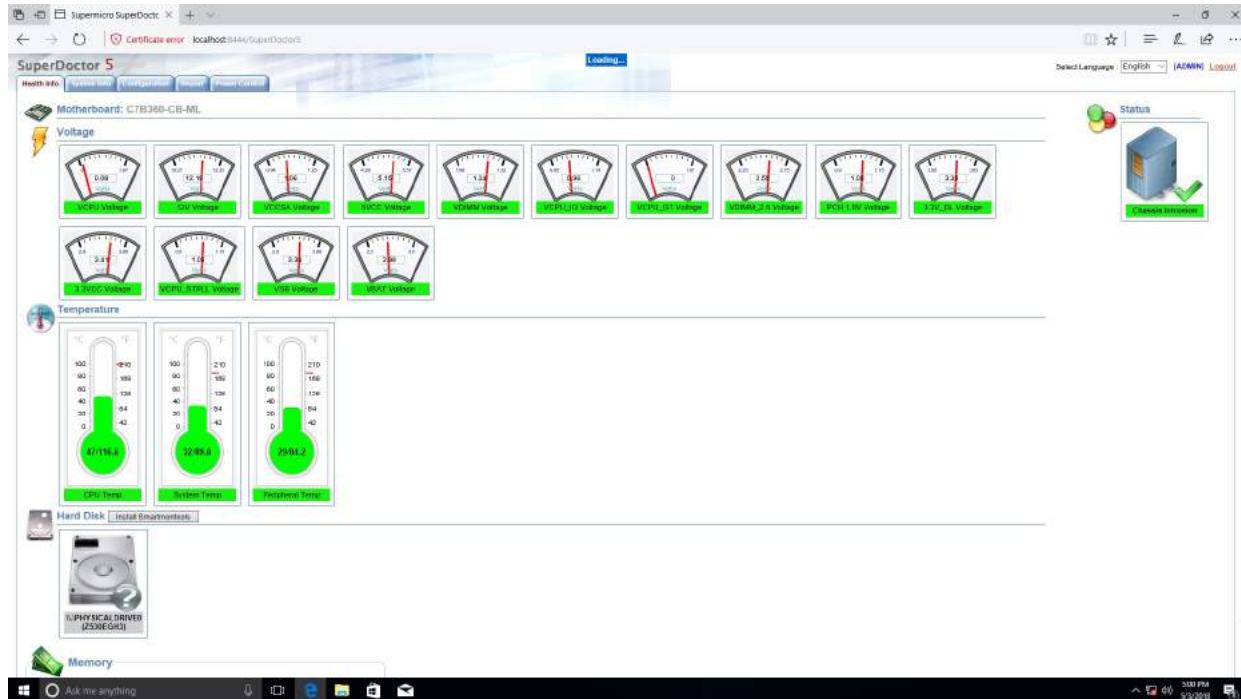


Figure 5-2. SuperDoctor 5 Interface Display Screen (Health Information)

5.4 IPMI

The X11DPG-O supports the Intelligent Platform Management Interface (IPMI). IPMI is used to provide remote access, monitoring and management. There are several BIOS settings that are related to IPMI.

For general documentation and information on IPMI, please visit our website at: <http://www.supermicro.com/products/info/IPMI.cfm>.

Chapter 6

UEFI BIOS

6.1 Introduction

This chapter describes the UEFI Setup utility for the X11DPG-O motherboard(s). The BIOS is stored on a chip and can be easily upgraded using a flash program.

Note: Due to periodic changes to the BIOS, some settings may have been added or deleted and might not yet be recorded in this manual. Please refer to the Manual Download area of our website for any changes to BIOS that may not be reflected in this manual.

Starting the Setup Utility

To enter the UEFI BIOS Setup Utility, hit the <Delete> key while the system is booting-up. (In most cases, the <Delete> key is used to invoke the BIOS setup screen. There are a few cases when other keys are used, such as <F1>, <F2>, etc.) Each main BIOS menu option is described in this manual.

The Main BIOS screen has two main frames. The left frame displays all the options that can be configured. “Grayed-out” options cannot be configured. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it. (Note that BIOS has default text messages built in. We retain the option to include, omit, or change any of these text messages.) Settings printed in **Bold** are the default values.

A "►" indicates a submenu. Highlighting such an item and pressing the <Enter> key will open the list of settings within that submenu.

The BIOS setup utility uses a key-based navigation system called hot keys. Most of these hot keys (<F1>, <F10>, <Enter>, <ESC>, <Arrow> keys, etc.) can be used at any time during the setup navigation process.

6.2 Main Setup

When you first enter the UEFI BIOS setup utility, you will enter the Main Setup screen. You can always return to the Main setup screen by selecting the Main tab on the top of the screen. The Main BIOS setup screen is shown below. The following Main menu items will be displayed:



System Date/System Time

Use this feature to change the system date and time. Highlight *System Date* or *System Time* using the arrow keys on the keyboard. Press the **<Tab>** key or the arrow keys to move between fields. The date must be entered in Day MM/DD/YYYY format. The time is entered in HH:MM:SS format.

Note: The time is in the 24-hour format. For example, 5:30 P.M. appears as 17:30:00. The date's default value is 01/01/2014 after RTC reset.

Supermicro X11DPH-Tq

BIOS Version

This item displays the version of the BIOS ROM used in the system.

Build Date

This item displays the date when the version of the BIOS ROM used in the system was built.

CPLD Version

This item displays the version of the CPLD (Complex-Programmable Logical Device) used in the system.

Memory Information

Total Memory

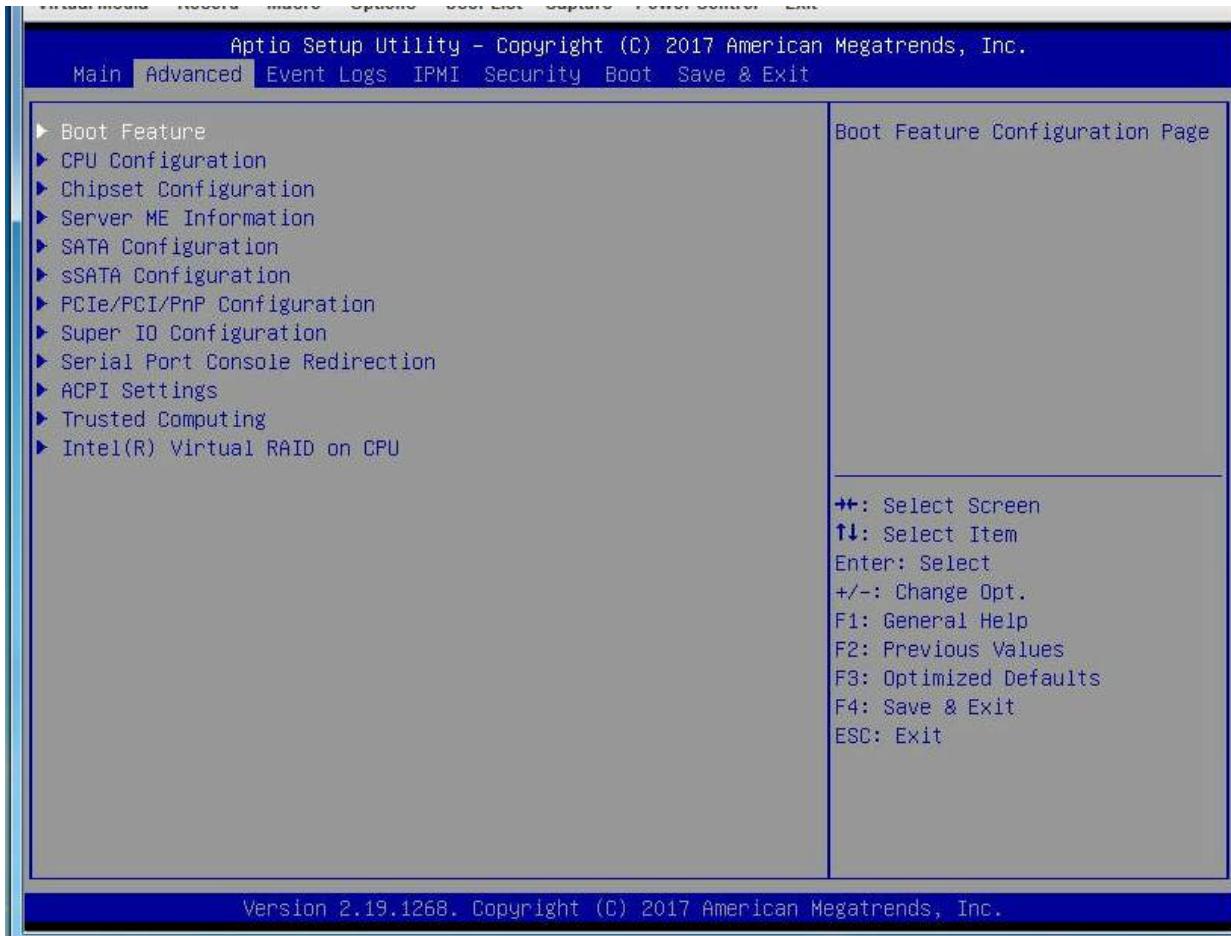
This item displays the total size of memory available in the system.

Memory Speed

This item displays the default speed of the memory modules installed in the system.

6.3 Advanced Setup Configurations

Use the arrow keys to select the Advanced submenu and press <Enter> to access the submenu items.



Warning: Take Caution when changing the Advanced settings. An incorrect value, an incorrect DRAM frequency, or an incorrect BIOS timing setting may cause the system to malfunction. When this occurs, restore the setting to the manufacture default setting.

▶ Boot Feature

Quiet Boot

Use this feature to select the screen between displaying POST messages or the OEM logo at bootup. Select Disabled to display the POST messages. Select Enabled to display the OEM logo instead of the normal POST messages. The options are **Enabled** and **Disabled**.

Note: POST message is always displayed regardless of the item setting.

Option ROM Messages

Use this feature to set the display mode for the Option ROM. Select Keep Current to use the current AddOn ROM display setting. Select Force BIOS to use the Option ROM display mode set by the system BIOS. The options are **Force BIOS** and **Keep Current**.

Bootup NumLock State

Use this feature to set the Power-on state for the Numlock key. The options are **Off** and **On**.

Wait For 'F1' If Error

Select Enabled to force the system to wait until the 'F1' key is pressed if an error occurs. The options are **Disabled** and **Enabled**.

INT19 Trap Response

Interrupt 19 is the software interrupt that handles the boot disk function. When this item is set to **Immediate**, the ROM BIOS of the host adaptors will "capture" Interrupt 19 at bootup immediately and allow the drives that are attached to these host adaptors to function as bootable disks. If this item is set to **Postponed**, the ROM BIOS of the host adaptors will not capture Interrupt 19 immediately and allow the drives attached to these adaptors to function as bootable devices at bootup. The options are **Immediate** and **Postponed**.

Re-try Boot

When **EFI** (Expansible Firmware Interface) Boot is selected, the system BIOS will automatically reboot the system from an EFI boot device after an initial boot failure. Select **Legacy Boot** to allow the BIOS to automatically reboot the system from a Legacy boot device after an initial boot failure. The options are **Disabled**, **Legacy Boot**, and **EFI Boot**.

Power Configuration

Watch Dog Function

Select Enabled to allow the Watch Dog timer to reboot the system when it is inactive for more than 5 minutes. The options are **Enabled** and **Disabled**.

Power Button Function

This feature controls how the system shuts down when the power button is pressed. Select **4 Seconds Override** for the user to power off the system after pressing and holding the power button for 4 seconds or longer. Select **Instant Off** to instantly power off the system as soon as the user presses the power button. The options are **4 Seconds Override** and **Instant Off**.

Restore on AC Power Loss

Use this feature to set the power state after a power outage. Select **Power-Off** for the system power to remain off after a power loss. Select **Power-On** for the system power to be turned on after a power loss. Select **Last State** to allow the system to resume its last power state before a power loss. The options are **Stay-Off**, **Power-On**, and **Last State**.

►CPU Configuration

Warning: Setting the wrong values in the following sections may cause the system to malfunction.

►Processor Configuration

The following CPU information will be displayed:

- Processor BSP Revision
- Processor Socket
- Processor ID
- Processor Frequency
- Processor Max Ratio
- Processor Min Ratio
- Microcode Revision
- L1 Cache RAM
- L2 Cache RAM
- L3 Cache RAM
- Processor 0 Version/Processor 1 Version

Trace Hub ACPIBAR Base

Select Enable to set PCH_TRACE_HUB_FW_BASE_ADDRESS in the MSR_TRACE_HUB_8TH_ACPIBAR_BASE (MSR 80h). The options are **Disable** and **Enable**.

Hyper-Threading (ALL)

Select Enable to use Intel Hyper-Threading Technology to enhance CPU performance. The options are **Enable** and **Disable**.

Core Disable Bitmap (Hex)

Use this feature to enable or disable CPU cores in the processor specified by the user. Enter 0 to enable all cores available in the processor. Enter FFFFFF to disable all CPU cores in the processor. Enter bit0 to disable CPU Core 0. Enter bit1 to disable CPU Core 1. Please note that the maximum of 24 CPU cores are currently available in each CPU package. The default setting is **0**.

Intel Enhanced Debug

This feature allows the user to determine how Intel Enhanced Debug support to be implemented in this system. The options are **4096K** and **Disable**.

IED Trace Memory

Use this feature to determine how the system should allocate memory for PSMI trace. The options are **Disable**, 4M, 8M, 16M, 32M, 64M, 128M, 256M, 512M, and 1G.

Execute Disable Bit (Available if supported by the OS & the CPU)

Select Enable to enable Execute Disable Bit support which will allow the processor to designate areas in the system memory where an application code can execute and where it cannot, thus preventing a worm or a virus from flooding illegal codes to overwhelm the processor, damaging the system during a virus attack. The options are **Enable** and **Disable**. (Refer to Intel and Microsoft websites for more information.)

Intel Virtualization Technology

Select Enable to use Intel Virtualization Technology which will allow the I/O device assignments to be directly reported to the VMM (Virtual Memory Management) through the DMAR ACPI tables. This feature offers fully-protected I/O resource-sharing across the Intel platforms, providing the user with greater reliability, security and availability in networking and data-sharing. The settings are **Enable** and **Disable**.

PPIN Control

Select Unlock/Enable to use the Protected-Processor Inventory Number (PPIN) in the system. The options are **Unlock/Enable** and **Unlock/Disable**.

Hardware Prefetcher (Available when supported by the CPU)

If this feature is set to Enable, the hardware prefetcher will prefetch streams of data and instructions from the main memory to the Level 2 (L2) cache to improve CPU performance. The options are **Disable** and **Enable**.

Adjacent Cache Prefetch (Available when supported by the CPU)

Select Enable for the CPU to prefetch both cache lines for 128 bytes as comprised. Select Disable for the CPU to prefetch both cache lines for 64 bytes. The options are **Disable** and **Enable**.

Note: Please power off and reboot the system for the changes you've made to take effect. Please refer to Intel's website for detailed information.

DCU Streamer Prefetcher (Available when supported by the CPU)

If this feature is set to Enable, the DCU (Data Cache Unit) streamer prefetcher will prefetch data streams from the cache memory to the DCU (Data Cache Unit) to speed up data accessing and processing for CPU performance enhancement. The options are **Disable** and **Enable**.

DCU IP Prefetcher

If this feature is set to Enable, the IP prefetcher in the DCU (Data Cache Unit) will prefetch IP addresses to improve network connectivity and system performance. The options are **Enable** and **Disable**.

LLC Prefetch

If this feature is set to Enable, LLC (hardware cache) prefetching on all threads will be supported. The options are **Disable** and **Enable**.

Extended APIC (Extended Advanced Programmable Interrupt Controller)

Based on the Intel Hyper-Threading technology, each logical processor (thread) is assigned 256 APIC IDs (APIDs) in 8-bit bandwidth. When this feature is set to Enable, the APIC ID will be expanded from 8 bits to 16 bits to provide 512 APIDs to each thread to enhance CPU performance. The options are **Disable** and **Enable**.

AES-NI

Select Enable to use the Intel Advanced Encryption Standard (AES) New Instructions (NI) to ensure data security. The options are **Enable** and **Disable**.

►Advanced Power Management Configuration

Power Technology

Select Energy Efficient to support power-saving mode. Select Custom to customize system power settings. Select Disabled to disable power-saving settings. The options are **Disable**, **Energy Efficient**, and **Custom**.

Power Performance Tuning (Available when "Power Technology" is set to Custom)

Select BIOS to allow the system BIOS to configure the Power-Performance Tuning Bias setting below. The options are **BIOS Controls EPB** and **OS Controls EPB**.

ENERGY_PERF_BIAS_CFG Mode (ENERGY PERFORMANCE BIAS CONFIGURATION Mode) (Available when "Power Performance Tuning" is set to BIOS Controls EPB)

This feature is internally used by the processor to control power settings vs performance features including Energy Efficient P-State/Demotion/Undemotion etc. The options are **Maximum Performance**, **Performance**, **Balanced Performance**, **Balanced Power**, **Power**, and **Max Power Efficient**.

►CPU P State Control (Available when "Power Technology" is set to Custom)

SpeedStep (PStates)

EIST (Enhanced Intel SpeedStep Technology) allows the system to automatically adjust processor voltage and core frequency in an effort to reduce power consumption and heat dissipation. Please refer to Intel's website for detailed information. The options are **Disable** and **Enable**.

EIST PSD Function (Available when "SpeedStep" is set to Enable)

Use this feature to configure the processor's P-State coordination settings. During a P-State, the voltage and frequency of the processor will be reduced when it is in operation. This makes the processor more energy efficient, resulting in further energy gains. The options are **HW_ALL**, **SW_ALL** and **SW-ANY**.

Turbo Mode (Available when "SpeedStep" is set to Enable)

Select **Enable** for processor cores to run faster than the frequency specified by the manufacturer. The options are **Disable** and **Enable**.

►Hardware PM (Power Management) State Control (Available when "Power Technology" is set to Custom)

Hardware P-States

If this feature is set to **Disable**, hardware will choose a P-state setting for the system based on an OS request. If this feature is set to **Native Mode**, hardware will choose a P-state setting based on OS guidance. If this feature is set to **Native Mode with No Legacy Support**, hardware will choose a P-state setting independently without OS guidance. The options are **Disable**, **Native Mode**, **Out of Band Mode**, and **Native Mode with No Legacy Support**.

►CPU C State Control (Available when "Power Technology" is set to Custom)

Autonomous Core C-State

Select **Enable** to support Autonomous Core C-State control which will allow the processor core to control its C-State setting automatically and independently. The options are **Enable** and **Disable**.

CPU C6 Report

Select **Enable** to allow the BIOS to report the CPU C6 state (ACPI C3) to the operating system. During the CPU C6 state, power to all caches is turned off. The options are **Auto**, **Enable**, and **Disable**.

Enhanced Halt State (C1E)

Select **Enable** to enable "Enhanced Halt State" support, which will significantly reduce the CPU's power consumption by minimizing CPU's clock cycles and reduce voltage during a "Halt State." The options are **Disable** and **Enable**.

► Package C State Control (Available when "Power Technology" is set to Custom)

Package C State

Use this feature to set the limit on the C-State package register. The options are C0/1 state, C2 state, C6 (non-Retention) state, C6 (Retention) state, No Limit, and **Auto**.

► CPU T State Control

Software Controlled T-States

If this feature is set to **Enable**, CPU throttling settings will be supported by the software of the system. The options are **Disable** and **Enable**.

► Chipset Configuration

Warning: Setting the wrong values in the following sections may cause the system to malfunction.

► North Bridge

This feature allows the user to configure the settings for the Intel North Bridge.

► UPI (Ultra Path Interconnect) Configuration

This section displays the following UPI General Configuration information:

- Number of CPU
- Number of IIO
- Current UPI Link Speed
- Current UPI Link Frequency
- UPI Global MMIO Low Base/Limit
- UPI Global MMIO High Base/Limit
- UPI PCI-E Configuration Base/Size

Degrade Precedence

Use this feature to select the degrading precedence option for Ultra Path Interconnect connections. Select Topology Precedent to degrade UPI features if system options are in conflict. Select Feature Precedent to degrade UPI topology if system options are in conflict. The options are **Topology Precedence** and Feature Precedence.

Link L0p Enable

Select Enable to enable Link L0p. The options are Disable, Enable, and **Auto**.

Link L1 Enable

Select Enable to enable Link L1 (Level 1 link). The options are Disable, Enable, and **Auto**.

IO Directory Cache (IODC)

Select Enable for the IODC (I/O Directory Cache) to generate snoops instead of generating memory lockups for remote IIO (InvlToM) and/or WCiLF (Cores). Select Auto for the IODC to generate snoops (instead of memory lockups) for WCiLF (Cores). The options are Disable, **Auto**, Enable for Remote InvlToM Hybrid Push, InvlToM AllocFlow, Enable for Remote InvlToM Hybrid AllocNonAlloc, and Enable for Remote InvlToM and Remote WViLF.

SNC

Select Enable to use the feature of "Secure Network Communications" (SNC), which supports full SNC (2-cluster) interleave and 1-way IMC interleave. Select Auto for 1-cluster or 2-cluster support depending on the satuts of IMC (Integrated Memory Controller) Interleaving. The options are **Disable**, Enable, and Auto.

XPT Prefetch

Select Enable to support XPT Prefetching to enhance system performance. The options are Enable and **Disable**.

KTI Prefetch

Select Enable to support KTI Prefetching to enhance system performance. The options are **Enable** and Disable.

Local/Remote Threshold

Use this feature to configure the threshold settings for local and remote systems that are connected in the network. The options are Disable, **Auto**, Low, Medium, and High.

Stale AtoS (A to S)

Select Enable to remove the contents and the structures of the files that are no longer needed in the remote host server but are still in use by the local client machine from Directory A to Directory S in the NFS (Network File System) to optimize system performance. The options are **Disable**, Enable, and Auto.

LLC Dead Line Alloc

Select Enable to opportunistically fill the deadlines in LLC. The options are **Enable**, **Disable**, and **Auto**.

Isoc Mode

Select Enable to enable Isochronous support to meet QoS (Quality of Service) requirements. This feature is especially important for Virtualization Technology. The options are **Disable**, **Enable**, and **Auto**.

►Memory Configuration

Enforce POR

Select POR to enforce POR restrictions for DDR4 memory frequency and voltage programming. The options are **POR** and **Disable**.

Memory Frequency

Use this feature to set the maximum memory frequency for onboard memory modules. The options are **Auto**, 1866, 2000, 2133, 2200, 2400, 2600, and 2666.

Data Scrambling for NVDIMM

Select Enable to enable data scrambling for onboard NVDIMM memory to enhance system performance and security. The options are **Auto**, **Disable**, and **Enable**.

Data Scrambling for DDR4

Select Enable to enable data scrambling for DDR4 memory to enhance system performance and security. The options are **Auto**, **Disable**, and **Enable**.

tCCD_L Relaxation

If this feature is enabled, SPD (Serial Presence Detect) will override tCCD_L ("Column to Column Delay-Long", or "Command to Command Delay-Long" on the column side.) If this feature is set to **Disable**, tCCD_L will be enforced based on the memory frequency. The options are **Disable** and **Auto**.

Memory tRWSR (Read to Write turnaround time for Same Rank) Relaxation

Select **Enable** to use the same tRWSR DDR timing setting among all memory channels, in which case, the worst case value among all channels will be used. Select **Disable** to use different values for the tRWSR DDR timing settings for different channels as trained. The options are **Disable** and **Enable**.

Enable ADR

Select **Enable** for ADR (Automatic Diagnostic Repository) support to enhance memory performance. The options are **Enable** and **Disable**.

2X Refresh

Select Enable for memory 2X refresh support to enhance memory performance. The options are Enable and **Auto**.

Page Policy

Use this feature to set the page policy for onboard memory support. The options are Closed, Adaptive and **Auto**.

IMC Interleaving

Use this feature to configure interleaving settings for the IMC (Integrated Memory Controller), which will improve memory performance. The options are 1-way Interleaving, 2-way Interleaving, and **Auto**.

►Memory Topology

This item displays the information of onboard memory modules as detected by the BIOS.

- P1 DIMMA1/P1 DIMMA2/P1 DIMMB1/P1 DIMMC1/P1 DIMMD1/P1 DIMMD2/P1 DIMME1/P1 DIMMF1/
- P2 DIMMA1/P2 DIMMA2/P2 DIMMB1/P2 DIMMC1/P2 DIMMD1/P2 DIMMD2/P2 DIMME1/P2 DIMMF1

►Memory RAS (Reliability_Availability_Serviceability) Configuration

Use this submenu to configure the following Memory RAS settings.

Static Virtual Lockstep Mode

Select Enable to support Static Virtual Lockstep mode to enhance memory performance. The options are Enable and **Disable**.

Mirror Mode

Use this feature to configure the mirror mode settings for all 1LM/2LM memory modules installed in the system which will create a duplicate copy of data stored in the memory to increase memory security, but it will reduce the memory capacity into half. The options are **Disable**, Mirror Mode 1LM, and Mirror Mode 2LM.

UEFI ARM Mirror

If this feature is set to Enable, mirror mode configuration settings for UEFI-based Address Range memory will be activated upon system boot. This will create a duplicate copy of data stored in the memory to increase memory security, but it will reduce the memory capacity into half. The options are **Disable** and Enable.

Memory Rank Sparing

Select Enable to support memory-rank sparing to optimize memory performance. The options are **Enable** and **Disable**.

Note: This item will not be available when memory mirror mode is enabled.

Correctable Error Threshold

Use this item to enter the threshold value for correctable memory errors. The default setting is 10.

SDDC Plus One

Select Enable for SDDC (Single Device Data Correction) Plus One support, which will activate memory ECC mode for memory error checking and correction. It will also protect against memory failures caused by 'single-bit' errors in the same memory rank. The options are **Enable** and **Disable**. (**Note:** the default setting for the X11DPH-Tq is **Enable**.)

ADDDC (Adaptive Double Device Data Correction) Sparing

Select Enable for Adaptive Double Device Data Correction (ADDDC) support, which will not only provide memory error checking and correction but will also prevent the system from issuing a performance penalty before a device fails. Please note that virtual lockstep mode will only start to work for ADDDC after a faulty DRAM module is spared. The options are **Enable** and **Disable**. (**Note:** the default setting for the X11DPH-Tq is **Enable**.)

ADDDC Error Injection (Available for the X11DPH-Tq board only)

Error injection is a technique used to enhance memory error recovery. Select Enable to propagate error injection equally between the primary device and the buddy device (with half of the cache line to each device). Please be sure to enable this feature when ADDDC Sparing is set to Enable. The options are **Enable** and **Disable**. (**Note:** the default setting for the X11DPH-Tq is **Enable**.)

Patrol Scrub

Patrol Scrubbing is a process that allows the CPU to correct correctable memory errors detected in a memory module and send the corrections to the requestor (the original source). When this item is set to Enable, the IO hub will read and write back one cache line every 16K cycles if there is no delay caused by internal processing. By using this method, roughly 64 GB of memory behind the IO hub will be scrubbed every day. The options are **Enable** and **Disable**.

Patrol Scrub Interval

Use this item to specify the number of hours (between 0 to 24) required for the system to complete a full patrol scrubbing. Enter 0 for patrol scrubbing to be performed automatically. The default setting is **24**.

Note: This item is hidden when Patrol Scrub item is set to Disable.

►IIO Configuration

EV DFX (Device Function On-Hide) Features

When this feature is set to Enable, the EV_DFX Lock Bits that are located in a processor will always remain clear during electric tuning. The options are **Disable** and **Enable**.

►CPU1 Configuration/CPU2 Configuration

IOU0 (IIO PCIe Br1)

This feature configures the PCI-E Bifurcation setting for a PCI-E port specified by the user. The options are x4x4x4x4, x4x4x8, x8x4x4, x8x8, x16, and **Auto**.

IOU1 (IIO PCIe Br2)

This feature configures the PCI-E Bifurcation setting for a PCI-E port specified by the user. The options are x4x4x4x4, x4x4x8, x8x4x4, x8x8, x16, and **Auto**.

IOU2 (IIO PCIe Br3)

This feature configures the PCI-E Bifurcation setting for a PCI-E port specified by the user. The options are x4x4x4x4, x4x4x8, x8x4x4, x8x8, x16, and **Auto**.

MCP0 (IIO PCIe Br4)

This feature configures the PCI-E Bifurcation setting for a PCI-E port specified by the user. The options are x16 and **Auto**.

MCP1 (IIO PCIe Br5)

This feature configures the PCI-E Bifurcation setting for a PCI-E port specified by the user. The options are x16 and **Auto**.

►CPU1 PCI-E Br0D00F0 - Port 0/DMI (Available for CPU 1 Configuration only)

Link Speed

This feature configures the link speed of a PCI-E port specified by the user. The options are **Auto**, Gen 1 (Generation 1) (2.5 GT/s), Gen 2 (Generation 2) (5 GT/s), and Gen 3 (Generation 3) (8 GT/s)

The following information will be displayed as well:

- PCI-E Port Link Status
- PCI-E Port Link Max
- PCI-E Port Link Speed

PCI-E Port Clocking (Available for CPU 1 Configuration only)

Use this feature to configure port overclocking settings between the port specified above and downstream components. The options are **Distinct** and **Common**.

PCI-E Port Max (Maximum) Payload Size (Available for CPU 1 Configuration only)

Select Auto for the system BIOS to automatically set the maximum payload value for a PCI-E device specified by the user to enhance system performance. The options are **Auto**, 128B, and 256B.

►IOAT Configuration

Disable TPH (TLP Processing Hint)

TPH is used for data-tagging with a destination ID and a few important attributes. It can send critical data to a particular cache without writing through to memory. Select No in this item for TLP Processing Hint support, which will allow a "TPL request" to provide "hints" to help optimize the processing of each transaction occurred in the target memory space. The options are Yes and **No**.

Prioritize TPH (TLP Processing Hint)

Select Yes to prioritize the TPL requests that will allow the "hints" to be sent to help facilitate and optimize the processing of certain transactions in the system memory. The options are **Enable** and **Disable**.

Relaxed Ordering

Select Enable to allow certain transactions to be processed and completed before other transactions that have already been enqueued. The options are **Disable** and **Enable**.

►Intel® VT for Directed I/O (VT-d)

Intel® VT for Directed I/O (VT-d)

Select Enable to use Intel Virtualization Technology support for Direct I/O VT-d by reporting the I/O device assignments to the VMM (Virtual Machine Monitor) through the DMAR ACPI tables. This feature offers fully-protected I/O resource sharing across Intel platforms, providing greater reliability, security and availability in networking and data-sharing. The options are **Enable** and **Disable**.

Interrupt Remapping

Select Enable for Interrupt Remapping support to enhance system performance. The options are **Enable** and **Disable**.

PassThrough DMA

Select Enable for the Non-Iscoh VT-d engine to pass through DMA (Direct Memory Access) to enhance system performance. The options are **Enable** and **Disable**.

ATS

Select Enable to enable ATS (Address Translation Services) support for the Non-Iscoh VT-d engine to enhance system performance. The options are **Enable** and **Disable**.

Posted Interrupt

Select Enable to support VT_D Posted Interrupt which will allow external interrupts to be sent directly from a direct-assigned device to a client machine in non-root mode to improve virtualization efficiency by simplifying interrupt migration and lessening the need of physical interrupts. The options are **Enable** and **Disable**.

Coherency Support (Non-Isocoh)

Select Enable for the Non-Isocoh VT-d engine to pass through DMA (Direct Memory Access) to enhance system performance. The options are **Enable** and **Disable**.

►Intel® VMD Technology

►Intel® VMD for Volume Management Device on CPU1/CPU2

VMD Configuration for PStack0/VMD Configuration for PStack1/VMD Configuration for PStack2

Intel® VMD for Volume Management Device

Select Enable to enable Intel Volume Management Device support for the device specified by the user. The options are **Disable** and **Enable**.

IIO-PCIE Express Global Options

PCI-E Completion Timeout Disable

Select Yes to disable the PCI-E Completion Time-out settings. The options are **Yes**, **No**, and **Per-Port**.

►South Bridge

The following South Bridge information will display:

- USB Module Version
- USB Devices

Legacy USB Support

Select Enabled to support onboard legacy USB devices. Select Auto to disable legacy support if there are no legacy USB devices present. Select Disable to have all USB devices available for EFI applications only. The options are **Enabled**, **Disabled** and **Auto**.

XHCI Hand-Off

This is a work-around solution for operating systems that do not support XHCI (Extensible Host Controller Interface) hand-off. The XHCI ownership change should be claimed by the XHCI driver. The options are **Enabled** and **Disabled**.

Port 60/64 Emulation

Select Enabled for I/O port 60h/64h emulation support, which in turn, will provide complete legacy USB keyboard support for the operating systems that do not support legacy USB devices. The options are **Enabled** and **Disabled**.

PCI-E PLL SSC

Select Enable for PCH PCIe Spread Spectrum Clocking support, which will allow the BIOS to monitor and attempt to reduce the level of Electromagnetic Interference caused by the components whenever needed. The options are **Enable** and **Disable**.

►Server ME (Management Engine) Configuration

This feature displays the following system ME configuration settings.

General ME Configuration

Operational Firmware Version

Backup Firmware Version

Recovery Firmware Version

ME Firmware Status #1/ME Firmware Status #2

 Current State

 Error Code

PTT Support (Available when TPM Configuration is set to Disable. This feature is for the X11DPH-Tq board only)

Select Enable to support Intel® Platform Trust Technology (PTT) to enhance system security and data integrity. Intel PTT integrates the Host software stack, BIOS, Manageability Engine (ME), and the PCH to run on Intel's TCG (Trusted Computing Group) and TPM (Trusted Platform Module) firmware to achieve the purpose. The options are **Disable** and **Enable**.

►PCH SATA Configuration

When this submenu is selected, the AMI BIOS automatically detects the presence of the SATA devices that are supported by the Intel PCH chip and displays the following items:

SATA Controller

This item enables or disables the onboard SATA controller supported by the Intel PCH chip. The options are **Enable** and **Disable**.

Configure SATA as (Available when the item above: SATA Controller is set to enabled)

Select AHCI to configure a SATA drive specified by the user as an AHCI drive. Select RAID to configure a SATA drive specified by the user as a RAID drive. The options are **AHCI** and **RAID**. (Note: This item is hidden when the SATA Controller item is set to Disabled.)

SATA HDD Unlock

Select Enable to unlock SATA HDD password in the OS. The options are **Enable** and **Disable**.

SATA RSTe Boot Info (Available when the item "Configure SATA as" is set to "RAID")

Select Enable to enable full int13h support for devices connected to the SATA controller which will allow these SATA devices to be used as boot devices for system boot. The options are **Disable** and **Enable**.

Aggressive Link Power Management

When this item is set to Enabled, the SATA AHCI controller manages the power use of the SATA link. The controller will put the link in a low power mode during an extended period of I/O inactivity, and will return the link to an active state when I/O activity resumes. The options are **Enable** and **Disable**.

SATA RAID Option ROM/UEFI Driver (Available when the item "Configure SATA as" is set to "RAID")

Select EFI to load the EFI driver for system boot. Select Legacy to load a legacy driver for system boot. The options are **Disable**, **EFI**, and **Legacy**.

SATA Port 0 - SATA Port 7

Hot Plug

Select Enable to support Hot-plugging for the device installed on a selected SATA port which will allow the user to replace the device installed in the slot without shutting down the system. The options are **Enable** and **Disable**.

Spin Up Device

On an edge detect from 0 to 1, set this item to allow the SATA device installed on the SATA port specified by the user to start a COMRESET initialization. The options are **Enable** and **Disable**.

SATA Device Type

Use this item to specify if the device installed on the SATA port selected by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are **Hard Disk Drive** and **Solid State Drive**.

►PCH sSATA Configuration

When this submenu is selected, AMI BIOS automatically detects the presence of the sSATA devices that are supported by the PCH sSATA controller and displays the following items:

sSATA Controller

This item enables or disables the onboard sSATA controller supported by the Intel SCU. The options are **Enable** and **Disable**.

Configure sSATA as

Select AHCI to configure an sSATA drive specified by the user as an AHCI drive. Select RAID to configure an sSATA drive specified by the user as a RAID drive. The options are **AHCI** and **RAID**. (**Note:** This item is hidden when the sSATA Controller item is set to **Disabled**.)

SATA HDD Unlock

Select **Enable** to unlock sSATA HDD password in the OS. The options are **Enable** and **Disable**.

sSATA RSTe Boot Info (Available when the item "Configure SATA as" is set to "RAID")

Select **Enable** to enable full int13h support for devices connected to the SATA controller which will allow these SATA devices to be used as boot devices for system boot. The options are **Disable** and **Enable**.

Aggressive Link Power Management

When this item is set to **Enable**, the sSATA AHCI controller manages the power use of the SATA link. The controller will put the link in a low power mode during an extended period of I/O inactivity, and will return the link to an active state when I/O activity resumes. The options are **Disable** and **Enable**.

sSATA RAID Option ROM/UEFI Driver (Available when the item "Configure SATA as" is set to "RAID")

Select **EFI** to load the EFI driver for system boot. Select **Legacy** to load a legacy driver for system boot. The options are **Disable**, **EFI**, and **Legacy**.

sSATA Port 0/sSATA Port 1

Hot Plug

Select **Enable** to support Hot-plugging for the device installed on an sSATA port selected by the user which will allow the user to replace the device installed in the slot without shutting down the system. The options are **Disable** and **Enabled**.

Spin Up Device

On an edge detect from 0 to 1, set this item to allow the sSATA device installed on the sSATA port specified by the user to start a COMRESET initialization. The options are **Enable** and **Disable**.

sSATA Device Type

Use this item to specify if the device installed on the sSATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are **Hard Disk Drive** and Solid State Drive.

►PCIe/PCI/PnP Configuration

The following PCI information will be displayed:

- PCI Bus Driver Version
- PCI Devices Common Settings:

Above 4G Decoding (Available if the system supports 64-bit PCI decoding)

Select Enabled to decode a PCI device that supports 64-bit in the space above 4G Address. The options are **Enabled** and **Disabled**.

SR-IOV Support (Available if the system supports Single-Root Virtualization)

Select Enabled for Single-Root IO Virtualization support. The options are **Enabled** and **Disabled**.

MMIO High Base

Use this feature to select the base memory size according to memory-address mapping for the IO hub. The base memory size must be between 4032G to 4078G. The options are **56T**, 40T, 24T, 16T, 4T, and 1T.

MMIO High Granularity Size

Use this feature to select the high memory size according to memory-address mapping for the IO hub. The options are 1G, 4G, 16G, 64G, **256G**, and 1024G.

PCI PERR/SERR Support

Use this feature to enable or disable the runtime event for SERR (System Error)/ PERR (PCI/PCI-E Parity Error). The options are **Enabled** and **Disabled**.

Maximum Read Request

Select Auto for the system BIOS to automatically set the maximum size for a read request for a PCI-E device to enhance system performance. The options are **Auto**, 128 Bytes, 256 Bytes, 512 Bytes, 1024 Bytes, 2048 Bytes, and 4096 Bytes.

MMCFG Base

This feature determines the lowest MMCFG (Memory-Mapped Configuration) base assigned to PCI devices. The options are 1G, 1.5G, 1.75G, **2G**, 2.25G, and 3G.

NVMe Firmware Source

This feature determines which type of the NVMe firmware should be used in your system. The options are **Vendor Defined Firmware** and **AMI Native Support**.

VGA Priority

This feature selects the graphics device to be used as the primary video display for system boot. The options are Auto, **Onboard** and Offboard.

CPU1 Slot 1 PCI-E 3.0 x8 OPROM/CPU2 Slot 2 PCI-E 3.0 x16 OPROM/CPU1 Slot 3 PCI-E 3.0 x8 OPROM/CPU2 Slot 4 PCI-E 3.0 x16 OPROM/CPU2 Slot 5 PCI-E 3.0 x16 OPROM/CPU1 Slot 6 PCI-E 3.0 x8 OPROM/CPU1 Slot 7 PCI-E 3.0 x8 OPROM/M.2-C1 3.0 x4 OPROM/M.2-C2 3.0 x4 OPROM

Select EFI to allow the user to boot the computer using an EFI (Expansible Firmware Interface) device installed on the PCI-E slot specified by the user. Select Legacy to allow the user to boot the computer using a legacy device installed on the PCI-E slot specified by the user. The options are Disabled, **Legacy** and EFI. (Note: Riser card names may differ in each system.)

Onboard LAN Device

Select Enable to use onboard LAN devices for internet connections. The options are Disabled and **Enable**.

Onboard LAN Option ROM Type

Use this feature to select the firmware type to be used for onboard LAN ports for system boot. The options are **Legacy** and EFI.

Onboard LAN1 Option ROM

Use this feature to select the type of device to be installed in LAN Port1 used for system boot. The options are **PXE**, iSCSI, and Disabled.

Onboard LAN2 Option ROM

Use this feature to select the type of device to be installed in LAN Port2 used for system boot. The options are **PXE** and **Disabled**.

Onboard Video Option ROM

Use this feature to select the Onboard Video Option ROM type. The options are Disabled, **Legacy** and EFI.

►Network Stack Configuration

Network Stack

Select Enabled to enable PXE (Preboot Execution Environment) or UEFI (Unified Extensible Firmware Interface) for network stack support. The options are **Enabled** and Disabled.

***If "Network Stack" is set to Enabled, the following items will display:**

Ipv4 PXE Support

Select Enabled to enable Ipv4 PXE boot support. If this feature is disabled, it will not create the Ipv4 PXE boot option. The options are Disabled and **Enabled**.

Ipv4 HTTP Support

Select Enabled to enable Ipv4 HTTP boot support. If this feature is disabled, it will not create the Ipv4 HTTP boot option. The options are Enabled and **Disabled**.

Ipv6 PXE Support

Select Enabled to enable Ipv6 PXE boot support. If this feature is disabled, it will not create the Ipv6 PXE boot option. The options are Disabled and **Enabled**.

Ipv6 HTTP Support

Select Enabled to enable Ipv6 HTTP boot support. If this feature is disabled, it will not create the Ipv6 HTTP boot option. The options are Enabled and **Disabled**.

PXE Boot Wait Time

Use this feature to select the wait time to press the <ESC> key to abort the PXE boot. The default is **0**.

Media Detect Time

Use this feature to select the wait time in seconds for the BIOS ROM to detect the LAN media (Internet connection or LAN port). The default is **1**.

►Super IO Configuration

Super IO Chip AST2500

►Serial Port 1 Configuration

Serial Port 1

Select Enabled to enable the onboard serial port specified by the user. The options are **Enabled** and **Disabled**.

Device Settings

This feature displays the base I/O port address and the Interrupt Request address of a serial port specified by the user.

Note: This item is hidden when Serial Port 1 is set to **Disabled**.

Change Settings

This feature specifies the base I/O port address and the Interrupt Request address of Serial Port 1. Select **Auto** for the BIOS to automatically assign the base I/O and IRQ address to a serial port specified.

The options for Serial Port 1 are **Auto**, (IO=3F8h; IRQ=4), (IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12); (IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), and (IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12).

►Serial Port 2 Configuration

Serial Port 2

Select Enabled to enable the onboard serial port specified by the user. The options are **Enabled** and **Disabled**.

Device Settings

This feature displays the base I/O port address and the Interrupt Request address of a serial port specified by the user.

Note: This item is hidden when Serial Port 1 is set to Disabled.

Change Settings

This feature specifies the base I/O port address and the Interrupt Request address of Serial Port 2. Select Auto for the BIOS to automatically assign the base I/O and IRQ address to a serial port specified. The options for Serial Port 2 are **Auto**, (IO=2F8h; IRQ=3), (IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12); (IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), and (IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12).

Serial Port 2 Attribute

Select SOL to use COM Port 2 as a Serial_Over_LAN (SOL) port for console redirection. The options are **COM** and **SOL**.

►Serial Port Console Redirection

COM 1 Console Redirection

Select Enabled to enable COM Port 1 for Console Redirection, which will allow a client machine to be connected to a host machine at a remote site for networking. The options are **Enabled** and **Disabled**.

**If the item above set to Enabled, the following items will become available for configuration:*

►Console Redirection Settings (when COM1 Console Redirection is Enabled)

Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are **ANSI**, **VT100**, **VT100+**, and **VT-UTF8**.

Bits Per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are **7 (Bits)** and **8 (Bits)**.

Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark and Space.

Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and **2**.

Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are **Enabled** and **Disabled**.

Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and **Enabled**.

Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are **Disabled** and **Enabled**.

Legacy OS Redirection Resolution

Use this feature to select the number of rows and columns used in Console Redirection for legacy OS support. The options are **80x24** and **80x25**.

Putty KeyPad

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, **LINUX**, **XTERM**, **SCO**, **ESCN**, and **VT400**.

Redirection After BIOS Post

Use this feature to enable or disable legacy Console Redirection after BIOS POST. When the option-Bootloader is selected, legacy Console Redirection is disabled before booting the OS. When the option-Always Enable is selected, legacy Console Redirection remains enabled upon OS bootup. The options are **Always Enable** and **Bootloader**.

SOL (Serial-Over-LAN)/COM2

Console Redirection (for SOL/COM2)

Select Enabled to use the SOL port for Console Redirection. The options are **Enabled** and **Disabled**.

**If the item above set to Enabled, the following items will become available for user's configuration:*

►Console Redirection Settings (for SOL/COM2)

Use this feature to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are **ANSI**, **VT100**, **VT100+**, and **VT-UTF8**.

Bits Per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are **7 (Bits)** and **8 (Bits)**.

Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark and Space.

Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and **2**.

Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start data-sending when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are **Enabled** and **Disabled**.

Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and **Enabled**.

Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are **Disabled** and **Enabled**.

Legacy OS Redirection Resolution

Use this feature to select the number of rows and columns used in Console Redirection for legacy OS support. The options are **80x24** and **80x25**.

Putty KeyPad

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, **LINUX**, **XTERMR6**, **SCO**, **ESCN**, and **VT400**.

Redirection After BIOS Post

Use this feature to enable or disable legacy Console Redirection after BIOS POST (Power-On Self-Test). When this feature is set to Bootloader, legacy Console Redirection is disabled before booting the OS. When this feature is set to Always Enable, legacy Console Redirection remains enabled upon OS boot. The options are **Always Enable** and **Bootloader**.

►Legacy Console Redirection Settings

Legacy Console Redirection Settings

Use the feature to select the COM port to display redirection of Legacy OS and Legacy OPROM messages. The default setting is **COM1**.

Serial Port for Out-of-Band Management/Windows Emergency Management Services (EMS)

The submenu allows the user to configure Console Redirection settings to support Out-of-Band Serial Port management.

Console Redirection (for EMS)

Select Enabled to use a COM port selected by the user for EMS Console Redirection. The options are **Disabled** and **Enabled**.

**If the item above set to Enabled, the following items will become available for user's configuration:*

►EMS Console Redirection Settings

Out-of-Band Management Port

The feature selects a serial port in a client server to be used by the Windows Emergency Management Services (EMS) to communicate with a remote host server. The options are **COM1 (Console Redirection)** and **COM2/SOL (Console Redirection)**.

Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII character set. Select VT100+ to add color and function key support. Select ANSI to use the extended ASCII character set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are **ANSI**, **VT100**, **VT100+**, and **VT-UTF8**.

Bits Per Second

This feature sets the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in both host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 57600, and **115200** (bits per second).

Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop data-sending when the receiving buffer is full. Send a "Start" signal to start data-sending when the receiving buffer is empty. The options are **None**, Hardware RTS/CTS, and Software Xon/Xoff.

The setting for each these features is displayed:

Data Bits, Parity, Stop Bits

►ACPI Settings

Use this feature to configure Advanced Configuration and Power Interface (ACPI) power management settings for your system.

NUMA Support (Available when the OS supports this feature)

Select Enabled to enable Non-Uniform Memory Access support to enhance system performance. The options are **Enabled** and **Disabled**.

WHEA Support

Select Enabled to support the Windows Hardware Error Architecture (WHEA) platform and provide a common infrastructure for the system to handle hardware errors within the Windows OS environment to reduce system crashes and to enhance system recovery and health monitoring. The options are **Enabled** and **Disabled**.

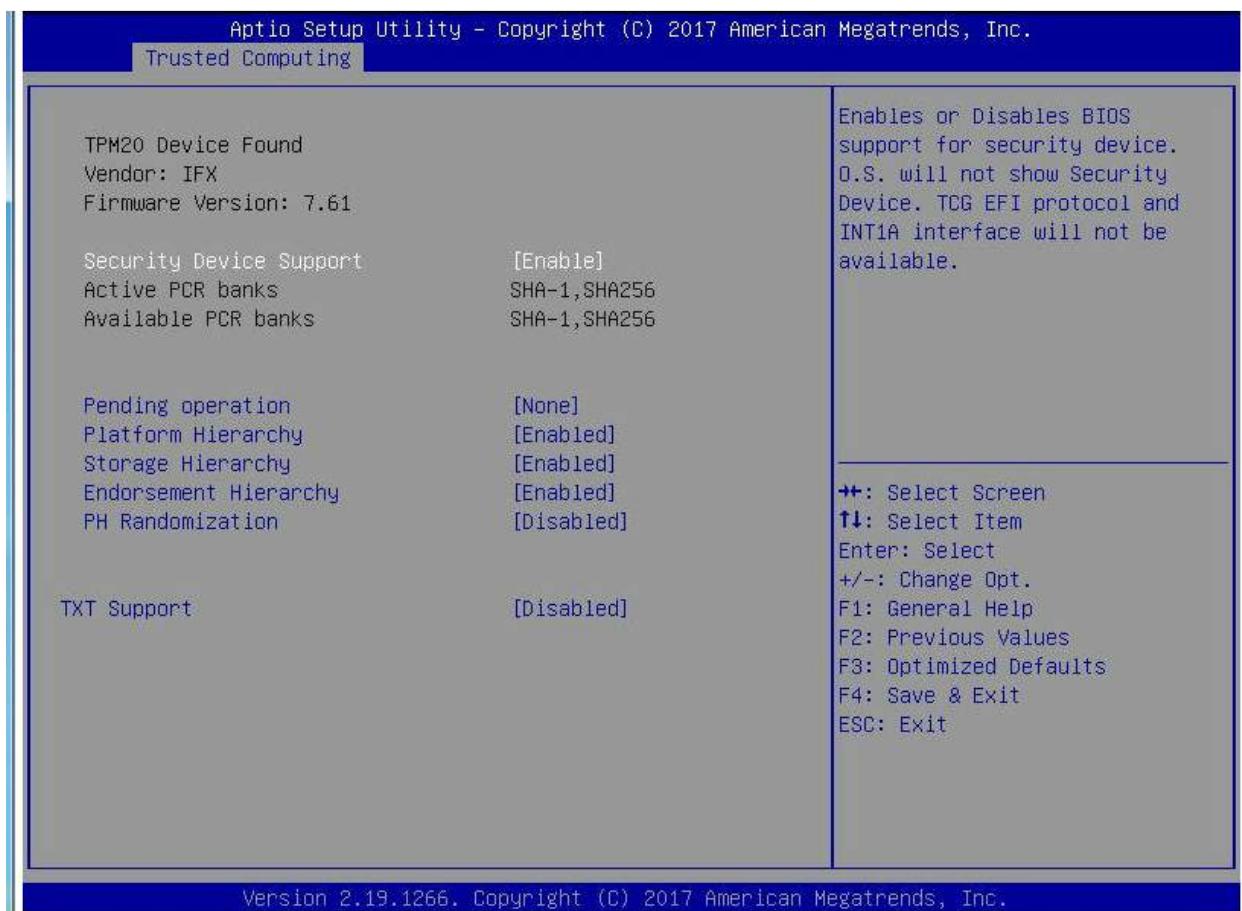
High Precision Timer

Select Enabled to activate the High Precision Event Timer (HPET) that produces periodic interrupts at a much higher frequency than a Real-time Clock (RTC) does in synchronizing multimedia streams, providing smooth playback and reducing the dependency on other timestamp calculation devices, such as an x86 RDTSC Instruction embedded in the CPU. The High Performance Event Timer is used to replace the 8254 Programmable Interval Timer. The options are **Enabled** and **Disabled**.

► Trusted Computing (Available when a TPM device is detected and PTT Support under "Server ME Config" is not Enabled)

When a TPM (Trusted-Platform Module) device is detected in your machine, the following information will display.

- TPM2.0 Device Found
- Vendor
- Firmware Version



Security Device Support

If this feature and the TPM jumper (JPT1) on the motherboard are both enabled, the onboard security (TPM) device will be enabled in the BIOS to enhance data integrity and system security. Please note that the OS will not show the security device. Neither TCG EFI protocol nor INT1A interaction will be made available for use. If you have made changes on the setting on this item, be sure to reboot the system for the change to take effect. The options are **Disable** and **Enable**. If this option is set to Enable, the following screen and items will display:

- Active PCR Banks
- Available PCR Banks

Pending Operation

Use this feature to schedule a TPM-related operation to be performed by a security (TPM) device at the next system boot to enhance system data integrity. Your system will reboot to carry out a pending TPM operation. The options are **None** and TPM Clear.

Note: Your system will reboot to carry out a pending TPM operation.

Platform Hierarchy (for TPM Version 2.0 and above)

Select Enabled for TPM Platform Hierarchy support which will allow the manufacturer to utilize the cryptographic algorithm to define a constant key or a fixed set of keys to be used for initial system boot. This early boot code is shipped with the platform and is included in the list of "public keys". During system boot, the platform firmware uses this trusted public key to verify a digital signature in an attempt to manage and control the security of the platform firmware used in a host system via a TPM device. The options are **Enabled** and Disabled.

Storage Hierarchy

Select Enabled for TPM Storage Hierarchy support that is intended to be used for non-privacy-sensitive operations by the platform owner such as an IT professional or the end user. Storage Hierarchy has an owner policy and an authorization value, both of which can be set and are held constant (-rarely changed) through reboots. This hierarchy can be cleared or changed independently of the other hierarchies. The options are **Enabled** and Disabled.

Endorsement Hierarchy

Select Enabled for Endorsement Hierarchy support, which contains separate controls to address the user's privacy concerns because the primary keys in this hierarchy are certified by the TPM or a manufacturer to be constrained to an authentic TPM device that is attached to an authentic platform. A primary key can be an encrypted, and a certificate can be created using TPM2_ActivateCredential. It allows the user to independently enable "flag, policy, and authorization value" without involving other hierarchies. A user with privacy concerns can disable the endorsement hierarchy while still using the storage hierarchy for TPM applications and permitting the platform software to use the TPM. The options are **Enabled** and Disabled.

PH (Platform Hierarchy) Randomization (for TPM Version 2.0 and above)

Select Enabled for Platform Hierarchy Randomization support, which is used only during the platform developmental stage. This feature cannot be enabled in the production platforms. The options are **Disabled** and Enabled.

TXT Support

Select Enabled to enable Intel Trusted Execution Technology (TXT) support to enhance system security and data integrity. The options are **Disabled** and Enabled.

Note 1: If the option for this item (TXT Support) is set to Enabled, be sure to disable EV DFX (Device Function On-Hide) support for the system to work properly. (EV DFX is under "IIO Configuration" in the "Chipset/North Bridge" submenu).

Note 2: For more information on TPM, please refer to the TPM manual at <http://www.supermicro.com/manuals/other>.

►iSCSI Configuration

iSCSI Initiator Name

This feature allows the user to enter the unique name of the iSCSI Initiator in IQN format. Once the name of the iSCSI Initiator is entered into the system, configure the proper settings for the following items.

►Add an Attempt

►Delete Attempts

►Change Attempt order

►Intel® Virtual RAID on CPU

When this submenu is selected and the RAID devices are detected, the BIOS screen displays the following items:

Intel® VROC with VMD Technology 5.2.0.1023

6.4 Event Logs

Use this feature to configure Event Log settings.



►Change SMBIOS Event Log Settings

Enabling/Disabling Options

SMBIOS Event Log

Select Enabled to enable SMBIOS (System Management BIOS) Event Logging during system boot. The options are **Enabled** and **Disabled**.

Erasing Settings

Erase Event Log

Select Enabled to erase all error events in the SMBIOS (System Management BIOS) log before an event logging is initialized at bootup. The options are **No**, **Yes**, **Next Reset**, and **Yes, Next Reset**.

When Log is Full

Select Erase Immediately to immediately erase all errors in the SMBIOS event log when the event log is full. Select Do Nothing for the system to do nothing when the SMBIOS event log is full. The options are **Do Nothing** and **Erase Immediately**.

SMBIOS Event Log Standard Settings

Log System Boot Event

Select Enabled to log system boot events. The options are Enabled and **Disabled**.

MECI (Multiple Event Count Increment)

Enter the increment value for the multiple event counter. Enter a number between 1 to 255. The default setting is **1**.

METW (Multiple Event Count Time Window)

Use this feature to determine how long (in minutes) the multiple event counter should wait before generating a new event log. Enter a number between 0 to 99. The default setting is **60**.

Note: Please reboot the system for the changes to take effect.

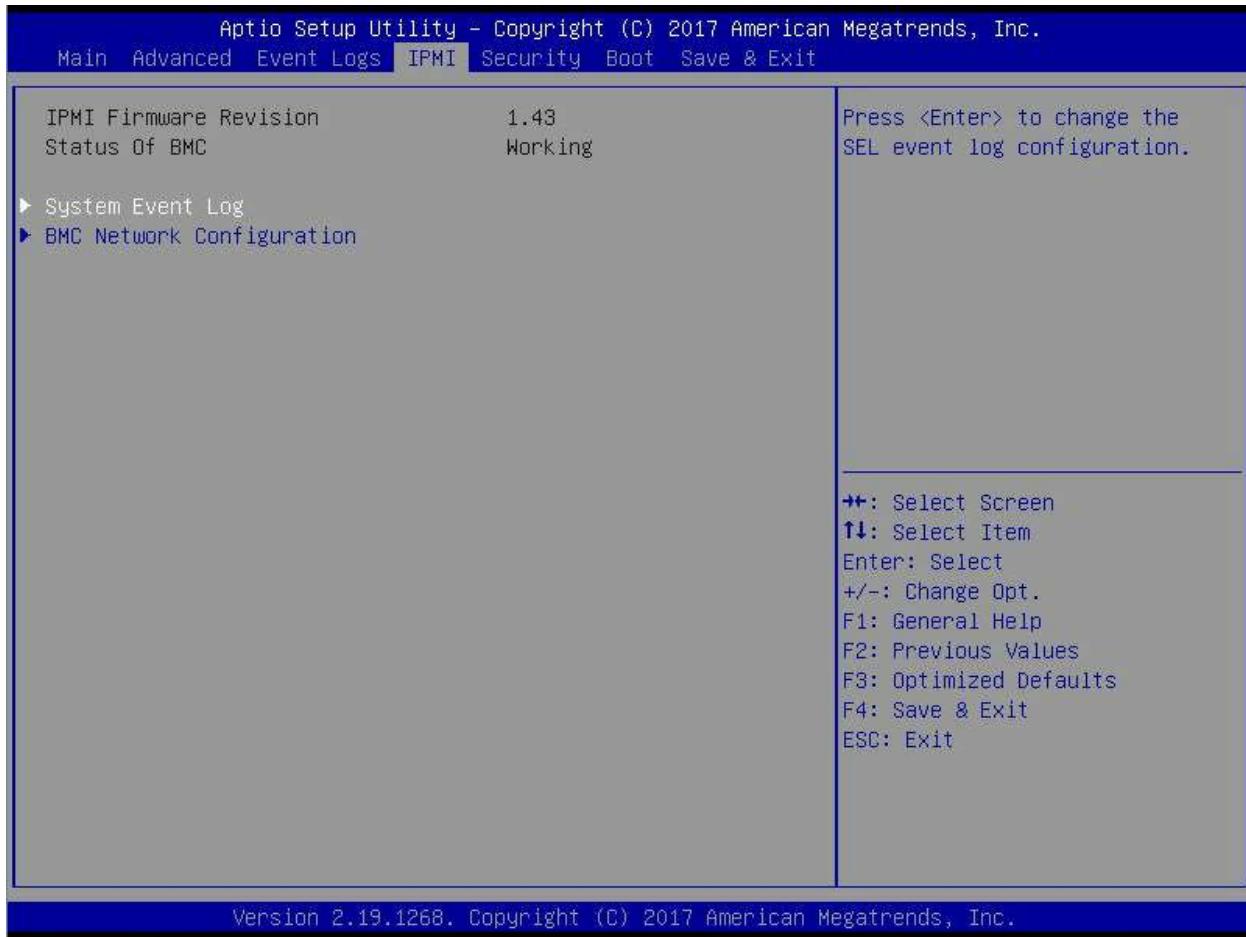
►View System Event Log

This item allows the user to view the event in the system event log. Select this feature and press <Enter> to view the status of an event in the log. The following categories are displayed:

Date/Time/Error Code/Severity

6.5 IPMI

Use this feature to configure Intelligent Platform Management Interface (IPMI) settings.



When you select this submenu and press the <Enter> key, the following information will display:

- BMC Firmware Revision: This feature indicates the IPMI firmware revision used in your system.
- IPMI Status: This feature indicates IPMI status of your system.

►System Event Log

Enabling/Disabling Options

SEL Components

Select Enabled for all system event logging at bootup. The options are **Enabled** and **Disabled**.

Erasing Settings

Erase SEL

Select Yes, On next reset to erase all system event logs upon next system reboot. Select Yes, On every reset to erase all system event logs upon each system reboot. Select No to keep all system event logs after each system reboot. The options are **No**, Yes, On next reset, and Yes, On every reset.

When SEL is Full

This feature allows the user to determine what the BIOS should do when the system event log is full. Select Erase Immediately to erase all events in the log when the system event log is full. The options are **Do Nothing** and Erase Immediately.

►BMC Network Configuration

The following items will be displayed:

- IPMI LAN Selection: This feature displays the IPMI LAN setting. The default setting is **Failover**.
- IPMI Network Link Status: This item displays the IPMI Network Link status. The default setting is **Dedicated LAN**.
- Update IPMI LAN Configuration: Select Yes for the BIOS to implement all IP/MAC address changes at the next system boot. The default setting is **No**.
- Current Configuration Address Source: This feature displays the source of the current IPMI LAN address. The default setting is **DHCP (Dynamic Host Configuration Protocol)**.
- Station IP Address: This feature displays the Station IP address for this computer. This should be in decimal and in dotted quad form (i.e., 192.168.10.253).
- Subnet Mask: This feature displays the sub-network that this computer belongs to. The value of each three-digit number separated by dots should not exceed 255.
- Station MAC Address: This feature displays the Station MAC address for this computer. Mac addresses are 6 two-digit hexadecimal numbers.
- Gateway IP Address: This feature displays the Gateway IP address for this computer. This should be in decimal and in dotted quad form (i.e., 192.168.10.253).
- VLAN: Select Enabled to enable IPMI VLAN support. The options are **Enabled** and **Disabled**.
- LAN Channel 1

IPV6 Support

Select Enabled for IPV6 support. The options are **Enabled** and **Disabled**.

Configuration Address Source

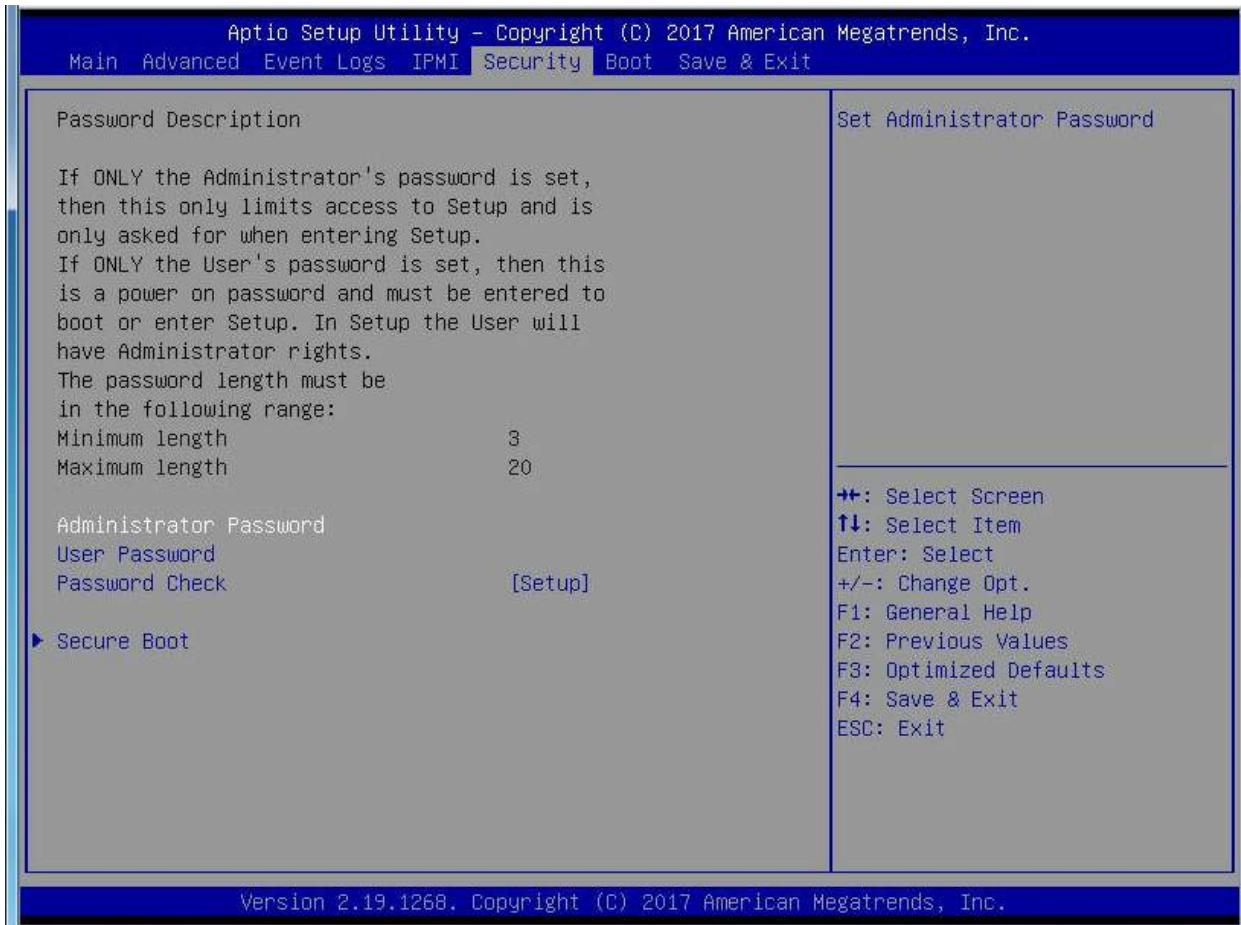
Use this feature to select the IP address source for this computer. If Static is selected, you will need to know the IP address of this computer and enter it to the system manually in the field. If DHCP is selected, AMI BIOS will search for a DHCP (Dynamic Host Configuration Protocol) server attached to the network and request the next available IP address for this computer. The options are **Unspecified**, **DHCP** and **Static**.

The following features will be displayed:

- Current Configuration Address Source
- Station IPV6 Address
- Prefix Length
- IPV6 Router1 IP Address
- IPV6 Address Status
- IPV6 DHCP Algorithm

6.6 Security Settings

This menu allows the user to configure the following security settings for the system.



Administrator Password

Use this feature to set the administrator password which is required to enter the BIOS setup utility. The length of the password should be from 3 characters to 20 characters long.

User Password

Use this feature to set the user password which is required to enter the BIOS setup utility. The length of the password should be from 3 characters to 20 characters long.

Password Check

Select Setup for the system to check for a password at Setup. Select Always for the system to check for a password at bootup or upon entering the BIOS Setup utility. The options are **Setup** and **Always**.

►Secure Boot

When you select this submenu and press the <Enter> key, the following items will display:

- System Mode
- Secure Boot
- Vendor Keys

Secure Boot

If this feature is set to Enabled, Secure Boot will be activated when a Platform Key (PK) is entered. A Platform Key is a security key used to manage the security settings of the platform firmware used in your system. The options are Enabled and **Disabled**.

Secure Boot Mode

Use this feature to select the desired secure boot mode for the system. The options are Standard and **Custom**.

CMS Support

Select Enabled for CMS support to enhance system performance. The options are **Enabled** and Disabled.

►Key Management

Provision Factory Default Keys

Select Enabled to install all manufacturer default keys to configure the following system security settings. The options are Enabled and **Disabled**.

►Enroll All Factor Defaults

Select Yes to install all manufacturer defaults to configure the following system security settings. The options are **Yes** and No.

►Enroll EFI Image

Select this item and press <Enter> to select an EFI (Extensible Firmware Interface) image for the system to operate in Secure Boot mode.

►Save All Secure Boot Variables

This feature allows the user to set and save the secure boot key variables specified by the user.

►Platform Key (PK)

This feature allows the user to enter and configure a set of values to be used as a platform firmware key for the system. This set of values also indicate the size, the keys numbers, and the key source of the Platform Key. The options are **Save to File**, Set New, and Erase.

►Key Exchange Keys

This feature allows the user to enter and configure a set of values to be used as a Key-Exchange-Keys for the system. This set of values also indicate the size, the keys numbers, and the key source of the Key-Exchange-Keys. The options are **Save to File**, Set New, and Erase.

►Authorized Signatures

This feature allows the user to enter and configure a set of values to be used as Authorized Signatures for the system. These values also indicate the size, the keys numbers, and the key source of the Authorized Signatures. The options are **Set New** and Append.

Secure Boot Variable/Size/Key#/Key Sources The options are **Save to File**, Set New, and Erase.

►Forbidden Signatures

This feature allows the user to enter and configure a set of values to be used as Forbidden Signatures for the system. These values also indicate the size, the keys numbers, and the key source of the Forbidden Signatures. The options are **Save to File**, Set New, and Erase.

►Authorized TimeStamps

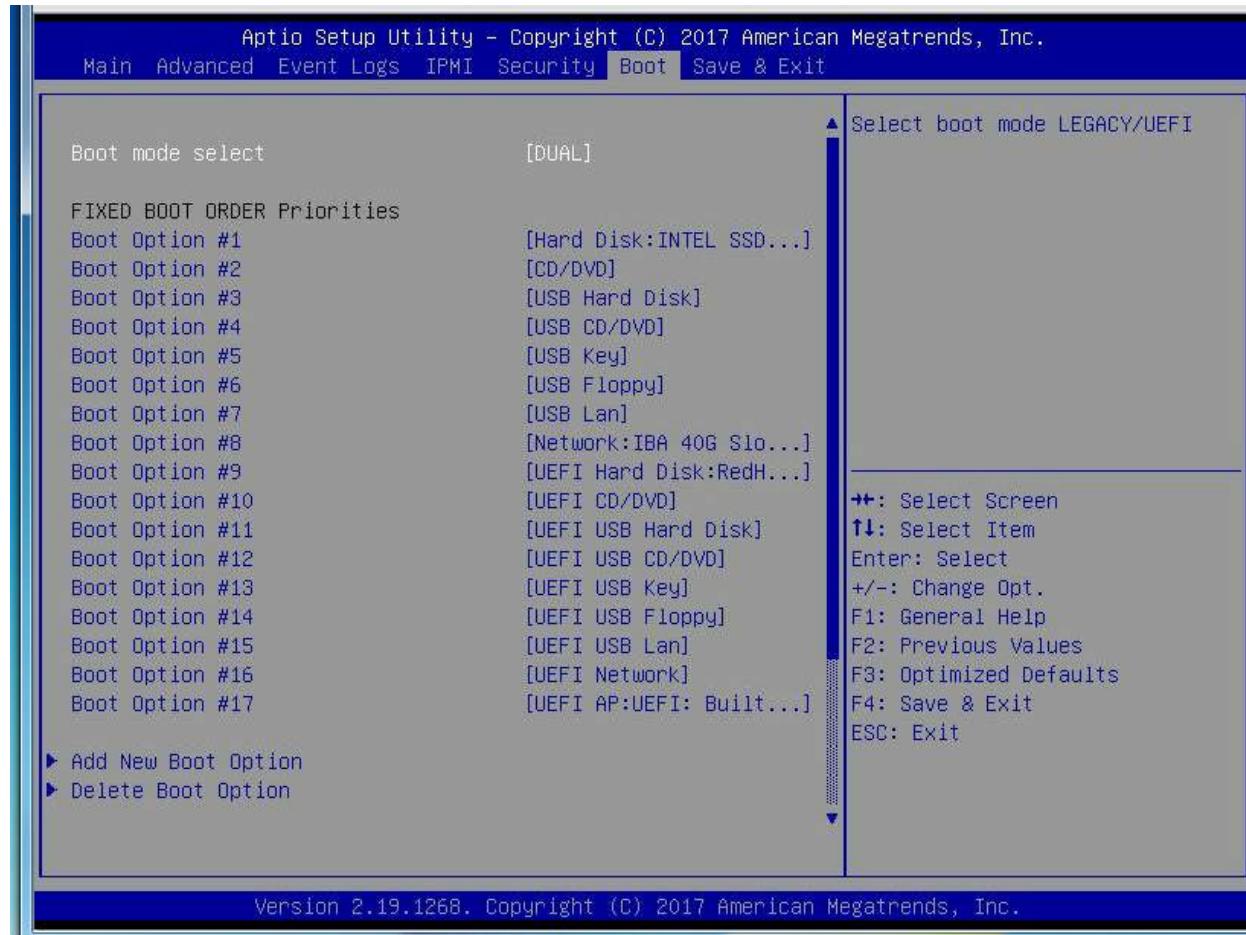
This feature allows the user to set and save the timestamps for Authorized Signatures to indicate when these signatures were entered into the system. The options are **Save to File**, Set New, and Erase.

►OsRecovery Signatures

This feature allows the user to set and save the Authorized Signatures used for OS recovery. The options are **Save to File**, Set New, and Erase.

6.7 Boot Settings

Use this feature to configure Boot Settings:



Boot Mode Select

Use this feature to select the type of devices to be used for system boot. The options are **Legacy**, **UEFI** (Unified Extensible Firmware Interface), and **Dual**.

Legacy to EFI Support

Select **Enabled** for the system to boot from an EFI OS when the Legacy OS fails. The options are **Enabled** and **Disabled**.

Fixed Boot Order Priorities

This feature prioritizes the order of a bootable device from which the system will boot. Press <Enter> on each entry from top to bottom to select devices.

When the item above -"Boot Mode Select" is set to **Dual** (default), the following items will be displayed for configuration:

- Boot Option #1 - Boot Option #17

When the item above -"Boot Mode Select" is set to Legacy, the following items will be display for configuration:

- Boot Option #1 - Boot Option #8

When the item above -"Boot Mode Select" is set to UEFI, the following items will be display for configuration:

- Boot Option #1 - Boot Option #9

Add New Boot Option

This feature allows the user to add a new boot option to the boot priority features for your system.

Add Boot Option

Use this feature to specify the name for the new boot option.

Path for Boot Option

Use this feature to enter the path for the new boot option in the format fsx:\path\filename.efi.

Boot Option File Path

Use this feature to specify the file path for the new boot option.

Create

After the name and the file path for the boot option are set, press <Enter> to create the new boot option in the boot priority list.

►Delete Boot Option

Use this feature to select a boot device to delete from the boot priority list.

Delete Boot Option

Use this feature to remove an EFI boot option from the boot priority list.

►Add New Driver Option

Use this feature to select a new driver to add to the boot priority list.

Add Driver Option

Use this feature to specify the name of the driver that the new boot option is added to.

Path for Driver Option

Use this feature to specify the path to the driver that the new boot option is added to.

Driver Option File Path

Use this feature to specify the file path of the driver that the new boot option is added to.

Create

After the driver option name and the file path are set, press <Enter> to enter to submenu and click OK to create the new boot option drive.

►Delete Driver Option

Use this item to select a boot driver to delete from the boot priority list.

Delete Drive Option

Select the target boot driver to delete from the boot priority list.

►Hard Disk Drive BBS Priorities

- Boot Option #1 - #5

►Network Drive BBS Priorities

- Boot Option #1

►USB Key Drive BBS Priorities

- Boot Option #1

►UEFI Hard Disk Drive BBS Priorities

- Boot Option #1

►UEFI USB Key Drive BBS Priorities

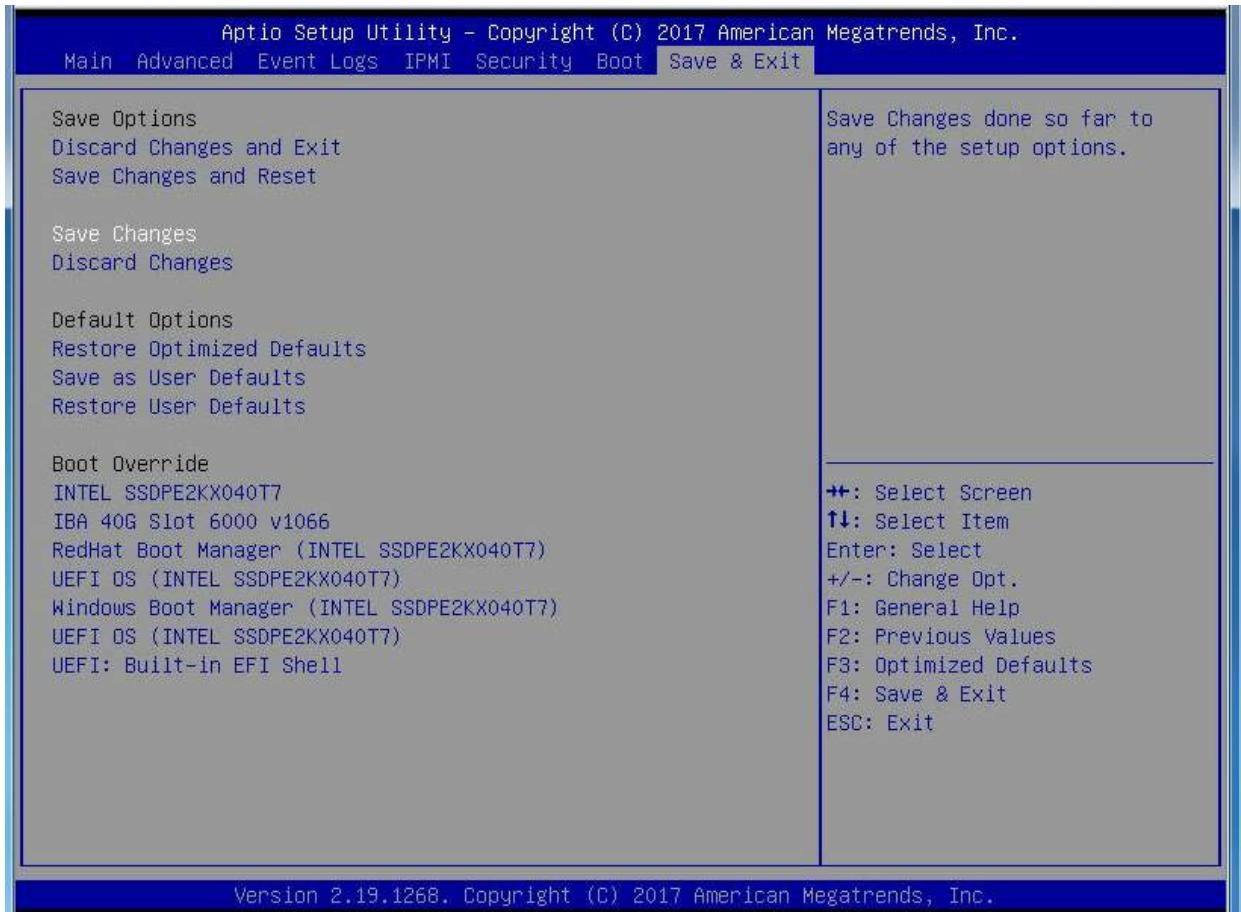
- Boot Option #1

►UEFI Application Boot Priorities

- Boot Option #1

6.8 Save & Exit

Select the Save & Exit tab from the BIOS setup screen to configure the settings below.



Save Options

Discard Changes and Exit

Select this option to quit the BIOS setup without making any permanent changes to the system configuration and reboot the computer. Select Discard Changes and Exit from the Exit menu and press <Enter>.

Save Changes and Reset

When you have completed the system configuration changes, select this option to leave the BIOS setup utility and reboot the computer for the new system configuration parameters to take effect. Select Save Changes and Exit from the Exit menu and press <Enter>.

Save Changes

When you have completed the system configuration changes, select this option to save all changes made. This will not reset (reboot) the system.

Discard Changes

Select this option and press <Enter> to discard all the changes and return to the AMI BIOS setup utility.

Default Options

Restore Optimized Defaults

To set this feature, select Restore Defaults from the Exit menu and press <Enter> to load manufacturer default settings which are intended for maximum system performance but not for maximum stability.

Save As User Defaults

To set this feature, select Save as User Defaults from the Exit menu and press <Enter>. This enables the user to save any changes to the BIOS setup for future use.

Restore User Defaults

To set this feature, select Restore User Defaults from the Exit menu and press <Enter>. Use this feature to retrieve user-defined settings that were saved previously.

Boot Override

This feature allows the user to override the Boot priorities sequence in the Boot menu, and immediately boot the system with a device specified by the user instead of the one specified in the boot list. This is a one-time override.

Appendix A

BIOS Error Codes

A.1 BIOS Error Beep (POST) Codes

During the POST (Power-On Self-Test) routines, which are performed each time the system is powered on, errors may occur.

Non-fatal errors are those which, in most cases, allow the system to continue the boot-up process. The error messages normally appear on the screen.

Fatal errors are those which will not allow the system to continue the boot-up procedure. If a fatal error occurs, you should consult with your system manufacturer for possible repairs.

These fatal errors are usually communicated through a series of audible beeps. The numbers on the fatal error list (on the following page) correspond to the number of beeps for the corresponding error.

BIOS Beep (POST) Codes		
Beep Code	Error Message	Description
1 beep	Refresh	Circuits have been reset (Ready to power up)
5 short, 1 long	Memory error	No memory detected in system
5 long, 2 short	Display memory read/write error	Video adapter missing or with faulty memory
1 long continuous	System OH	System overheat condition

A.2 Additional BIOS POST Codes

The AMI BIOS supplies additional checkpoint codes, which are documented online at <http://www.supermicro.com/support/manuals/> ("AMI BIOS POST Codes User's Guide").

When BIOS performs the Power On Self Test, it writes checkpoint codes to I/O port 0080h. If the computer cannot complete the boot process, a diagnostic card can be attached to the computer to read I/O port 0080h (Supermicro p/n AOC-LPC80-20).

For information on AMI updates, please refer to <http://www.ami.com/products/>.

Appendix B

Standardized Warning Statements for AC Systems

B.1 About Standardized Warning Statements

The following statements are industry standard warnings, provided to warn the user of situations which have the potential for bodily injury. Should you have questions or experience difficulty, contact Supermicro's Technical Support department for assistance. Only certified technicians should attempt to install or configure components.

Read this appendix in its entirety before installing or configuring components in the Supermicro chassis.

These warnings may also be found on our website at http://www.supermicro.com/about/policies/safety_information.cfm.

Warning Definition



Warning! This warning symbol means danger. You are in a situation that could cause bodily injury. Before you work on any equipment, be aware of the hazards involved with electrical circuitry and be familiar with standard practices for preventing accidents.

警告の定義

この警告サインは危険を意味します。

人身事故につながる可能性がありますので、いずれの機器でも動作させる前に、電気回路に含まれる危険性に注意して、標準的な事故防止策に精通して下さい。

此警告符号代表危险。

您正处于可能受到严重伤害的工作环境中。在您使用设备开始工作之前，必须充分意识到触电的危险，并熟练掌握防止事故发生的标准工作程序。请根据每项警告结尾的声明号码找到此设备的安全性警告说明的翻译文本。

此警告符號代表危險。

您正處於可能身體可能會受損傷的工作環境中。在您使用任何設備之前，請注意觸電的危險，並且要熟悉預防事故發生的標準工作程序。請依照每一注意事項後的號碼找到相關的翻譯說明內容。

Warnung

WICHTIGE SICHERHEITSHINWEISE

Dieses Warnsymbol bedeutet Gefahr. Sie befinden sich in einer Situation, die zu Verletzungen führen kann. Machen Sie sich vor der Arbeit mit Geräten mit den Gefahren elektrischer Schaltungen und den üblichen Verfahren zur Vorbeugung von Unfällen vertraut. Suchen Sie mit der am Ende jeder Warnung angegebenen Anweisungsnummer nach der jeweiligen Übersetzung in den übersetzten Sicherheitshinweisen, die zusammen mit diesem Gerät ausgeliefert wurden.

BEWAHREN SIE DIESE HINWEISE GUT AUF.

INSTRUCCIONES IMPORTANTES DE SEGURIDAD

Este símbolo de aviso indica peligro. Existe riesgo para su integridad física. Antes de manipular cualquier equipo, considere los riesgos de la corriente eléctrica y familiarícese con los procedimientos estándar de prevención de accidentes. Al final de cada advertencia encontrará el número que le ayudará a encontrar el texto traducido en el apartado de traducciones que acompaña a este dispositivo.

GUARDE ESTAS INSTRUCCIONES.

IMPORTANTES INFORMATIONS DE SÉCURITÉ

Ce symbole d'avertissement indique un danger. Vous vous trouvez dans une situation pouvant entraîner des blessures ou des dommages corporels. Avant de travailler sur un équipement, soyez conscient des dangers liés aux circuits électriques et familiarisez-vous avec les procédures couramment utilisées pour éviter les accidents. Pour prendre connaissance des traductions des avertissements figurant dans les consignes de sécurité traduites qui accompagnent cet appareil, référez-vous au numéro de l'instruction situé à la fin de chaque avertissement.

CONSERVEZ CES INFORMATIONS.

תקנון הצהרות אזהרה

הצהרות הבאות הן אזהרות על פי התקני התעשייה, על מנת להזהיר את המשתמש מפני חבלה פיזית אפשרית. במידה ויש שאלות או היתקלות בעיה כלשהי, יש ליצור קשר עם מחלקת תמייה טכנית של סופרמייקרו. טכנאים מוסמכים בלבד רשאים להתקין או להגדיר את הרכיבים. יש לקרוא את הנספח במלואו לפני התקנת או הגדרת הרכיבים במאזן סופרמייקרו.

ا گ ف حالة وکی ای تتسبب ف اصابة جسد هـ هذا الزهز ع خطز! تحد زـ .
 قبل ای تغول على ای هعذات، کي على عن بالوخارط ال اجـ عـ عـ الذـائـ
 الكـهـزـبـائـيـهـ
 وکـيـ عـلـىـ درـاـهـ بـالـوـوـارـسـاتـ الـقـائـيـهـ لـوـعـ وـقـعـ اـيـ حـادـثـ
 استـخـدـمـ رـقـنـ الـبـ اـيـ الـوـصـصـ فـهـاـهـ کـلـ تـحدـ زـ لـلـعـثـرـ تـزـجـوـتـهـاـ

안전을 위한 주의사항

경고!

이 경고 기호는 위험이 있음을 알려 줍니다. 작업자의 신체에 부상을 야기 할 수 있는 상태에 있게 됩니다. 모든 장비에 대한 작업을 수행하기 전에 전기회로와 관련된 위험요소들을 확인하시고 사전에 사고를 방지할 수 있도록 표준 작업절차를 준수해 주시기 바랍니다.

해당 번역문을 찾기 위해 각 경고의 마지막 부분에 제공된 경고문 번호를 참조하십시오

BELANGRIJKE VEILIGHEIDSINSTRUCTIES

Dit waarschuwingssymbool betekent gevaar. U verkeert in een situatie die lichamelijk letsel kan veroorzaken. Voordat u aan enige apparatuur gaat werken, dient u zich bewust te zijn van de bij een elektrische installatie betrokken risico's en dient u op de hoogte te zijn van de standaard procedures om ongelukken te voorkomen. Gebruik de nummers aan het eind van elke waarschuwing om deze te herleiden naar de desbetreffende locatie.

BEWAAR DEZE INSTRUCTIES

Installation Instructions



Warning! Read the installation instructions before connecting the system to the power source.

設置手順書

システムを電源に接続する前に、設置手順書をお読み下さい。

警告

将此系统连接电源前,请先阅读安装说明。

警告

將系統與電源連接前，請先閱讀安裝說明。

Warnung

Vor dem Anschließen des Systems an die Stromquelle die Installationsanweisungen lesen.

¡Advertencia!

Lea las instrucciones de instalación antes de conectar el sistema a la red de alimentación.

Attention

Avant de brancher le système sur la source d'alimentation, consulter les directives d'installation.

יש לקרוא את הוראות התקינה לפני חיבור המערכת למקור מתח.

اقر إرشادات التركيب قبل توصيل النظام إلى مصدر للطاقة

시스템을 전원에 연결하기 전에 설치 안내를 읽어주십시오.

Waarschuwing

Raadpleeg de installatie-instructies voordat u het systeem op de voedingsbron aansluit.

Circuit Breaker



Warning! This product relies on the building's installation for short-circuit (overcurrent) protection. Ensure that the protective device is rated not greater than: 250 V, 20 A.

サーキット・ブレーカー

この製品は、短絡(過電流)保護装置がある建物での設置を前提としています。

保護装置の定格が250 V、20 Aを超えないことを確認下さい。

警告

此产品的短路(过载电流)保护由建筑物的供电系统提供,确保短路保护设备的额定电流不大于250V,20A。

警告

此產品的短路(過載電流)保護由建築物的供電系統提供,確保短路保護設備的額定電流不大於250V,20A。

Warnung

Dieses Produkt ist darauf angewiesen, dass im Gebäude ein Kurzschluss- bzw. Überstromschutz installiert ist. Stellen Sie sicher, dass der Nennwert der Schutzvorrichtung nicht mehr als: 250 V, 20 A beträgt.

¡Advertencia!

Este equipo utiliza el sistema de protección contra cortocircuitos (o sobrecorrientes) del edificio. Asegúrese de que el dispositivo de protección no sea superior a: 250 V, 20 A.

Attention

Pour ce qui est de la protection contre les courts-circuits (surtension), ce produit dépend de l'installation électrique du local. Vérifiez que le courant nominal du dispositif de protection n'est pas supérieur à :250 V, 20 A.

מווצר זה מסתמך על הגנה המותקנת במבנים **למניעת קצר חשמל**. יש לוודא כי המכשיר המגן מפני הקצר החשמלי הוא לא יותר מ- 20A, 250VDC.

هذا المنتج يعتمد على معدات الحماية من الدوائر القصيرة التي تم تثبيتها في المبني
تأكد من أن تقييم الجهاز الوقائي ليس أكثر من : 20A, 250V

경고!

이 제품은 전원의 단락(과전류)방지에 대해서 전적으로 건물의 관련 설비에 의존합니다.
보호장치의 정격이 반드시 250V(볼트), 20A(암페어)를 초과하지 않도록 해야 합니다.

Waarschuwing

Dit product is afhankelijk van de kortsluitbeveiliging (overspanning) van uw electrische installatie. Controleer of het beveiligde apparaat niet groter gedimensioneerd is dan 250V, 20A.

Power Disconnection Warning



Warning! The system must be disconnected from all sources of power and the power cord removed from the power supply module(s) before accessing the chassis interior to install or remove system components.

電源切断の警告

システムコンポーネントの取り付けまたは取り外しのために、シャーシー内部にアクセスするには、システムの電源はすべてのソースから切断され、電源コードは電源モジュールから取り外す必要があります。

警告

在你打开机箱并安装或移除内部器件前,必须将系统完全断电,并移除电源线。

警告

在您打開機殼安裝或移除內部元件前，必須將系統完全斷電，並移除電源線。

Warnung

Das System muss von allen Quellen der Energie und vom Netzanschlusskabel getrennt sein, das von den Spg. Versorgungsteilmodulen entfernt wird, bevor es auf den Chassisinnenraum zurückgreift, um Systemsbestandteile anzubringen oder zu entfernen.

¡Advertencia!

El sistema debe ser disconnected de todas las fuentes de energía y del cable eléctrico quitado de los módulos de fuente de alimentación antes de tener acceso el interior del chasis para instalar o para quitar componentes de sistema.

Attention

Le système doit être débranché de toutes les sources de puissance ainsi que de son cordon d'alimentation secteur avant d'accéder à l'intérieur du châssis pour installer ou enlever des composants de système.

ازהרה מפני ניתוק חשמלי,

ازהרה!

יש לנתק את המערכת מכל מקורות החשמל ויש להסיר את כבל החשמלי מהספק. לפני גישה לחלק הפנימי של המארז לצורך התקנת או הסרת רכיבים.

يجب فصل انظاوا من جميع مصادر انطاقت وإزانت سهك انكهرباء من وحدة امداد
انطاقت قبم
انصل إني انمناطق انداخهيت نههيكم نتشبيج أو إزانت مكناث الجهاز

경고!

시스템에 부품들을 장착하거나 제거하기 위해서는 새시 내부에 접근하기 전에 반드시 전원 공급장치로부터 연결되어있는 모든 전원과 전기코드를 분리해주어야 합니다.

Waarschuwing

Voordat u toegang neemt tot het binnenwerk van de behuizing voor het installeren of verwijderen van systeem onderdelen, dient u alle spanningsbronnen en alle stroomkabels aangesloten op de voeding(en) van de behuizing te verwijderen

Equipment Installation



Warning! Only trained and qualified personnel should be allowed to install, replace, or service this equipment.

機器の設置

トレーニングを受け認定された人だけがこの装置の設置、交換、またはサービスを許可されています。

警告

只有经过培训且具有资格的人员才能进行此设备的安装、更换和维修。

警告

只有經過受訓且具資格人員才可安裝、更換與維修此設備。

Warnung

Das Installieren, Ersetzen oder Bedienen dieser Ausrüstung sollte nur geschultem, qualifiziertem Personal gestattet werden.

¡Advertencia!

Solamente el personal calificado debe instalar, reemplazar o utilizar este equipo.

Attention

Il est vivement recommandé de confier l'installation, le remplacement et la maintenance de ces équipements à des personnels qualifiés et expérimentés.

אזהרה!

צוות מוסמך בלבד רשאי להתקין, להחליף את הציוד או לחת שירות עבור הציוד.

والمدربين لتزكيب واستبدال أو خدمة هذا الجهاز يجب أن يسمح فقط للموظفه المؤهلية

경고!

훈련을 받고 공인된 기술자만이 이 장비의 설치, 교체 또는 서비스를 수행할 수 있습니다.

Waarschuwing

Deze apparatuur mag alleen worden geïnstalleerd, vervangen of hersteld door geschoold en gekwalificeerd personeel.

Restricted Area

Warning! This unit is intended for installation in restricted access areas. A restricted access area can be accessed only through the use of a special tool, lock and key, or other means of security. (This warning does not apply to workstations).

アクセス制限区域

このユニットは、アクセス制限区域に設置されることを想定しています。

アクセス制限区域は、特別なツール、鍵と錠前、その他のセキュリティの手段を用いてのみ出入りが可能です。

警告

此部件应安装在限制进出的场所，限制进出的场所指只能通过使用特殊工具、锁和钥匙或其它安全手段进出的场所。

警告

此裝置僅限安裝於進出管制區域，進出管制區域係指僅能以特殊工具、鎖頭及鑰匙或其他安全方式才能進入的區域。

Warnung

Diese Einheit ist zur Installation in Bereichen mit beschränktem Zutritt vorgesehen. Der Zutritt zu derartigen Bereichen ist nur mit einem Spezialwerkzeug, Schloss und Schlüssel oder einer sonstigen Sicherheitsvorkehrung möglich.

¡Advertencia!

Esta unidad ha sido diseñada para instalación en áreas de acceso restringido. Sólo puede obtenerse acceso a una de estas áreas mediante la utilización de una herramienta especial, cerradura con llave u otro medio de seguridad.

Attention

Cet appareil doit être installée dans des zones d'accès réservés. L'accès à une zone d'accès réservé n'est possible qu'en utilisant un outil spécial, un mécanisme de verrouillage et une clé, ou tout autre moyen de sécurité.

אזור עם גישה מוגבלת
ゾーハה!

יש להתקן את היחידה באזוריים שיש בהם הגבלת גישה. הגישה ניתנת בעזרת
כלי אבטחה בלבד (מפתח, מנעול ועוד).

تحصيص هذه انحذة ترك بها ف مناطق محظورة تم .
يمكن انتصيل إن منطق ممحظورة فقط من خلال استخدام أداة خاصة
أو أوس هُت أخرى نلاًاما قم و مفتاح

경고!

이 장치는 접근이 제한된 구역에 설치하도록 되어있습니다. 특수도구, 잠금 장치 및 키,
또는 기타 보안 수단을 통해서만 접근 제한 구역에 들어갈 수 있습니다.

Waarschuwing

Dit apparaat is bedoeld voor installatie in gebieden met een beperkte toegang. Toegang tot dergelijke gebieden kunnen alleen verkregen worden door gebruik te maken van speciaal gereedschap, slot en sleutel of andere veiligheidsmaatregelen.

Battery Handling



Warning! There is the danger of explosion if the battery is replaced incorrectly. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions

電池の取り扱い

電池交換が正しく行われなかった場合、破裂の危険性があります。交換する電池はメーカーが推奨する型、または同等のものを使用下さい。使用済電池は製造元の指示に従って処分して下さい。

警告

电池更换不当会有爆炸危险。请只使用同类电池或制造商推荐的功能相当的电池更换原有电池。请按制造商的说明处理废旧电池。

警告

電池更換不當會有爆炸危險。請使用製造商建議之相同或功能相當的電池更換原有電池。請按照製造商的說明指示處理廢棄舊電池。

Warnung

Bei Einsetzen einer falschen Batterie besteht Explosionsgefahr. Ersetzen Sie die Batterie nur durch den gleichen oder vom Hersteller empfohlenen Batterietyp. Entsorgen Sie die benutzten Batterien nach den Anweisungen des Herstellers.

Attention

Danger d'explosion si la pile n'est pas remplacée correctement. Ne la remplacer que par une pile de type semblable ou équivalent, recommandée par le fabricant. Jeter les piles usagées conformément aux instructions du fabricant.

¡Advertencia!

Existe peligro de explosión si la batería se reemplaza de manera incorrecta. Reemplazar la batería exclusivamente con el mismo tipo o el equivalente recomendado por el fabricante. Desechar las baterías gastadas según las instrucciones del fabricante.

אזהרה!

קיימת סכנת פיצוץ של הסוללה במידה והוחלפה בדרך לא תקינה. יש להחליף את הסוללה בסוג התואם מחברת יצור מומלצת.
סילוק הסוללות המשומשות יש לבצע לפי הוראות היצרן.

هناك خطر من انفجار في حالة اسحذال البطارية بطريقة غير صحيحة فعليل
اسحذال البطارية

فقط بنفس النوع أو ما يعادلها مما أوصى به الشرمة المصنعة
جخلص من البطاريات المسحعملة وفقا لتعليمات الشرمة الصانعة

경고!

배터리가 올바르게 교체되지 않으면 폭발의 위험이 있습니다. 기존 배터리와 동일하거나 제조사에서 권장하는 동등한 종류의 배터리로만 교체해야 합니다. 제조사의 안내에 따라 사용된 배터리를 처리하여 주십시오.

Waarschuwing

Er is ontploffingsgevaar indien de batterij verkeerd vervangen wordt. Vervang de batterij slechts met hetzelfde of een equivalent type die door de fabrikant aanbevolen wordt. Gebruikte batterijen dienen overeenkomstig fabrieksvoorschriften afgevoerd te worden.

Redundant Power Supplies



Warning! This unit might have more than one power supply connection. All connections must be removed to de-energize the unit.

冗長電源装置

このユニットは複数の電源装置が接続されている場合があります。

ユニットの電源を切るためには、すべての接続を取り外さなければなりません。

警告

此部件连接的电源可能不止一个，必须将所有电源断开才能停止给该部件供电。

警告

此裝置連接的電源可能不只一個，必須切斷所有電源才能停止對該裝置的供電。

Warnung

Dieses Gerät kann mehr als eine Stromzufuhr haben. Um sicherzustellen, dass der Einheit kein Strom zugeführt wird, müssen alle Verbindungen entfernt werden.

¡Advertencia!

Puede que esta unidad tenga más de una conexión para fuentes de alimentación. Para cortar por completo el suministro de energía, deben desconectarse todas las conexiones.

Attention

Cette unité peut avoir plus d'une connexion d'alimentation. Pour supprimer toute tension et tout courant électrique de l'unité, toutes les connexions d'alimentation doivent être débranchées.

אם קיים יותר מספק אחד
אוורה!

ליחדה יש יותר מחיבור אחד של ספק. יש להסיר את כל החיבורים על מנת לרוקן
את היחידה.

قد يكون لهذا الجهاز عدة اتصالات بوحدات امداد الطاقة .
يجب إزالة كافة الاتصالات لعزل الوحدة عن الكهرباء

경고!

이 장치에는 한 개 이상의 전원 공급 단자가 연결되어 있을 수 있습니다. 이 장치에 전원을 차단하기 위해서는 모든 연결 단자를 제거해야만 합니다.

Waarschuwing

Deze eenheid kan meer dan één stroomtoevoeraansluiting bevatten. Alle aansluitingen dienen verwijderd te worden om het apparaat stroomloos te maken.

Backplane Voltage



Warning! Hazardous voltage or energy is present on the backplane when the system is operating. Use caution when servicing.

バックプレーンの電圧

システムの稼働中は危険な電圧または電力が、バックプレーン上にかかりています。

修理する際には注意ください。

警告

当系统正在进行时，背板上有很危险的电压或能量，进行维修时务必小心。

警告

當系統正在進行時，背板上有危險的電壓或能量，進行維修時務必小心。

Warnung

Wenn das System in Betrieb ist, treten auf der Rückwandplatine gefährliche Spannungen oder Energien auf. Vorsicht bei der Wartung.

¡Advertencia!

Cuando el sistema está en funcionamiento, el voltaje del plano trasero es peligroso. Tenga cuidado cuando lo revise.

Attention

Lorsque le système est en fonctionnement, des tensions électriques circulent sur le fond de panier. Prendre des précautions lors de la maintenance.

מתה בפנל האחורי

אוּהָרָה!

קיימת סכנת מתה בפנל האחורי בזמן תפעול המערכת. יש להיזהר במהלך העבודה.

هناك خطر مه التيار الكهربائي أو الطاقة المبذدة على اللحمة
عندما يكن النظام يعمل كه حذرا عند خدمة هذا الجهاز

경고!

시스템이 동작 중일 때 후면판 (Backplane)에는 위험한 전압이나 에너지가 발생 합니다.
서비스 작업 시 주의하십시오.

Waarschuwing

Een gevaarlijke spanning of energie is aanwezig op de backplane wanneer het systeem in gebruik is. Voorzichtigheid is geboden tijdens het onderhoud.

Comply with Local and National Electrical Codes



Warning! Installation of the equipment must comply with local and national electrical codes.

地方および国の電気規格に準拠

機器の取り付けはその地方および国の電気規格に準拠する必要があります。

警告

设备安装必须符合本地与本国电气法规。

警告

設備安裝必須符合本地與本國電氣法規。

Warnung

Die Installation der Geräte muss den Sicherheitsstandards entsprechen.

¡Advertencia!

La instalacion del equipo debe cumplir con las normas de electricidad locales y nacionales.

Attention

L'équipement doit être installé conformément aux normes électriques nationales et locales.

תיאום חוקי החשמל הארצי

אוורה!

התקנת הציוד חייבת להיות תואמת לחוקי החשמל המקומיים והלאומיים.

تركيب المعدات الكهربائية يجب أن يمتثل للقوانين المحلية والدولية المتعلقة بالكهرباء.

경고!

현 지역 및 국가의 전기 규정에 따라 장비를 설치해야 합니다.

Waarschuwing

Bij installatie van de apparatuur moet worden voldaan aan de lokale en nationale elektriciteitsvoorschriften.

Product Disposal



Warning! Ultimate disposal of this product should be handled according to all national laws and regulations.

製品の廃棄

この製品を廃棄処分する場合、国の関係する全ての法律・条例に従い処理する必要があります。

警告

本产品的废弃处理应根据所有国家的法律和规章进行。

警告

本產品的廢棄處理應根據所有國家的法律和規章進行。

Warnung

Die Entsorgung dieses Produkts sollte gemäß allen Bestimmungen und Gesetzen des Landes erfolgen.

¡Advertencia!

Al deshacerse por completo de este producto debe seguir todas las leyes y reglamentos nacionales.

Attention

La mise au rebut ou le recyclage de ce produit sont généralement soumis à des lois et/ou directives de respect de l'environnement. Renseignez-vous auprès de l'organisme compétent.

סילוק המוצר

ازהרה!

סילוק סופי של מוצר זה חייב להיות בהתאם להנחיות וחוקי המדינה.

التخلص النهائي من هذا المنتج ينبغي التعامل معه وفقاً لجميع القوانين واللائحة البيئية عند

경고!

이 제품은 해당 국가의 관련 법규 및 규정에 따라 폐기되어야 합니다.

Waarschuwing

De uiteindelijke verwijdering van dit product dient te geschieden in overeenstemming met alle nationale wetten en reglementen.

Hot Swap Fan Warning



Warning! Hazardous moving parts. Keep away from moving fan blades. The fans might still be turning when you remove the fan assembly from the chassis. Keep fingers, screwdrivers, and other objects away from the openings in the fan assembly's housing.

ファン・ホットスワップの警告

警告!回転部品に注意。運転中は回転部(羽根)に触れないでください。シャーシから冷却ファン装置を取り外した際、ファンがまだ回転している可能性があります。ファンの開口部に、指、ドライバー、およびその他のものを近づけないで下さい。

警告!

警告! 危险的可移动性零件。请务必与转动的风扇叶片保持距离。当您从机架移除风扇装置，风扇可能仍在转动。小心不要将手指、螺丝起子和其他物品太靠近风扇

警告

危險的可移動性零件。請務必與轉動的風扇葉片保持距離。當您從機架移除風扇裝置，風扇可能仍在轉動。小心不要將手指、螺絲起子和其他物品太靠近風扇。

Warnung

Gefährlich Bewegende Teile. Von den bewegenden Lüfterblätter fern halten. Die Lüfter drehen sich u. U. noch, wenn die Lüfterbaugruppe aus dem Chassis genommen wird. Halten Sie Finger, Schraubendreher und andere Gegenstände von den Öffnungen des Lüftergehäuses entfernt.

¡Advertencia!

Riesgo de piezas móviles. Mantener alejado de las aspas del ventilador. Los ventiladores podran dar vuelta cuando usted quite el montaje del ventilador del chasis. Mantenga los dedos, los destornilladores y todos los objetos lejos de las aberturas del ventilador

Attention

Pieces mobiles dangereuses. Se tenir a l'écart des lames du ventilateur Il est possible que les ventilateurs soient toujours en rotation lorsque vous retirerez le bloc ventilateur du châssis. Prenez garde à ce que doigts, tournevis et autres objets soient éloignés du logement du bloc ventilateur.

ازهارה!

חלקים נייחים מסוכנים. התרחק מלהבי המאוחר בפעולת הCACSR מסירם את חלקו המאוחר מהמארז, יתכן והמאורותים עדיין עובדים. יש להרחק למרחק בטוח את האצבעות וכל עבודה שונות מהפתחים בתוך המאוחר

تحذير! أجزاء متحركة خطيرة. ابتعد عن شفرات المروحة المتحركة. من الممكن أن المروحة لا تزال تدور عند إزالة كتلة المروحة من الهيكل يجب إبقاء الأصابع ومفكات البراغي وغيرها من الأشياء بعيداً عن الفتحات في كتلة المروحة.

경고!

움직이는 위험한 부품. 회전하는 송풍 날개에 접근하지 마세요. 새시로부터 팬 조립품을 제거할 때 팬은 여전히 회전하고 있을 수 있습니다. 팬 조립품 외관의 열려있는 부분들로부터 손가락 및 스크류드라이버, 다른 물체들이 가까이 하지 않도록 배치해 주십시오.

Waarschuwing

Gevaarlijk bewegende onderdelen. Houd voldoende afstand tot de bewegende ventilatorbladen. Het is mogelijk dat de ventilator nog draait tijdens het verwijderen van het ventilatorsamenstel uit het chassis. Houd uw vingers, schroevendraaiers en eventuele andere voorwerpen uit de buurt van de openingen in de ventilatorbehuizing.

Power Cable and AC Adapter



Warning! When installing the product, use the provided or designated connection cables, power cables and AC adaptors. Using any other cables and adaptors could cause a malfunction or a fire. Electrical Appliance and Material Safety Law prohibits the use of UL or CSA -certified cables (that have UL/CSA shown on the code) for any other electrical devices than products designated by Supermicro only.

電源コードとACアダプター

製品を設置する場合、提供または指定および購入された接続ケーブル、電源コードとACアダプターを該当する地域の条例や安全基準に適合するコードサイズやプラグと共に使用下さい。他のケーブルやアダプタを使用すると故障や火災の原因になることがあります。

電気用品安全法は、ULまたはCSA認定のケーブル(UL/CSEマークがコードに表記)を Supermicro が指定する製品以外に使用することを禁止しています。

警告

安装此产品时,请使用本身提供的或指定的或采购的连接线,电源线和电源适配器, 包含遵照当地法规和安全要求的合规的电源线尺寸和插头. 使用其它线材或适配器可能会引起故障或火灾。

除了Supermicro所指定的产品,电气用品和材料安全法律规定禁止

使用未经UL或CSA认证的线材。 (线材上会显示UL/CSA符号)。

警告

安裝此產品時,請使用本身提供的或指定的或採購的連接線,電源線和電源適配器, 包含遵照當地法規和安全要求的合規的電源線尺寸和插頭. 使用其它線材或適配器可能會引起故障或火災。

除了Supermicro所指定的產品,電氣用品和材料安全法律規定禁止

使用未經UL或CSA認證的線材。 (線材上會顯示UL/CSA符號)。

Warnung

Nutzen Sie beim Installieren des Produkts ausschließlich die von uns zur Verfügung gestellten Verbindungskabeln, Stromkabeln und/oder Adapter, die Ihre örtlichen Sicherheitsstandards einhalten. Der Gebrauch von anderen Kabeln und Adaptern können Fehlfunktionen oder Feuer verursachen. Die Richtlinien untersagen das Nutzen von UL oder CAS zertifizierten Kabeln (mit UL/CSA gekennzeichnet), an Geräten oder Produkten die nicht mit Supermicro gekennzeichnet sind.

¡Advertencia!

Cuando instale el producto, utilice la conexión provista o designada o procure cables, Cables de alimentación y adaptadores de CA que cumplan con los códigos locales y los requisitos de seguridad, incluyendo el tamaño adecuado del cable y el enchufe. El uso de otros cables y adaptadores podría causar un mal funcionamiento o un incendio. La Ley de Seguridad de Aparatos Eléctricos y de Materiales prohíbe El uso de cables certificados por UL o CSA (que tienen el certificado UL / CSA en el código) para cualquier otros dispositivos eléctricos que los productos designados únicamente por Supermicro.

Attention

Lors de l'installation du produit, utilisez les cables de connection fournis ou désigné ou achetez des cables, cables de puissance et adaptateurs respectant les normes locales et les conditions de sécurité y compris les tailles de cables et les prises électriques appropriées. L'utilisation d'autres cables et adaptateurs peut provoquer un dysfonctionnement ou un incendie. Appareils électroménagers et la Loi sur la Sécurité Matériel interdit l'utilisation de câbles certifiés- UL ou CSA (qui ont UL ou CSA indiqué sur le code) pour tous les autres appareils électriques sauf les produits désignés par Supermicro seulement.

AC כבלים חשמליים ומותאמים

אזהרה!

אשר נרכשו או הותאמו לצורך ההתקנה, ואשר הותאמו לדרישות AC כאשר מתקנים את המזוזה, יש להשתמש בכבלים, ספקים ומוגדים הבטיחות והמקומיות, כולל מידת נכונה של הcabל והתקע. שימוש בכל cabל או מוגד מסווג אורה, עלול לגרום לתקלה או קוצר חשמלי. בהתאם לדרישות UL או CSA - להזקי השימוש במכשירי החשמל וחוקי הבטיחות, קיים איסור להשתמש בכבלים המומסכים בלבד Supermicro עבור כל מוגדר חשמלי אורה, אלא רק במוגדר אשר הותאם ע"י (UL/CSA) בלבד.

عند تركيب المنتج، قم باستخدام التوصيات المتوفرة أو المحددة أو قم بشراء الكابلات الكهربائية ومحولات التيار المتردد مع الالتزام بقوانين ومتطلبات السلامة المحلية بما في ذلك حجم الموصى والقبس السليم. استخدام أي كابلات ومحولات أخرى قد يتسبب في عطل أو حريق. يحظر قانون السلامة للأجهزة الكهربائية والمعدات استخدام الكابلات المعتمدة مع أي معدات أخرى غير المنتجات المعنية والمحددة من قبل (UL/CSA) والتي تحمل علامة CSA أو UL من قبل Supermicro.

전원 케이블 및 AC 어댑터

경고! 제품을 설치할 때 현지 코드 및 적절한 굽기의 코드와 플러그를 포함한 안전 요구 사항을 준수하여 제공되거나 지정된 연결 혹은 구매 케이블, 전원 케이블 및 AC 어댑터를 사용하십시오.

다른 케이블이나 어댑터를 사용하면 오작동이나 화재가 발생할 수 있습니다. 전기 용품 안전법은 UL 또는 CSA 인증 케이블 (코드에 UL / CSA가 표시된 케이블)을 Supermicro 가 지정한 제품 이외의 전기 장치에 사용하는 것을 금지합니다.

Stroomkabel en AC-Adapter

Waarschuwing! Bij het aansluiten van het Product uitsluitend gebruik maken van de geleverde Kabels of een andere geschikte aan te schaffen Aansluitmethode, deze moet altijd voldoen aan de lokale voorschriften en veiligheidsnormen, inclusief de juiste kabeldikte en stekker. Het gebruik van niet geschikte Kabels en/of Adapters kan een storing of brand veroorzaken. Wetgeving voor Elektrische apparatuur en Materiaalveiligheid verbied het gebruik van UL of CSA -gecertificeerde Kabels (met UL/CSA in de code) voor elke andere toepassing dan de door Supermicro hiervoor beoogde Producten.

Appendix C

System Specifications

Processors

Dual Intel® 81xx/61xx/51xx/41xx/31xx or 82xx/62xx/52xx/42xx/32xx series processors in an Socket P0-LGA3647 type socket

Note: Please refer to the motherboard specifications pages on our website for updates to supported processors.

Chipset

Intel PCH C622 chipset

BIOS

256 Mb SPI AMI BIOS® SM Flash UEFI BIOS Flash ROM

Memory

Up to 6 TB of 3DS Load Reduced DIMM (3DS LRDIMM)/Load Reduced DIMM (LRDIMM)/3DS Registered DIMM (3DS RDIMM)/Registered DIMM (RDIMM) DDR4 (288-pin) ECC 2933/2666/2400/2133 MHz speed and up to 128 GB size at 1.2V in 24 slots.

SATA Controller

On-chip (Intel PCH C622) controller

Drive Bays

Up to 24 2.5" drives (8 are connected and support 8x SATA HDD by default)

PCI Expansion Slots

Up to 11 PCI-E slots for -TRT and 12 PCI-E slots for -TRT2/TRT3

Motherboard

X11DPG-O; 17" (W) x 19.5" (L) (431.8-mm x 495.3-mm)

Chassis

SC418GTS-R4000BP2; 4U Rackmount, (WxHxD) 17.2 x 1.7 x 23.5 in. (437 x 43 x 597 mm)

System Cooling

Up to eight 9-cm counter-rotating PWM fans

Power Supply

Model: PWS-2K05A-1R

AC Input Voltages: 100-240 VAC auto-range

Rated Input Current: 1000W: 100-127V/12.9A, 1600W: 200-240V/9.5A

Rated Input Frequency: 50-60 Hz

Rated Output Power: 1000/1800/1890/2000W

Rated Output Voltages: 12V+ 83.3/150/165/167A

Operating Environment

Operating Temperature: 10° to 35° C (50° to 95° F)

Non-operating Temperature: -40° to 70° C (-40° to 158° F)

Operating Relative Humidity: 8% to 90% (non-condensing)

Non-operating Relative Humidity: 5 to 95% (non-condensing)

Note: The system power supplies are redundant, hot-plug.

Regulatory Compliance

Electromagnetic Emissions: FCC Class A, EN 55032 Class A, EN 61000-3-2/3-3, CISPR 32 Class A

Electromagnetic Immunity: EN 55024/CISPR 24, (EN 61000-4-2, EN 61000-4-3, EN 61000-4-4, EN 61000-4-5, EN 61000-4-6, EN 61000-4-8, EN 61000-4-11), CNS14336-1, CNS13438, GB4943.1-2011, GB9254-2008(Class A) and GB17625.1-2012

Safety: CSA/EN/IEC/UL 60950-1 Compliant, UL or CSA Listed (USA and Canada), CE Marking (Europe)

Other: VCCI-CISPR 32 and AS/NZS CISPR 32

Environmental: Directive 2011/65/EU and Delegated Directive (EU) 2015/863 and Directive 2012/19/EU

Perchlorate Warning

California Best Management Practices Regulations for Perchlorate Materials: This Perchlorate warning applies only to products containing CR (Manganese Dioxide) Lithium coin cells. "Perchlorate Material-special handling may apply. See www.dtsc.ca.gov/hazardouswaste/perchlorate"

Appendix D

UEFI BIOS Recovery

Warning: Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall Supermicro be liable for direct, indirect, special, incidental, or consequential damages arising from a BIOS update. If you need to update the BIOS, do not shut down or reset the system while the BIOS is updating to avoid possible boot failure.

D.1 Overview

The Unified Extensible Firmware Interface (UEFI) provides a software-based interface between the operating system and the platform firmware in the pre-boot environment. The UEFI specification supports an architecture-independent mechanism that will allow the UEFI OS loader stored in an add-on card to boot the system. The UEFI offers clean, hands-off management to a computer during system boot.

D.2 Recovering the UEFI BIOS Image

A UEFI BIOS flash chip consists of a recovery BIOS block and a main BIOS block (a main BIOS image). The boot block contains critical BIOS codes, including memory detection and recovery codes for the user to flash a new BIOS image if the original main BIOS image is corrupted. When the system power is first turned on, the boot block codes execute first. Once this process is completed, the main BIOS code will continue with system initialization and the remaining POST (Power-On Self-Test) routines.

Note 1: Follow the BIOS recovery instructions below for BIOS recovery when the main BIOS boot crashes.

Note 2: When the BIOS boot block crashes, you will need to follow the procedures to make a Returned Merchandise Authorization (RMA) request. (For a RMA request, please see section 3.5 for more information). Also, you may use the Supermicro Update Manager (SUM) Out-of-Band (OOB) (https://www.supermicro.com.tw/products/nfo/SMS_SUM.cfm) to refresh the BIOS.

D.3 Recovering the BIOS Block with a USB Device

This feature allows the user to recover a BIOS image using a USB-attached device without additional utilities used. A USB flash device such as a USB Flash Drive, or a USB CD/DVD ROM/RW device can be used for this purpose. However, a USB Hard Disk drive cannot be used for BIOS recovery at this time.

The file system supported by UEFI is FAT (including FAT12, FAT16, and FAT32) which is installed on a bootable or non-bootable USB-attached device. However, the BIOS might need several minutes to locate the SUPER.ROM file if the media size becomes too large due to the huge volumes of folders and files stored in the device.

To perform UEFI BIOS recovery using a USB-attached device, follow the instructions below.

1. Using a different machine, copy the "Super.ROM" binary image file into the disc Root "\\" directory of a USB device or a writable CD/DVD.

Note: If you cannot locate the "Super.ROM" file in your driver disk, visit our website at www.supermicro.com to download the BIOS image into a USB flash device and rename it "Super.ROM" for BIOS recovery use.

2. Insert the USB device that contains the new BIOS image ("Super.ROM") into your USB drive and power on the system
3. While powering on the system, please keep pressing <Ctrl> and <Home> simultaneously on your keyboard *until the following screen (or a screen similar to the one below) displays.*



Note: On the other hand, if the following screen displays, please load the "Super.ROM" file to the root folder and connect this folder to the system. (You can do so by inserting a USB device that contains the new "Super.ROM" image to your machine for BIOS recovery.)



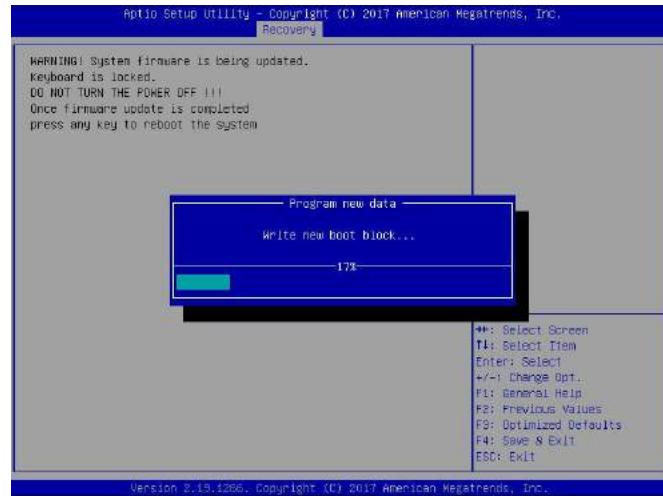
Warning: Please **stop** pressing the <Ctrl> and <Home> keys immediately when you see the



screen (or a similar screen) below; otherwise, it will trigger a system reboot.

4. After locating the new BIOS binary image, the system will enter the BIOS Recovery menu as shown below.

Note: At this point, you may decide if you want to start the BIOS recovery. If you decide to proceed with BIOS recovery, follow the procedures below.

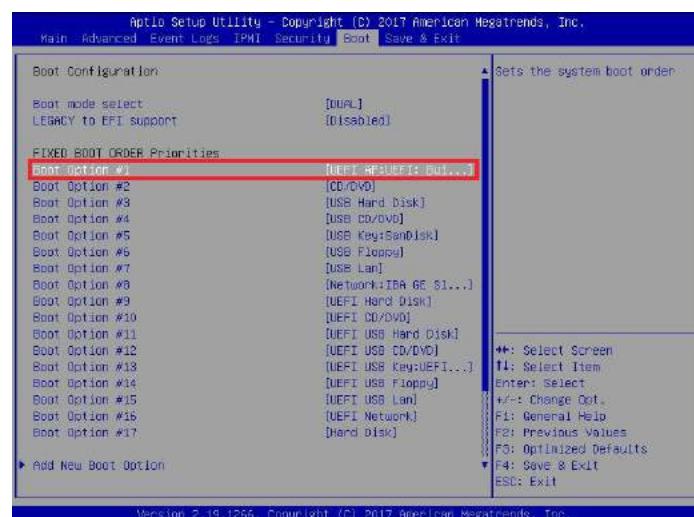


5. When the screen as shown above displays, use the arrow keys to select the item



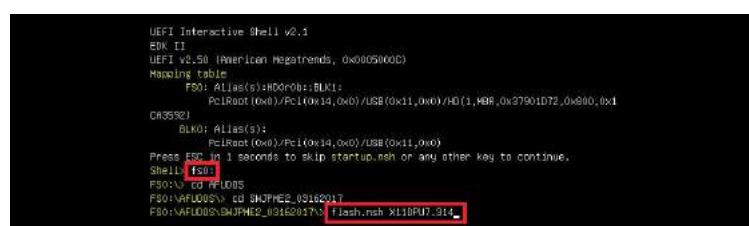
"Proceed with flash update" and press the <Enter> key. You will see the BIOS recovery progress as shown in the screen below.

Note: *Do not interrupt the BIOS flashing process until it has completed.*

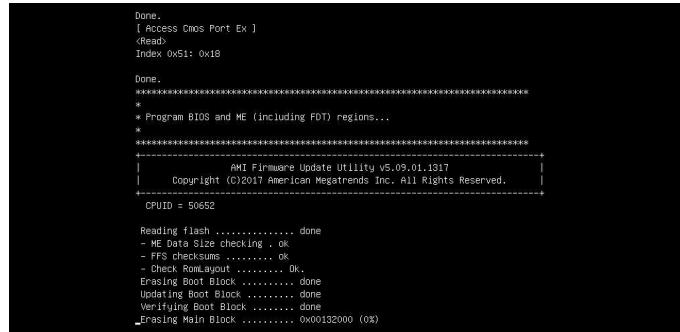


6. After the BIOS recovery process is completed, press any key to reboot the system.

7. Using a different system, extract the BIOS package into a USB flash drive.



8. Press **** continuously during system boot to enter the BIOS setup utility. From the



```

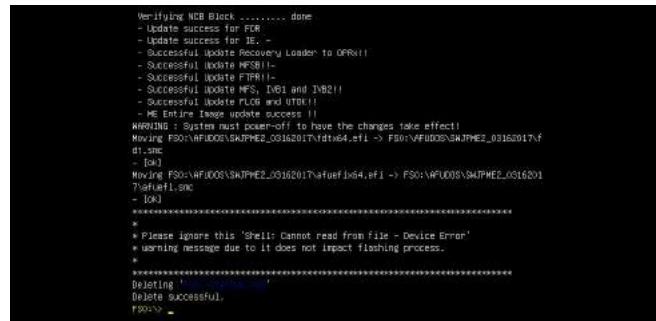
Done.
[ Access Cmos Port Ex ]
<Read>
Index 0x51: 0x18

Done.
*****
* Program BIOS and ME (including FDT) regions...
*
*****
| AMI Firmware Update Utility v5.09.01.1317
| Copyright (C)2017 American Megatrends Inc. All Rights Reserved.
+-----+
CPUID = 50652

Reading flash ..... done
- ME1 L1 Cache checking . ok
- FDT checksum ..... ok
- Check Romlayout ..... ok
Erasing Boot Block ..... done
Updating Boot Block ..... done
Verifying Boot Block ..... done
Erasing Main Block ..... 0x001032000 (0x)

```

top of the tool bar, click on Boot and press **<Enter>** to enter the submenu. From the submenu list, select Boot Option #1 as shown below. Then, boot Option #1 to [UEFI



```

Verifying NCB Block ..... done
- Update success for FCR
- Update success for ME1
- Successful update Recovery Loader to OFF!!! 
Successful update ME1!!!
Successful update FTR!!! 
Successful update ME1, IVB1 and IVB2!!!
Successful update FLOG and UTD!!!
- ME Entire Image update success !!
WARNING : System must power-off to have the changes take effect!
Moving FSO:\VFL0008\SMI\PM2_09162017\fdtx64.efl -> FSO:\VFL0008\SMI\PM2_09162017\f
[ok]
Moving FSO:\VFL0008\SMI\PM2_09162017\afuef164.efl -> FSO:\VFL0008\SMI\PM2_09162017\f
[ok]
[ok]
- [ok]
*****
* Please ignore this 'Shell: Cannot read from file - Device Error'
warning message due to it does not impact flashing process.
*
*****
Deleting 'SMI\PM2_09162017'
Delete successful.
F9052>

```

AP:UEFI: Built-in EFI Shell]. Press **<F4>** to save the settings and exit the BIOS setup utility.

9. When the UEFI Shell prompt appears, type **fs#** to change the device directory path. Go to the directory that contains the BIOS package you extracted earlier from Step 7. Enter **flash.nsh BIOSname.###** at the prompt to start the BIOS update process.

Note: Do not interrupt this process until the BIOS flashing is complete.

10. The screen above indicates that the BIOS update process is completed. When you see the screen above, unplug the AC power cable from the power supply, clear CMOS, and plug the AC power cable in the power supply again to power on the system.
11. Press **** continuously to enter the BIOS setup utility.
12. Press **<F3>** to load the default settings.
13. After loading the default settings, press **<F4>** to save the settings and exit the BIOS setup utility.

Appendix E

Traditional Chinese Version of Safety Warnings

限用物質含有情況標示聲明書

Declaration of the Presence Condition of the Restricted Substances Marking

設備名稱：伺服器/ Server Equipment name						
型號 (型式) : 418G-X11 Type designation (Type)						
系列型號 : 4029GP-TRT; 4029GP-TRT2						
限用物質及其化學符號 Restricted substances and its chemical symbols						
單元 Unit	鉛Lead (Pb)	汞Mercury (Hg)	鎘Cadmium (Cd)	六價鉻 Hexavalent chromium (Cr⁺⁶)	多溴聯苯 Polybrominated biphenyls (PBB)	多溴二苯囑 Polybrominated diphenyl ethers (PBDE)
機殼 (Chassis)						
機殼風扇 (Chassis Fan)	—					
線材 (Cable)						
主機板 (Motherboard)	—					
電源供應器 (Power Supply)	—					
硬碟 (Hard Disk)	—					
電源背板 (PDB)	—					
附加卡 (Add-on Card)	—					
備考1. <input checked="" type="checkbox"/> 『超出0.1 wt %』及 <input checked="" type="checkbox"/> 『超出0.01 wt %』係指限用物質之百分比含量超出百分比含量基準囑。						
Note 1 : "Exceeding 0.1 wt %" and "exceeding 0.01 wt %" indicate that the percentage content of the restricted substance exceeds the reference percentage value of presence condition.						
備考2. <input type="checkbox"/> 『—』係指該項限用物質之百分比含量未超出百分比含量基準囑。						
Note 2 : "—" indicates that the percentage content of the restricted substance does not exceed the percentage of reference value of presence.						
備考3. <input type="checkbox"/> 『—』係指該項限用物質為排除項目。						
Note 3 : The "—" indicates that the restricted substance corresponds to the exemption.						



警告使用者：

這是甲類的資訊產品，在居住的環境中使用時，可能會造成
射頻干擾，在這種情況下，使用者會被要求採取某些適當的對策

- * 輸入額定: 100-140V~, 60-50Hz, 12-9.8A
180-240V~, 60-50Hz, 10-9.8A

- * 使用者不能任意拆除或替換內部配備

- * 報驗義務人之姓名或名稱:
美超微電腦股份有限公司

- * 報驗義務人之地址:
新北市中和區建一路 150 號 3 樓